



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

I M.TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R14), JULY – 2018

TIME TABLE

TIME → AN: 02:00 PM TO 05:00 PM

Date : 02-07-2018

BRANCH	DATE AND DAY					
	18-07-2018 WEDNESDAY	20-07-2018 FRIDAY	23-07-2018 MONDAY	25-07-2018 WEDNESDAY	27-07-2018 FRIDAY	30-07-2018 MONDAY
Structural Engineering (20 – SE)	Computer Oriented Numerical Methods	Theory of Elasticity and Plasticity	Theory and Analysis of Plates	Advanced Reinforced Concrete Design	Concrete Technology	Advanced Structural Analysis
VLSI System Design (57 – VLSI SD)	VLSI Technology and Design	Digital System Design	Micro Controllers for Embedded System Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	CPLD and FPGA Architectures and Applications
Embedded Systems (55 – ES)	VLSI Technology and Design	Digital System Design	Micro Controllers for Embedded System Design	----	----	----
Wireless & Mobile Communications (65 – WMC)	Image and Video Processing	Detection & Estimation Theory	----	----	----	----
Computer Science and Engineering (58 – CSE)	Advanced Operating Systems	Software Process and Project Management	----	----	----	----

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among concerned Students and Faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

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