



# ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

## EXAMINATION BRANCH

### I M.TECH II SEMESTER – REGULAR / SUPPLEMENTARY EXAMINATIONS (R15), JULY – 2018

#### TIME TABLE


TIME → FN: 10:00 AM TO 01:00 PM

Date : 02-07-2018

BRANCH	DATE AND DAY					
	18-07-2018 WEDNESDAY	20-07-2018 FRIDAY	23-07-2018 MONDAY	25-07-2018 WEDNESDAY	27-07-2018 FRIDAY	30-07-2018 MONDAY
<b>Structural Engineering (20 – SE)</b>	Finite Element Methods	Structural Dynamics	Earthquake Resistant Design of Buildings	Pre stressed concrete	Stability of Structures	----
<b>Machine Design (15 – MD)</b>	Mechanical Vibrations	Experimental Stress Analysis	Mechatronics	Advanced Optimization Techniques and Applications	----	----
<b>Power Electronics and Electrical Drives (54 – PEED)</b>	Power Electronic Converters – II	Neural Networks and Fuzzy Systems	Power Electronic Control of AC Drives	Power Quality	Flexible AC Transmission Systems	AI Techniques in Electrical Engineering
<b>VLSI System Design (57 – VLSI SD)</b>	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CAD for VLSI Circuits
						Coding Theory and Techniques

**NOTE:** The students should occupy their seats 10 min before Commencement of the Examination.

To  
HODs to circulate among the concerned students and faculty.  
Department Notice Board  
Examination Branch Notice Board  
Administrative Office

  
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