



# ANURAG Engineering College

(An Autonomous Institution)

(Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)

Ananthagiri (V & M), Kodad, Suryapet (Dt). Pin: 508 206.

## LIST OF VALUE ADDED COURSES OFFERED IN THE A.Y. 2015 – 16

S. No.	Course Code	Name of Value Added Course Offered
1	CE15VA02	Training on STAAD Pro (Analysis and Designing)
2	CE15VA01	Safety In Construction
3	EE15VA01	Introduction to PSPICE
4	EE15VA02	Introduction to MATLAB
5	ME15VA01	ANSYS
6	ME15VA02	CREO
7	EC15VA01	Introduction to Mentor graphics Design Tools
8	EC15VA02	Introduction to LAB View
9	CS15VA01	Python Programming
10	CS15VA02	PHP
11	MB15VA01	Soft Skills

  
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Principal

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Ananthagiri (V&M), Kodad,  
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Ananthagiri (V) Kodad (M) ,Nalgonda (Dt). Pin: 508 206.

## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### TRAINING ON STAAD PRO

STAAD Pro is a Structural Analysis and Design Computer Program. It supports several steel, concrete and timber design codes. It can also make use of various forms of dynamic analysis from modal extraction to time-history and response spectrum analysis.

#### Purpose the Course

To improve professional skills.

#### Eligibility

Anyone interested Pursuing graduate (C.E).

#### Prerequisites

Knowledge on DRCS, DSS and Structural Analysis.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

  
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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand the importance of STAAD in designing the structures and basic commands, model generation, application of loads, material properties. Students are analyse simple structures without errors and to analyse concrete and steel structural designing.

### Course Outline

Course Outline		
Module 1	Introduction Of STAAD PRO Layout & Tool Bars, STAAD Entities.	4 Hours
Module 2	Solving And Understanding 2D & 3D Structures.	4 Hours
Module 3	STAAD Coordinate System, Viewing Results and Debugging.	4 Hours
Module 4	Assigning Properties, Material, and Supports & Loads to a beam.	4 Hours
Module 5	Moving, Wind and Seismic Loads.	4 Hours
Module 6	Design Of Steel Structures and Classification Of Structures.	4 Hours
Module 7	Animations, Concrete Design and Specifications.	3 Hours
Module 8	Concrete Parameters and Creating Geometry Of Plane Frame Using Structure Wizard.	3 Hours


### COURSE OUTCOMES:

On successful completion of this course, it is expected that

**CO 1:** Basics and Introduction about STAAD PRO analysis.

**CO 2:** Students will be able to generate a model & can apply the loads & properties of the material for beams.

**CO 3:** Students will be able to analyse & design a steel structure.

  
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**CO 4:** Students will understand performance of steel Structures.

**CO 5:** Students will be able to analyse & design a plane frame.

### Reference Books

1. Design of R C C Buildings using Staad Pro V8i with Indian Examples English by Author T S Sarma.
2. Illustrated Design Of Reinforced Concrete Buildings by Dr. S.R.Karve & Dr. V.L.Shah Paperback
3. Exploring Bentley STAAD.Pro V8i (SELECTseries 6) by Sham Tickoo/TIET

Course Coordinator	Resource Person
D.VNV Laxmi Alekhya Asst. professor CE Department Anurag Engineering College	C.Manikanta Reddy Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT CIVIL ENGINEERING

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DATE: 29-6-2015

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2015-16.

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Name of the Value added course: Training on STAAD Pro (Analysis and Designing)

Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 01-07-2015.

Course coordinator:

D.VNV LAXMI ALEKHYA

Asst. Professor,

CE Dept.,

Mobile No: 8463944571

*C. M. Reddy*  
29/6/15  
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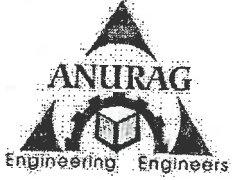
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## DEPARTMENT CIVIL ENGINEERING

DATE: 06-07-2015

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Training on STAAD Pro (Analysis and Designing)" is scheduled from 07 July 2015 to 17 Oct 2015.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

C. Manikanta Reddy

Asst. Professor,

CE Department,

Mobile: 9703111700

*C. Manikanta Reddy*  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### SAFETY IN CONSTRUCTION

Students are given training in regarding safety in construction local and international standards and laws associated with health and safety in construction activities, identification and controlling of workplace dangers and hazards and practical application of knowledge in real time situations. To join and study this course, no previous health and safety knowledge is required although it is advisable that candidates should have basic understanding of construction activities.

### Purpose the Course

To improve professional skills.

### Eligibility

III B TECH (CE) Students.

### Prerequisites

Knowledge on Building materials and construction.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	40	4.00-5.00PM	-

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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand safety rules, regulations and norms which will help them in construction site. How to handle and maintains of tools and machinery. Also by learning about SHE, carefully planned, organised, monitored and controlled.

### Course Outline

Module 1	Machine Operation And Guarding	5 Hours
Module 2	Materials Handling And Storage Of Materials	5 Hours
Module 3	Construction Industry	5 Hours
Module 4	Working At Heights	7 Hours
Module 5	Noise And Vibration	8 Hours

### COURSE OUTCOMES:

After successfully completion of this course, the student will be able to

**CO 1:** Know how to Handle and Protect the Machine and himself while working on it

**CO 2:** Know the Knowledge of lifting and carrying the materials and also maintenance of mechanical material handling equipment

**CO 3:** Understand the knowledge of safety rules and regulations of working at construction industry

**CO 4:** Understand the knowledge about working at all stages in different heights at construction site

**CO 5:** Know the knowledge of different types of noise and vibration, its causes, effects and controlling measures.

  
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## DEPARTMENT OF CIVIL ENGINEERING

### Reference Books

1. Industrial safety management By: L.M. Deshmukh Publishers: Tata Megraw Hill ,New Delhi  
Year: 2006 Edition: First
2. Industrial safety health and environment Management system By: R.K. Jain & Sunil S. Rao  
Publishers: Khanna Publishers Year: 2008 Edition: Second

Course Coordinator	Resource Person
P. Sruthi Asst. professor CE Department Anurag Engineering College	G. Narendar Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 07-12-2015

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2015-16.

Name of the Value added course: Safety in Construction

Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 10-12-2015.

Course coordinator:

P. Sruthi.

Asst. Professor,

CE Dept.,

Mobile No: 9110714939

*C. M. Reddy*  
7/12/15  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 14-12-2015

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Safety In Construction "is scheduled from 15 Dec 2015 to 02 April 2016.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

G. Narendar

Asst. Professor,

CE Department,

Mobile: 9492909685

*C. mreddy*  
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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

### Introduction to PSPICE (Personal Computer Simulation Program with Integrated Circuit Emphasis)

#### About The Course

This course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters.

#### Purpose of the Course

This course is focused on engineers, professors and researchers in power systems and power electronics, interested in exploring the modern tools available for modeling, design and analysis.

#### Eligibility

II B.Tech I Semester (Electrical and Electronics Engineering)

#### Prerequisites

Basic schematic entry with ORCAD Capture

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To use techniques of creating PSPICE Designs.
- To Run DC bias, DC sweep, AC sweep, mesh and nodal analysis. Design of circuits for power electronics converters.
- To learn how to edit a stimulus and run a parametric analysis.
- To create, edit and implement models, create sub-circuits and create parts for simulation.
- To analyze the single phase converters with RLE loads.

### Course Outlines

MODULE	Topic	Hours
MODULE 1	Introduction to PSPICE	6 Hours
MODULE 2	Preparing a design for simulation	6 Hours
MODULE 3	Creating and Editing Models	6 Hours
MODULE 4	Creating Parts for Models	6 Hours
MODULE 5	Simulation Examples	6 Hours

### Course Outcomes

After completing this course, student will be able to:

- Use techniques of creating PSPICE Designs.
- Run DC bias, DC sweep, AC sweep, mesh and nodal analysis and able to design power electronics converter circuits for simulation.
- Edit a stimulus and run a parametric analysis
- Create, edit and implement models, create sub-circuits and create parts for simulation.
- Analyze the single phase converters with RLE loads.

### Reference Books

1. Muhammad H. Rashid, "Spice for Circuits and Electronics Using Pspice", 2<sup>nd</sup> Edition, Prentice Hall India, 1990.
2. Muhammad H. Rashid, "SPICE for Power Electronics and Electric Power" CRC Press, 3<sup>rd</sup> Edition, 2012.

<b>Course Coordinator</b>	<b>Resource Person</b>
Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College	T.Raghu Asst. professor EEE Department Anurag Engineering College

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 15.07.2015


### CIRCULAR

All the II B.Tech I Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2015-16.

**Name of the Value added course:** Introduction to PSPICE  
**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 18.07.2015

**Course coordinator:**  
J.Srinivas Rao  
Asst.Professor,  
EEE Dept.,  
Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 20.07.2015

### CIRCULAR

All the II B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to PSPICE" is scheduled from 21.07.2015 to 15.11.2015.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

T.Raghu,  
Asst. Professor,  
EEE Department,  
Mobile:9959692070




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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to MATLAB

### About The Course

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include:

- Math and computation
- Algorithm development
- Modeling, simulation, and prototyping
- Data analysis, exploration, and visualization
- Scientific and engineering graphics
- Application development, including Graphical User Interface building

### Purpose of the Course

A proprietary programming language developed by MathWorks, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, Java, Fortran and Python.

### Eligibility

III B.Tech II Semester (Electrical and Electronics Engineering)

### Prerequisites

Once you get to know the representation of data and other things used while programming you can easily pull it off. The Prerequisites are

- Basic knowledge of Programming.
- Know the need of MATLAB and its implementation.
- The most important aspect is the syntax.
- Get to know the need of different tools used in it.
- Know the applications of it in different fields

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

  
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### Course Objectives

- To familiarize the student in introducing and exploring MATLAB software.
- To enable the student on how to approach for solving Engineering problems using simulation tools.
- To prepare the students to use MATLAB in their project works.
- To provide a foundation in use of this softwares for real time applications.
- To familiarize the student in modeling the networks by using MATLAB software.

### Course Outlines

MODULE 1	<p>MATLAB Window Environment and the Base Program: Introduction to MATLAB family of programs Brief introduction to MATLAB base program in an interactive "hands on" tutorial style Brief introduction to various tool boxes related to electrical and electronics engineering design and applications.</p>	6 Hours
MODULE 2	<p>Simulink: Building simple Simulink simulations. Running Simulink simulation to predict a system's behaviour. Feedback System Simulation: Presenting MATLAB functions to carryout block diagram manipulations, followed by time-response analysis. Using Simulink simulation for time-response analysis.</p>	6 Hours
MODULE 3	<p>Time Response Characteristics and LTI Viewer: Analysis of the system behavior to changes in input reference signals or disturbances using steady-state and transient performance indices. Brief introduction to LTI Viewer, and time response analysis using this GUI tool. Stability Analysis on Root Locus Plots: Stability analysis in state space. Stability analysis using root locus plots.</p>	6 Hours
MODULE 4	<p>Root Locus Design and SISO Design Tools: Design using root locus plots. Brief introduction to SISO Design Tool, and root locus design using this GUI tool. Stability Analysis on Bode / Nyquist Plots: Introducing MATLAB commands for Bode and Nyquist plots. Stability analysis using these plots.</p>	6 Hours
MODULE 5	<p>Simulink models: Simulink models for single phase and three phase full converters. Simulink models for single phase and three phase half controlled converters.</p>	6 Hours



	Simulink models for single area load frequency control problem.	
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<b>Course Outcomes</b>	
•	Student gets familiarize in using the MATLAB software.
•	Student gain the knowledge for solving Engineering problems using simulation tools.
•	Student will be able to do their project works by using MATLAB software.
•	Student develops awareness in use of this software for real time applications.
•	Student will be able to analyze and modeling the networks by using MATLAB software.

<b>Reference Books</b>	
1.	Modeling and Simulation using MATLAB - Simulink, 2ed
2.	Getting Started with MATLAB: A Quick Introduction for Scientists & Engineers
3.	MATLAB: An Introduction with Applications, 4ed
4.	MATLAB Programming for Engineers by Stephen J. Chapman
5.	Introduction to MATLAB for Engineers by William J. Palm III
6.	An Introduction to Scientific Computing: Twelve Computational Projects Solved with MATLAB by Ionut Danaila

<b>Course Coordinator</b>	<b>Resource Person</b>
T.Raghu Asst. professor EEE Department Anurag Engineering College	Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 07.12.2015

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2015-16.

**Name of the Value added course:** Introduction to MATLAB

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 09.12.2015


**Course coordinator:**

T.Raghu

Asst. Professor,

EEE Dept.,

Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 10.12.2015

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to MATLAB" is scheduled from 11.12.2015 to 02.04.2016.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

J.Srinivas Rao

Asst. Professor,

EEE Department,

Mobile:9848044209



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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ANSYS

To learn the basis of AUTOCAD commands, 3D Modelling and Analysis of various problems

#### Purpose the Course

To generate the engineering drawings, creates the 3D modelling and analyses the various engineering component problems.

#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

3D Modelling.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

- To be able to understand and handle design problems in a systematic manner
- To be able to apply CAD in real life Applications.
- To be able to understand the 3D Design Components.
- To be able to apply Stress Analysis on Various Beams.
- To be able to apply Stress Analysis on Various Parts.

### Course Outcomes

- CO 1: Ability to understand the basics of AUTOCAD.
- CO 2: To get Practice on 2D and Isometric Diagrams.
- CO 3: To get Practice on 3D Components.
- CO 4: To get Practice on stress analysis of Various Beams.
- CO 5: To get practice on Truss Analysis.

### Course Outline

Module	Topic	Hours
Module 1	Basics of AUTOCAD	6hours
Module 2	Practice on 2D and isometric Diagrams	6hours
Module 3	Introduction to CREO Practice on 3D Modelling	6hours
Module 4	Introduction to ANSYS	6hours
Module 5	Practice on various engineering components Analysis.	6hours

### Reference Books

1. AutoCAD reference guide by Chetan Prakasan Published by CCAD Solution training and sense
2. PTC Creo Parametric by Prof. Shan Tickoo.
3. Engineering analysis with ansys software by Tadeusz stolarski s. yoshimoto, yasuniro narkasone.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 07-12-2015

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2015-16.

Name of the Value added course: ANSYS  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 10-12-2015


Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008



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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 14-12-2015

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "ANSYS" is scheduled from 15-12-2015 to 02-04-2016.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM (Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

CREO

To Learn the Sketching, 3D Modelling and assembling of Mechanical Components

#### Purpose the Course

To Generate the Engineering drawings and creates the 3D Modelling.


#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

AutoCAD Commands.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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Ananthagiri(V),Kodad(M) Nalgonda(Dt),Telangana-508 206.

## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to apply CAD in real life applications.  
To be able to apply Sketching of 2D Commands.  
To be able to apply Conversion of Diagrams.  
To be able to apply Modelling Techniques.  
To be able to apply Assembling Components.

### Course Outcomes

CO 1: Ability to understand the basics of CREO  
CO 2: Ability to understand the sketching of 2D.  
CO 3: To get Practice converting from 2D to 3D.  
CO 4: To get Practice on 3D modelling  
CO 5: To get practice on assembling of 3D components.

### Course Outline

Module	Topic	Hours
Module 1	Introduction to CREO	6hours
Module 2	Practice on Sketching	6hours
Module 3	Conversion of 2D to 3D	6hours
Module 4	Practice on 3D Modelling	6hours
Module 5	Practice on assembly	6hours

### Reference Books

1.PTC Creo Parametric by Prof.Shan Tickoo.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

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Nuryapet (Dt.), Telangana-508206



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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 29-06-2015

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2015-16.

Name of the Value added course: PTC CREO

Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 01-07-2015

Course coordinator:

E Sadanandam

Assistant Professor.

Department of Mechanical Engineering

Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 06-07-2015

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PTC CREO" is scheduled from 07-07-2015 to 17-10-2015.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM (Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

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Encl: List of students

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

### Introduction to Mentor graphics Design Tools

#### About The Course

Fundamental of Designers can develop an executable functional specification that documents the exact behavior of all the components and their interfaces Designers can make decisions about cost, performance, power, and area earlier in the design process Designers can create tools which automatically manipulate the design for verification, synthesis, optimization, etc. Hardware description languages are an essential part of modern digital design.

#### Purpose The Course

This course is focused to engineers, to Digital Design concepts & implementation.

#### Eligibility

IV B.Tech (Electronics and Communication Engineering)

#### Prerequisites

Knowledge on schematic, simulation-calibre and analog and digital electronics, VLSI.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

#### Course Objectives

- Learn the mentor graphic tool or equivalent tool for design the schematic and simulation of all gates.
- To understand the concept of digital system design method through practical domain.
- Design combinational and sequential circuits
- HDLs help manage complexity inherent in modern designs.
- Analyze and layout design of CMOS circuits in micron and submicron level using any platform.

  
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Course Outlines		
<b>MODULE 1</b>	Introduction to mentor graphics <ul style="list-style-type: none"> <li>• Mentor graphics resources</li> <li>• Different Modeling styles used in Verilog.</li> <li>• Mentor graphics Application builder</li> <li>• DUT verification by simulation using Directed Test bench</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual instruments <ul style="list-style-type: none"> <li>• Introduction to back end</li> <li>• FPGA Architecture basics</li> <li>• ASIC &amp; FPGA Design flow</li> <li>• Synthesis and FPGA Implementation</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	Mentor graphics Environment <ul style="list-style-type: none"> <li>• Verifying the HDL codes by writing Test benches using ISim</li> <li>• Synthesis of HDL codes of Xilinx FPGA Kits</li> <li>• IPCoregen flow</li> <li>• mentor graphics tool functions description</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Build the schematic and simulation <ul style="list-style-type: none"> <li>• Full Adder,</li> <li>• Ripple-carry Adder.</li> <li>• DFF, Counters, Shifters.</li> <li>• ALU, RAM/ROM etc..</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Layout design <ul style="list-style-type: none"> <li>• DRC, PEX</li> <li>• FPGA development kits</li> <li>• ASIC v/s FPGA design flows</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1: Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary For engineering practice.
CO2: Use computer-aided design tools to synthesize, map, place, routing, and download the digital Designs on the FPGA board.
CO3: Model digital systems in VHDL and Partition a digital system into different subsystems..
CO4: Transfer a design from a version possible to simulate to a version possible to synthesize..
CO5: Learn the basics of Fabrication and Layout of CMOS Integrated Circuits..

References
1. <a href="https://www.mentor.com/products/ic-manufacturing/">https://www.mentor.com/products/ic-manufacturing/</a>
2. <a href="https://www.mentor.com/training">https://www.mentor.com/training</a>

COURSE COORDINATOR:	RESOURCE PERSON:
Mr.M.Basha, Associate Professor, ECE Dept. Anurag Engineering college	Mrs.T.Sridevi, Assistant Professor ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DATE:29-06-2015

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2015-2016.

Name of the Value-added course: Introduction to Mentor graphics Design Tools.  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 01-07-2015

Course coordinator:  
Mr.M.Basha,  
Associate Professor,  
ECE Dept.  
Mobile No: 9985046220

*Mushy*  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE:02-07-2015

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to Mentor graphics Design Tools" is scheduled from 03<sup>rd</sup> July 2015 to 17<sup>th</sup> Oct 2016.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mrs.T.Sridevi,  
Assistant Professor  
ECE Dept,  
Mobile: 9951897944.

*Mrs. T. Sridevi*  
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DEPARTMENT OF ELECTRONICS and COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY

## About The Course

This course describes the LABVIEW graphical programming environment and techniques for building applications in LABVIEW, such as test and measurement, data acquisition, instrument control, data logging, measurement analysis, and report generation applications.

## Purpose The Course

This course is to learn about LABVIEW programming features, including the LABVIEW user interface and programming workspaces, and the LABVIEW palettes and tools. This course does not include specific information about each palette, tool, menu, dialog box, control, or built-in VI or function.

## Eligibility

III B.Tech (Electronics And Communication Engineering)

## Prerequisites

Knowledge on logical designing, signals and electronic analysis.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

1. Introduction to LABVIEW
2. Introduction to Virtual Instruments
3. LABVIEW Environment
4. Building the Front Panel and Building the Block Diagram
5. Running and Debugging VIs and Creating VIs and Sub VIs

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 07-12-2015

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2015-16.

Name of the Value-added course:INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY  
Eligible branches:ECE.

Interested students can enrol their names with the course coordinator on or before 10-12-2015

Course coordinator:  
Mr.M.Basha,  
Associate Professor,  
ECE Dept.  
Mobile No: 9985046220

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

---

DATE: 10-12-2015

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to LABVIEW Laboratory Feasibility" is scheduled from 11<sup>th</sup> Dec 2015 to 02<sup>nd</sup> April 2016.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Miss.Sohana Parveen,

Assistant Professor,

ECE Dept.

Mobile: 9652921300

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PYTHON PROGRAMMING

Python is a popular general purpose programming language used for both large and small-scale applications. Python's wide-spread adoption is due in part to its large standard library, easy readability and support of multiple paradigms including functional, procedural and object-oriented programming styles.

#### Purpose the Course

Python is a general-purpose language, which means it can be used to build just about anything, which will be made easy with the right tools/libraries. Professionally, Python is great for backend web development, data analysis, artificial intelligence, and scientific computing.


#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Basic programming concepts

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	60	4:00PM-5:00PM	-

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives

- This course introduces core programming basics—including data types, control structures, files and program design with functions—via the Python programming language.
- Students will solve problems, explore real-world software development challenges, and create practical and contemporary applications.

### Course Outline

Module 1	Installing Python, Running Code in the Interactive Shell, Input-Output Processing, IDLE	6 Hours
Module 2	Data Types, Expressions, Built-in Functions	6 Hours
Module 3	Control Statements	6 Hours
Module 4	Strings	4 Hours
Module 5	Lists, Tuples & Dictionaries	8 Hours

### Course Outcomes

After completion of this course the students will be able to

1. Run scripts using python IDLE
2. Design scripts using functions
3. Use control statements in python scripts
4. Manipulate strings in python
5. Apply data structures in python programming

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Reference Books

Fundamentals of Python: First Programs, Second Edition by Kenneth A. Lambert, Martin Osborne, CENGAGE Learning.

Course Coordinator	Resource Person
M.Shailaja Asst. Prof. Department Of C.S.E Anurag Engineering College	P.Sandeep Reddy Assoc. Prof. Department Of C.S.E Anurag Engineering College

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:29-06-2015

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering is going to offer the following value added courses for the academic year 2015-16.

**Name of the Value Added Course:** Python Programming  
**Eligible Branches:** CSE

Interested students can enroll their names with the course coordinator on or before 02-07-2015


**Course Coordinator:**

M.Shailaja,  
Asst. Professor,  
CSE Department,  
Mobile:9640003186

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 03-07-2015

### CIRCULAR


All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Python Programming" is scheduled from 06 July 2015 to 17 October 2015.

**Venue:** A-209, Block-A

**Timings:** 4:00 PM to 5:00 PM (Thursday - Saturday)

**Resource Person Details:**

P.Sandeep Reddy,  
Assoc. Professor,  
CSE Department,  
Mobile:9989900651

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PHP

PHP is the most popular server-side language used to build dynamic websites, and though it is a very extensive language. The stateless web (HTML, CSS and JavaScript) can only do so much without a dynamic language such as PHP to add the ability to interact with the web server.

#### Purpose the Course

To learn how to make pages dynamic based upon user interaction, interacting with HTML forms and store and retrieve information from local data sources which include a database.


#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Knowledge on HTML, XHTML, CSS and JavaScript.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	40	60	4:00PM-5:00PM	-

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives


To introduce PHP language for server side scripting, understanding usage of function and arrays in PHP scripting, validating Form Data, user authentication concepts, MySQL Database and handling file uploads.

### Course Outline

Module 1	<b>PHP Basics:</b> Setting up a development environment Variables, numbers and strings, Calculations with PHP, Using Arrays, Control Structures and Loops	6Hours
Module 2	<b>Functions, Objects and Errors:</b> PHP's Built-in functions, Creating Custom functions, Passing Values by Reference, Understanding Objects, Server-side includes.	6 Hours
Module 3	<b>Working with Forms:</b> Building a Form, Processing a Form's Data, Differences between POST and GET, Preserving User Input, Dealing with checkboxes and radio buttons, Retrieving values from lists, Validating and restricting data, Sending Email	6 Hours
Module 4	<b>Storing and Protecting Data:</b> Setting and Reading Cookies, Protecting Online Files, Understanding Session Variables	6 Hours
Module 5	<b>MySQL Database Overview:</b> phpMyAdmin Overview, Using a MySQL Database, Reading and Writing Data	6 Hours

### Course Outcomes

1. Have understanding of server side scripting with PHP language.
2. Create PHP programs that use various PHP library functions, and Custom functions
3. Able to analyze and solve common Web application tasks by writing PHP programs.
4. Able to protect online files.
5. Able to analyze and solve various database tasks using the PHP language.

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Reference Books

1. W. Jason Gilmore, Beginning PHP and MySQL, 4th Edition. Apress, 2010.
2. Steven Holzner, PHP: The Complete Reference. McGraw-Hill, 2008

Course Coordinator	Resource Person
K.Vijaya Kumar Assoc. Prof. Department Of C.S.E Anurag Engineering College	Y.V.R.Naga Pawan Assoc. Prof. Department Of C.S.E Anurag Engineering College

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:07-12-2015

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Computer Science and Engineering is going to offer the following value added courses for the academic year 2015-16.

**Name of the Value Added Course: PHP**

**Eligible Branches: CSE**

Interested students can enroll their names with the course coordinator on or before 10-12-2015.

**Course Coordinator:**

K.Vijay Kumar

Assoc.Professor,

CSE Dept.,

Mobile No: 9030900962

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:11-12-2015

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PHP" is scheduled from 14 Dec 2015 to 02 April 2016.

**Venue:** A-209, Block-A

**Timings:** 4:00 PM to 5:00 PM (Thursday - Saturday)

**Resource Person Details:**

Y.V.R Naga Pawan,

Assoc. Professor,

CSE Department,

Mobile:9553122276

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### SOFT SKILLS

#### About the Course

Soft skills are a cluster of productive personality traits that characterize one's relationships in a milieu.

#### Purpose the Course

Soft skills training gives benefits for Empowerment, Mastery & Purpose ability to communicate effectively with co-workers, employers, clients and customers, friends and family members etc.


#### Eligibility

MBA

#### Prerequisites

- Leadership Skills,
- Work Ethics.
- Teamwork.
- Time Management.
- Critical Thinking.
- Adaptability.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timmings	Fees
30	20	60	4:00 pm to 5:00 pm	NILL

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### Course Objectives

Each student has a realistic perspective of work and work expectations, to help formulate problem solving skills, to guide students in making appropriate and responsible decisions, to create a desire to fulfill individual goals.

### Course Outline

Course Outline		
Module 1	<ol style="list-style-type: none"><li>1. Introduction to Soft Skills &amp; Hard Skills</li><li>2. Break the ice berg – FEAR</li><li>3. Self Development – Etiquette &amp; Manners</li></ol>	6 Hours
Module 2	<ol style="list-style-type: none"><li>1. Communication Skills</li><li>2. Body Language</li><li>3. Priority Management &amp; Time Management</li></ol>	6 Hours
Module 3	<ol style="list-style-type: none"><li>1. Positive Attitude &amp; Self Confidence</li><li>2. Motivation Skills &amp; Personality</li><li>3. Development</li></ol>	6 Hours
Module 4	<ol style="list-style-type: none"><li>1. Career Planning</li><li>2. Resume Building</li></ol>	6 Hours

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

	3. Goal Setting	
Module 5	1. Psychometric Test 2. Interviewing Skills 3. Group Discussion (GD)	6 Hours

## Reference Books

1. "Leading with GRIT: Inspiring Action and Accountability with Generosity, Respect, Integrity, and Truth" By **Laurie Sudbrink**
2. "Stronger: Develop the Resilience You Need to Succeed" By **George S. Everly Jr. and Douglas A. Strouse**
3. Extreme Ownership: How U.S. Navy SEALs Lead and Win" By **Jocko Willink and Leif Babin**

Course Coordinator	Resource Person
V.Sarada Asst. professor MBA Department Anurag Engineering College	CH.Ramesh Asst. professor MBA Department Anurag Engineering College

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 02-01-2016

### CIRCULAR

All the II MBA II Semester students are hereby informed that the department of Master of Business Administration is going to offer the following value added courses for the academic year 2015-16.

Name of the Value added course: Soft Skills

Eligible branches: MBA

Interested students can enrol their names with the course coordinator on or before 20-01-2016

Course coordinator:

V.Sarada

Asst.Professor,

MBA Dept.,

Mobile No: 9347244972


  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 01-02-2016

### CIRCULAR

All the II MBA II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "SOFT SKILLS" is scheduled from 04-02-2016 to 28-06-2016.

Venue: A-308, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

CH.Ramesh,

Asst. Professor,

MBA Department,

Mobile: 9553122280

  
HOD


**Encl:** List of students

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## LIST OF VALUE ADDED COURSES OFFERED IN THE A.Y. 2016 – 17

S. No.	Course Code	Name of Value Added Course Offered
1	CE16VA02	Training on STAAD Pro (Analysis and Designing)
2	CE16VA03	MIDAS TRAINING PROGRAMME
3	EE16VA01	Introduction to PSPICE
4	EE16VA02	Introduction to MATLAB
5	ME16VA01	ANSYS
6	ME16VA02	CREO
7	EC16VA01	Introduction to Mentor graphics Design Tools
8	EC16VA02	Introduction to LAB View
9	CS16VA01	Internet of Things
10	CS16VA02	Programming for problem solving
11	MB16VA01	Soft Skills

  
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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### TRAINING ON STAAD PRO

STAAD Pro is a Structural Analysis and Design Computer Program. It supports several steel, concrete and timber design codes. It can also make use of various forms of dynamic analysis from modal extraction to time history and response spectrum analysis.

#### Purpose the Course

To improve professional skills.


#### Eligibility

Anyone interested Pursuing graduate.

#### Prerequisites

Knowledge on DRCS, DSS and Structural Analysis.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	29	4.00-5.00PM	-

  
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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand the importance of STAAD in designing the structures and basic commands, model generation, application of loads, material properties. Students are analyse simple structures without errors and to analyse concrete and steel structural designing.

### Course Outline

Course Outline		
Module 1	Introduction Of STAAD PRO Layout & Tool Bars, STAAD Entities.	4 Hours
Module 2	Solving And Understanding 2D & 3D Structures.	4 Hours
Module 3	STAAD Coordinate System, Viewing Results and Debugging.	4 Hours
Module 4	Assigning Properties, Material, and Supports & Loads to a beam.	4 Hours
Module 5	Moving, Wind and Seismic Loads.	4 Hours
Module 6	Design Of Steel Structures and Classification Of Structures.	4 Hours
Module 7	Animations, Concrete Design and Specifications.	3 Hours
Module 8	Concrete Parameters and Creating Geometry Of Plane Frame Using Structure Wizard.	3 Hours

### COURSE OUTCOMES:

On successful completion of this course, it is expected that

**CO 1:** Basics and Introduction about STAAD PRO analysis.

**CO 2:** Students will be able to generate a mode & can apply the loads & properties of the material for beams.

**CO 3:** Students will be able to analyse & design a steel structure.

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CO 4: Students will understand performance of steel Structures.

CO 5: Students will be able to analyse & design a plane frame.

### Reference Books

1. Design of R C C Buildings using Staad Pro V8i with Indian Examples English by Author T S Sarma.
2. Illustrated Design Of Reinforced Concrete Buildings by Dr. S.R.Karve & Dr. V.L.Shah Paperback
3. Exploring Bentley STAAD.Pro V8i (SELECTseries 6) by Sham Tickoo/TIET

Course Coordinator	Resource Person
D.VNV Laxmi Alekhya Asst. professor CE Department Anurag Engineering College	C. Manikanta Reddy Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 13-06-2016

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2016-17.

Name of the Value added course: Training on STAAD Pro (Analysis and Designing)  
Eligible branches: CE


Interested students can enrol their names with the course coordinator on or before 16-6-2016

Course coordinator:  
D.VNV LAXMI ALEKHYA  
Asst. Professor,  
CE Dept.,  
Mobile No: 8463944571

*C. M. Reddy*  
13/6/16  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 19-06-2017

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Training on STAAD Pro (Analysis and Designing)" is scheduled from 20 June 2017 to 16 Oct 2017.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

#### Resource Person Details:

C. Manikanta Reddy

Asst. Professor,

CE Department,

Mobile: 9703111700

  
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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### MIDAS TRAINING PROGRAMME

Students are introduced to MIDAS software, it is the most advanced structural engineering system, which has integrated the total process of structural engineering practice – 3D modelling, analysis, design and 2D drawings. With the automation and optimization facility, it generates comprehensive structural drawings, structural calculation reports and quantity takeoffs, which help the engineers, reduce time required to produce high quality design deliverables and gain productivity.

### Purpose the Course

Students who wish to learn the techniques and tools to design the total process of structural engineering practice – 3D modelling, analysis, design and 2D drawings. With the automation and reduce time required to produce high quality design deliverables and gain productivity.

### Eligibility

Anyone interested Pursuing graduate.

### Prerequisites

Knowledge On SA and DSS.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
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## DEPARTMENT OF CIVIL ENGINEERING

30	10	15	4.00-5.00PM	-
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### Course Objectives

Students will get analysis and design of structures using this software getting more efficiency and getting more accuracy to manual calculations.

### Course Outline

Module 1 -	Introduction, Basic Steps for Modelling Cable Stay Bridge	5 Hours
Module 2	Finding Cable Pretension Forces, Unknown Load Factor	5 Hours
Module 3	Cable Tuning, Bending energy method, Construction Stage Analysis	5 Hours
Module 4	Unknown Load Factor, In construction stages, Lack of Fit Force	8Hours
Module 5	Moving load analysis Buckling of Dynamic analysis	7 Hours

### COURSE OUTCOMES:

At the end of course student will about to


CO 1: Basics in modelling of cable stay bridge.

CO 2: How to calculate forces in cable stay bridge.

CO 3: Understand how to Analyse cable Stay Bridge.

CO 4: Various construction stages involved in cable stay bridge.

CO 5: Basics in moving and buckling of dynamic analysis.

  
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## DEPARTMENT OF CIVIL ENGINEERING

### Reference Books

MIDAS software Manual.

Course Coordinator	Resource Person
S.Naresh Asst. professor CE Department Anurag Engineering College	C. Manikanta Reddy Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 21-11-2016

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2016-17.

Name of the Value added course: MIDAS TRAINING PROGRAMME

Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 24-11-2016.

Course coordinator:

S.NARESH

Asst. Professor,

CE Dept.,

Mobile No: 9705916301

*C.M. Reddy*  
21/11/16  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 28-11-2016

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "MIDAS TRAINING PROGRAMME" is scheduled from 29 Nov 2016 to 23 March 2017.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

**Resource Person Details:**

C. Manikanta Reddy

Asst. Professor,

CE Department,

Mobile: 9703111700

*C. Manikanta Reddy*  
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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

#### Introduction to PSPICE (Personal Computer Simulation Program with Integrated Circuit Emphasis)

#### About The Course

This course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters.

#### Purpose of the Course

This course is focused on engineers, professors and researchers in power systems and power electronics, interested in exploring the modern tools available for modeling, design and analysis.


#### Eligibility

Pursuing Graduates of Electrical and Electronics Engineering.

#### Prerequisites

Basic schematic entry with ORCAD Capture

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

  
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### Course Objectives

- To use techniques of creating PSPICE Designs.
- To Run DC bias, DC sweep, and AC sweep analysis
- To learn how to edit a stimulus and run a parametric analysis
- To create, edit and implement models, create sub-circuits and create parts for simulation.
- To create linear and nonlinear transformers, and perform temperature, worst-case, and noise analysis

### Course Outlines

MODULE	Topic	Hours
MODULE 1	Introduction to PSPICE	6 Hours
MODULE 2	Preparing a design for simulation	6 Hours
MODULE 3	Creating and Editing Models	6 Hours
MODULE 4	Creating Parts for Models	6 Hours
MODULE 5	Simulation Examples	6 Hours

### Course Outcomes

After completing this course, student will be able to:

- Use techniques of creating PSPICE Designs.
- Run DC bias, DC sweep, and AC sweep analysis
- Edit a stimulus and run a parametric analysis
- Create, edit and implement models, create sub-circuits and create parts for simulation.
- Create linear and nonlinear transformers, and perform temperature, worst-case, and noise analysis

### Reference Books

1. Muhammad H. Rashid, "Spice for Circuits and Electronics Using Pspice", 2<sup>nd</sup> Edition, Prentice Hall India, 1990.
2. Muhammad H. Rashid, "SPICE for Power Electronics and Electric Power" CRC Press, 3<sup>rd</sup> Edition, 2012.

<b>Course Coordinator</b>	<b>Resource Person</b>
Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College	T.Raghu Asst. professor EEE Department Anurag Engineering College

  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 27.06.2016

### CIRCULAR

All the II B.Tech I Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2016-17.

**Name of the Value added course:** Introduction to PSPICE

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 29.06.2016

**Course coordinator:**

J.Srinivas Rao

Asst.Professor,

EEE Dept.,

Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 30.06.2016

### CIRCULAR

All the II B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to PSPICE" is scheduled from 01.07.2016 to 29.10.2016.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

T.Raghu,  
Asst. Professor,  
EEE Department,  
Mobile:9959692070




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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

#### Introduction to MATLAB

#### About The Course

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include:

- Math and computation
- Algorithm development
- Modeling, simulation, and prototyping
- Data analysis, exploration, and visualization
- Scientific and engineering graphics
- Application development, including Graphical User Interface building

#### Purpose of the Course

A proprietary programming language developed by Math Works, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, Java, Fortran and Python.

#### Eligibility


Pursuing Graduates of Electrical and Electronics Engineering.

#### Prerequisites

Once you get to know the representation of data and other things used while programming you can easily pull it off. The Prerequisites are

- Basic knowledge of Programming.
- Know the need of MATLAB and its implementation.
- The most important aspect is the syntax.
- Get to know the need of different tools used in it.
- Know the applications of it in different fields

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

  
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### Course Objectives

- To familiarize the student in introducing and exploring MATLAB software.
- To enable the student on how to approach for solving Engineering problems using simulation tools.
- To prepare the students to use MATLAB in their project works.
- To provide a foundation in use of this softwares for real time applications.
- To familiarize the student in modeling the networks by using MATLAB software.

### Course Outlines


MODULE 1	<p>MATLAB Window Environment and the Base Program:                      Introduction to MATLAB family of programs                      Brief introduction to MATLAB base program in an interactive "hands on" tutorial style                      Control System Toolbox and Symbolic Math Toolbox:                      Using 'MATLAB base program + Control System toolbox', for control systems analysis and design.                      Brief introduction to Symbolic Math toolbox.</p>	6 Hours
MODULE 2	<p>Simulink:                      Building simple Simulink simulations.                      Running Simulink simulation to predict a system's behaviour.                      Feedback System Simulation:                      Presenting MATLAB functions to carryout block diagram manipulations, followed by time-response analysis.                      Using Simulink simulation for time-response analysis.</p>	6 Hours
MODULE 3	<p>Time Response Characteristics and LTI Viewer:                      Analysis of the system behavior to changes in input reference signals or disturbances using steady-state and transient performance indices.                      Brief introduction to LTI Viewer, and time response analysis using this GUI tool.                      Stability Analysis on Root Locus Plots:                      Stability analysis in state space.                      Stability analysis using root locus plots.</p>	6 Hours
MODULE 4	<p>Root Locus Design and SISO Design Tools:                      Design using root locus plots.                      Brief introduction to SISO Design Tool, and root locus design using this GUI tool.                      Stability Analysis on Bode / Nyquist Plots:                      Introducing MATLAB commands for Bode and Nyquist plots.                      Stability analysis using these plots.</p>	6 Hours
MODULE 5	<p>Frequency Response Characteristics:                      Frequency response measures from Bode plots and</p>	6 Hours

	<p>Nichols chart, for unity-feedback systems.  Frequency response measures from Bode plots for non-unity feedback systems.  Frequency Response Design and SISO Design Tool:  Design using Bode plots and Nichols chart.  Frequency response design using SISO Design Tool.</p>	
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<b>Course Outcomes</b>
<ul style="list-style-type: none"> <li>● Student gets familiarize in using the MATLAB software.</li> </ul>
<ul style="list-style-type: none"> <li>● Student gain the knowledge for solving Engineering problems using simulation tools.</li> </ul>
<ul style="list-style-type: none"> <li>● Student will be able to do their project works by using MATLAB software.</li> </ul>
<ul style="list-style-type: none"> <li>● Student develops awareness in use of this software for real time applications.</li> </ul>
<ul style="list-style-type: none"> <li>● Student will be able to analyze and modeling the networks by using MATLAB software.</li> </ul>

<b>Reference Books</b>
1. Modeling and Simulation using MATLAB - Simulink, 2ed
2. Getting Started with MATLAB: A Quick Introduction for Scientists & Engineers
3. MATLAB: An Introduction with Applications, 4ed
4. MATLAB Programming for Engineers by Stephen J. Chapman
5. Introduction to MATLAB for Engineers by William J. Palm III
6. An Introduction to Scientific Computing: Twelve Computational Projects Solved with MATLAB by Ionut Danaila

<b>Course Coordinator</b>	<b>Resource Person</b>
<p>T.Raghu  Asst. professor  EEE Department  Anurag Engineering College</p>	<p>Mr.J.Srinivas Rao  Asst. professor  EEE Department  Anurag Engineering College</p>

  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 21.11.2016

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2016-17.

**Name of the Value added course:** Introduction to MATLAB

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 23.11.2016

Course coordinator:

T.Raghu

Asst.Professor,

EEE Dept.,

Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 24.11.2016

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to MATLAB" is scheduled from 25.11.2016 to 23.03.2017.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

J.Srinivas Rao

Asst. Professor,

EEE Department,

Mobile:9848044209



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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ANSYS

To learn the basis of AUTOCAD commands, 3D Modelling and Analysis of various problems

#### Purpose the Course

To generate the engineering drawings, creates the 3D modelling and analyses the various engineering component problems.

#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

3D Modelling.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to understand and handle design problems in a systematic manner  
To be able to apply CAD in real life Applications.  
To be able to understand the 3D Design Components.  
To be able to apply Stress Analysis on Various Beams.  
To be able to apply Stress Analysis on Various Parts.

### Course Outcomes

CO 1: Ability to understand the basics of AUTOCAD.  
CO 2: To get Practice on 2D and Isometric Diagrams.  
CO 3: To get Practice on 3D Components.  
CO 4: To get Practice on stress analysis of Various Beams.  
CO 5: To get practice on Truss Analysis.

### Course Outline

Module	Topic	Hours
Module 1	Basics of AUTOCAD	6hours
Module 2	Practice on 2D and isometric Diagrams	6hours
Module 3	Introduction to CREO Practice on 3D Modelling	6hours
Module 4	Introduction to ANSYS	6hours
Module 5	Practice on various engineering components Analysis.	6hours

### Reference Books

1. AutoCAD reference guide by Chetan Prakasan Published by CCAD Solution training and sense
2. PTC Creo Parametric by Prof. Shan Tickoo.
3. Engineering analysis with ansys software by Tadeusz stolarski s. yoshimoto, yasuniro narkasone.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 21-11-2016

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2016-17.

Name of the Value added course: ANSYS  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 24-11-2016

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 28-11-2016

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "ANSYS" is scheduled from 29-11-2016 to 23-03-2017.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.



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## DEPARTMENT OF MECHANICAL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### About the Course

CREO

To Learn the Sketching, 3D Modelling and assembling of Mechanical Components

### Purpose the Course

To Generate the Engineering drawings and creates the 3D Modelling.


### Eligibility

Pursuing Graduates of Mechanical Engineering.

### Prerequisites

AutoCAD Commands.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to apply CAD in real life applications.  
To be able to apply Sketching of 2D Commands.  
To be able to apply Conversion of Diagrams.  
To be able to apply Modelling Techniques.  
To be able to apply Assembling Components.

### Course Outcomes

CO 1: Ability to understand the basics of CREO  
CO 2: Ability to understand the sketching of 2D.  
CO 3: To get Practice converting from 2D to 3D.  
CO 4: To get Practice on 3D modelling  
CO 5: To get practice on assembling of 3D components.


### Course Outline

Module	Topic	Hours
Module 1	Introduction to CREO	6hours
Module 2	Practice on Sketching	6hours
Module 3	Conversion of 2D to 3D	6hours
Module 4	Practice on 3D Modelling	6hours
Module 5	Practice on assembly	6hours

### Reference Books

1.PTC Creo Parametric by Prof.Shan Tickoo.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 13-06-2016

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2016-17.

Name of the Value added course: PTC CREO  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 16-06-2016

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008



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## DEPARTMENT OF MECHANICAL ENGINEERING

---

DATE: 20-06-2016

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PTC CREO" is scheduled from 21-06-2016 to 03-11-2016.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

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# ANURAG ENGINEERING COLLEGE

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES**

**Introduction to Mentor graphics Design Tools**

## About The Course

Fundamental of Designers can develop an executable functional specification that documents the exact behavior of all the components and their interfaces Designers can make decisions about cost, performance, power, and area earlier in the design process Designers can create tools which automatically manipulate the design for verification, synthesis, optimization, etc. Hardware description languages are an essential part of modern digital design.

## Purpose The Course

This course is focused to engineers, to Digital Design concepts & implementation.

## Eligibility

IV B.Tech (Electronics and Communication Engineering)


## Prerequisites

Knowledge on schematic, simulation-calbre and analog and digital electronics, VLSI.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Learn the mentor graphic tool or equivalent tool for design the schematic and simulation of all gates.
- To understand the concept of digital system design method through practical domain.
- Design combinational and sequential circuits
- HDLs help manage complexity inherent in modern designs.
- Analyze and layout design of CMOS circuits in micron and submicron level using any platform.


  
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Course Outlines		
<b>MODULE 1</b>	Introduction to mentor graphics <ul style="list-style-type: none"> <li>• Mentor graphics resources</li> <li>• Different Modeling styles used in Verilog.</li> <li>• Mentor graphics Application builder</li> <li>• DUT verification by simulation using Directed Test bench</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual instruments <ul style="list-style-type: none"> <li>• Introduction to back end</li> <li>• FPGA Architecture basics</li> <li>• ASIC &amp; FPGA Design flow</li> <li>• Synthesis and FPGA Implementation</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	Mentor graphics Environment <ul style="list-style-type: none"> <li>• Verifying the HDL codes by writing Test benches using ISim .</li> <li>• Synthesis of HDL codes of Xilinx FPGA Kits</li> <li>• IPCoregen flow</li> <li>• mentor graphics tool functions description</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Build the schematic and simulation <ul style="list-style-type: none"> <li>• Full Adder,</li> <li>• Ripple-carry Adder.</li> <li>• DFF. Counters. Shifters.</li> <li>• ALU, RAM/ROM etc..</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Layout design <ul style="list-style-type: none"> <li>• DRC, PEX</li> <li>• FPGA development kits</li> <li>• ASIC v/s FPGA design flows</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1: Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary For engineering practice.
CO2: Use computer-aided design tools to synthesize, map, place, routing, and download the digital Designs on the FPGA board.
CO3: Model digital systems in VHDL and Partition a digital system into different subsystems..
CO4: Transfer a design from a version possible to simulate to a version possible to synthesize..
CO5: Learn the basics of Fabrication and Layout of CMOS Integrated Circuits..

References
1. <a href="https://www.mentor.com/products/ic-manufacturing/">https://www.mentor.com/products/ic-manufacturing/</a>
2. <a href="https://www.mentor.com/training">https://www.mentor.com/training</a>

COURSE COORDINATOR:	RESOURCE PERSON:
Dr.B.Rajan. Professor, ECE Dept. Anurag Engineering college	Mr.P.Subhakar, Assistant Professor, ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 13-06-2016

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2016-2017.

Name of the Value-added course: Introduction to Mentor graphics Design Tools.  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 15-06-2016.

Course coordinator:  
Dr. B. Rajan,  
Professor,  
ECE Dept.  
Mobile No: 79971 30343

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE:15-06-2016

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to Mentor graphics Design Tools" is scheduled from 16<sup>th</sup> June 2016 to 03<sup>rd</sup> Nov 2017.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr.P.Subhakar,  
Assistant Professor,  
ECE Dept,  
Mobile: 97041 13489.

*muf*  
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DEPARTMENT OF ELECTRONICS and COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY

## About The Course

This course describes the LABVIEW graphical programming environment and techniques for building applications in LABVIEW, such as test and measurement, data acquisition, instrument control, data logging, measurement analysis, and report generation applications.

## Purpose The Course

This course is to learn about LABVIEW programming features, including the LABVIEW user interface and programming workspaces, and the LABVIEW palettes and tools. This course does not include specific information about each palette, tool, menu, dialog box, control, or built-in VI or function.

## Eligibility

III B.Tech (Electronics And Communication Engineering)

## Prerequisites

Knowledge on logical designing, signals and electronic analysis.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Introduction to LABVIEW
- Introduction to Virtual Instruments
- LABVIEW Environment
- Building the Front Panel and Building the Block Diagram
- Running and Debugging VIs and Creating VIs and Sub VIs

## Course Outlines

Course Outlines		
<b>MODULE 1</b>	Introduction To Labview <ul style="list-style-type: none"><li>• LABVIEW Resources</li><li>• LABVIEW Measurements</li><li>• LABVIEW Application Builder and</li><li>• LABVIEW Template VIs</li></ul>	<b>6 Hours</b>

<b>MODULE 2</b>	Introduction to Virtual Instruments <ul style="list-style-type: none"> <li>● Introduction to Front Panel</li> <li>● Introduction to Nodes, Wires and Structures</li> <li>● Introduction to Icon and Connector Pane</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	LABVIEW Environment <ul style="list-style-type: none"> <li>● Controls and Functions palettes</li> <li>● Navigating the Controls and Functions Palettes</li> <li>● Tools Palette</li> <li>● Menus and the Toolbar</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Building the Front Panel and Building the Block Diagrams <ul style="list-style-type: none"> <li>● Configuring Front Panel Objects and Changing Controls to Indicators and Indicators to Controls</li> <li>● Replacing Front Panel Objects and Configuring the Front Panel</li> <li>● Relationship between Front Panel Objects and Block Diagram Terminals</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Running and Debugging VIs and Creating VIs and Sub Vis <ul style="list-style-type: none"> <li>● Running Vis and Configuring How a VI Runs</li> <li>● Correcting Broken VIs</li> <li>● Finding Causes for Broken Vis and Common Causes of Broken VIs</li> <li>● Debugging Techniques and Execution Highlighting</li> <li>● Disabling Debugging Tools</li> </ul>	<b>6 Hours</b>


#### Course Outcomes

CO1:Understand the basics of virtual instrumentation concept and dataflow programming
CO2:Understand various functions available in Lab VIEW for engineering applications
CO3:Design projects using the functions available in Lab VIEW
CO4:Understand the interfacing of DAQ devices and customized user designed hardware with Lab VIEW
CO5:Write the Certified LabVIEW Associate Developer (CLAD) exam, administered by National Instruments, for the certification and leading to placements in core companies

#### References

1. <a href="http://www.ni.com/en-in/innovations">www.ni.com/en-in/innovations</a> .
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<b>COURSE COORDINATOR:</b>	<b>RESOURCE PERSON:</b>
Dr.B.Rajan Professor ECE Dept. Anurag Engineering college	Mr.K.Vamshi Krishna, Assistant Professor, ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 02-12-2016

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2016-17.

Name of the Value-added course: INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 06-12-2016

Course coordinator:  
Dr. B. Rajan,  
Professor,  
ECE Dept.  
Mobile No: 79971 30343

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 08-12-2016

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to LABVIEW Laboratory Feasibility" is scheduled from 09<sup>th</sup> Dec 2016 to 14<sup>th</sup> April 2017.

Venue: Block-D: 408-SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr.K.Vamshi Krishna,  
Assistant Professor  
ECE Dept.  
Mobile: 9989655948

*M. S. J.*  
HOD

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### INTERNET OF THINGS

Internet of Things (IoT) is a scenario in which objects, animals or people are provided with unique identifiers and the ability to transfer data over a network without requiring human-to-human or human-to-computer interaction. The Internet of Things (IoT, sometimes Internet of Everything) is the network of physical objects or "things" embedded with electronics, software, sensors and connectivity to enable it to achieve greater value and service by exchanging data with the manufacturer, operator and/or other connected devices. Each thing is uniquely identifiable through its embedded computing system but is able to interoperate within the existing Internet infrastructure. Experts estimate that the IoT will consist of almost 50 billion objects by 2020.

#### Purpose the Course


Students will learn IoT introduction, significance, building their own IoT devices using Arduino and Raspberry Pi systems, sensors, IoT communication and programming. This training will help you be a part of the IoT revolution underway around the globe.

#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

- Microprocessor and controller.
- Python, C, C++, embedded C.
- Knowledge of sensor transducer actuator interfacing.
- Some networking concepts.
- Wireless communication protocol network.
- OSI reference model.

  
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No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	60	4.00PM-5.00PM	--

### Course Objectives

The objectives of the course are to introduce the students to the core concepts of Internet of Things (IoT), make them understand the building blocks & characteristics of IoT and to explain the role & scope of smart sensors for ensuring convergence of technologies

### Course Outline

Course Outline		
Module 1	IoT Architecture	6 Hours
Module 2	IoT Sensors	6 Hours
Module 3	The Arduino Platform and Interfacing	6 Hours
Module 4	The Raspberry Pi Platform and Interfacing	6 Hours
Module 5	IoT Programming	6 Hours

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Understand the concepts of Internet of Things.
2. Understand constraints and opportunities of wireless and mobile networks for Internet of Things.
3. Apply data analytics and use cloud offerings related to Internet of Things.
4. Interpret the impact and challenges posed by Internet of Things leading to Industry perspective.
5. Analyze applications of Internet of Things in real time scenario

### Reference Books

1. Biron and J. Follett, "Foundational Elements of an IoT Solution", O'Reilly Media, 2016.
2. Keysight Technologies, "The Internet of Things: Enabling Technologies and Solutions for Design and Test", Application Note, 2016.
3. Charles Bell, "Beginning Sensor Networks with Arduino and Raspberry Pi", Apress, 2013.

Course Coordinator	Resource Person
Y.Laxmi Prasanna Assoc. Prof. Department Of C.S.E Anurag Engineering College	G.Srinivasa Rao Assoc. Prof. Department Of C.S.E Anurag Engineering College

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:13-06-2016

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering is going to offer the following value added courses for the academic year 2016-17.

**Name of the Value Added Course:** Internet of Things

**Eligible Branches:** CSE

Interested students can enroll their names with the course coordinator on or before 16-06-2016.

**Course Coordinator:**

Y.Laxmi Prasanna

Assoc.Professor,

CSE Dept.,

Mobile No: 9959595655


  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:17-06-2016

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled “Internet of Things” is scheduled from 20 June 2016 to 18 October 2016.

**Venue:** A-209, Block-A

**Timings:** 4:00 PM to 5:00 PM (Thursday - Saturday)

**Resource Person Details:**

G.Srinivasa Rao,  
Assoc. Professor,  
CSE Department,  
Mobile:9989978583

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PROGRAMMING FOR PROBLEM SOLVING

Students are introduced to all major language elements including fundamental data types, flow control, and standard function libraries. The course explains the use of aggregate Arrays, structures, unions, and pointers early on so the students can practice extensively in the hands on labs. The course also includes coverage of portability tips drawn from experienced programmers working in production environments. Comprehensive hands on exercises are integrated throughout to reinforce learning and develop real competency.

#### Purpose the Course

To improve proficiency in programming

#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Knowledge on Mathematical and fundamental programming concepts .

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
31	40	180	4.00PM - 5:00PM	-

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives

Students will be able to develop logics which will help them to create programs, applications in C. Also by learning the basic programming constructs they can easily switch over to any other language in future.

### Course Outline

Course Outline		
Module 1	Introduction to Problem Solving ,Variables and Data Types, Operators and expressions	5 Hours
Module 2	Control Structures	5 Hours
Module 3	Arrays , 2-D arrays, Character Arrays and Strings	5 Hours
Module 4	Functions, Storage classes, Pointers	8 Hours
Module 5	Structures ,Unions ,files	8 Hours

### Reference Books

1. Byron Gottfried, Schaum's Outline of Programming with C, McGraw-Hill
2. E. Balaguruswamy, Programming in ANSI C, Tata McGraw-Hill
3. Brian W. Kernighan and Dennis M. Ritchie, The C Programming Language, Prentice Hall of India

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course outcomes

1. Able to understand the fundamental syntax & operators of C
2. Able to design programs utilizing decision making and repetition
3. Ability to work with arrays
4. Able to design modular programs using functions , Demonstrate the concept of pointer
5. Develop programs using user defined data types and perform I/O operations in files

Course Coordinator	Resource Person
CH.Krishna Prasad Assoc. Prof. Department Of C.S.E Anurag Engineering College	Y.V.R.Naga Pawan Assoc. Prof. Department Of C.S.E Anurag Engineering College

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 21-11-2016

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Computer Science and Engineering is going to offer the following value added courses for the academic year 2016-17.

**Name of the Value Added Course:** Programming for Problem Solving  
**Eligible Branches:** CSE

Interested students can enroll their names with the course coordinator on or before 24-11-2016.

**Course Coordinator:**

Ch.Krishna Prasad,  
Assoc.Professor,  
CSE Dept.,  
Mobile No: 9848684256

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 25-11-2016

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled “**Programming for Problem Solving**” is scheduled from 26 November 2016 to 23 March 2017.

**Venue:** A-209, Block-A

**Timings:** 4:00 PM to 5:00 PM (Thursday - Saturday)

**Resource Person Details:**


Y.V.R Naga Pawan,  
Assoc. Professor,  
CSE Department,  
Mobile:9553122276

  
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Ananthagiri (V&M), Kodad,  
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# ANURAG Engineering College

(An Autonomous Institution)

(Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)  
Ananthagiri (V&M), Suryapet (Dt). Pin: 508 206.

## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### SOFT SKILLS

#### About the Course

Soft skills are a cluster of productive personality traits that characterize one's relationships in a milieu.

#### Purpose the Course

Soft skills training gives benefits for Empowerment, Mastery & Purpose ability to communicate effectively with co-workers, employers, clients and customers, friends and family members etc.


#### Eligibility

MBA

#### Prerequisites

- Leadership Skills,
- Work Ethics.
- Teamwork.
- Time Management.
- Critical Thinking.
- Adaptability.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timmings	Fees
30	20	60	4:00 pm to 5:00 pm	NILL

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### Course Objectives

Each student has a realistic perspective of work and work expectations, to help formulate problem solving skills, to guide students in making appropriate and responsible decisions, to create a desire to fulfill individual goals.

### Course Outline

Course Outline		
Module 1	<ol style="list-style-type: none"><li>1. Introduction to Soft Skills &amp; Hard Skills</li><li>2. Break the ice berg – FEAR</li><li>3. Self Development – Etiquette &amp; Manners</li></ol>	6 Hours
Module 2	<ol style="list-style-type: none"><li>1. Communication Skills</li><li>2. Body Language</li><li>3. Priority Management &amp; Time Management</li></ol>	6 Hours
Module 3	<ol style="list-style-type: none"><li>1. Positive Attitude &amp; Self Confidence</li><li>2. Motivation Skills &amp; Personality</li><li>3. Development</li></ol>	6 Hours
Module 4	<ol style="list-style-type: none"><li>1. Career Planning</li><li>2. Resume Building</li><li>3. Goal Setting</li></ol>	6 Hours

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

Module 5	<ol style="list-style-type: none"><li>1. Psychometric Test</li><li>2. Interviewing Skills</li><li>3. Group Discussion (GD)</li></ol>	6 Hours
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### Reference Books

1. "Leading with GRIT: Inspiring Action and Accountability with Generosity, Respect, Integrity, and Truth" By Laurie Sudbrink
2. "Stronger: Develop the Resilience You Need to Succeed" By George S. Everly Jr. and Douglas A. Strouse
3. Extreme Ownership: How U.S. Navy SEALs Lead and Win" By Jocko Willink and Leif Babin

Course Coordinator	Resource Person
G.Varma Asst. professor MBA Department Anurag Engineering College	CH.Ramesh Asst. professor MBA Department Anurag Engineering College

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 31-12-2016

### CIRCULAR

All the II MBA II Semester students are hereby informed that the department of Master of Business Administration is going to offer the following value added courses for the academic year 2016-17.

Name of the Value added course: SOFT SKILLS

Eligible branches: MBA

Interested students can enrol their names with the course coordinator on or before 19-01-2017

Course coordinator:

G.Varma

Asst.Professor,

MBA Dept.,

Mobile No: 9989382096


  
HOD

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

DATE: 03-02-2017

### CIRCULAR

All the II MBA II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "SOFT SKILLS" is scheduled from 06-02-2017 to 02-07-2017.

Venue: A-308, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

CH.Ramesh,

Asst. Professor,

MBA Department,

Mobile: 9553122280

  
HOD


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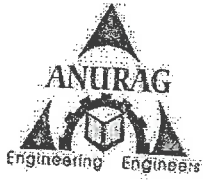
## LIST OF VALUE ADDED COURSES OFFERED IN THE A.Y. 2017 – 18

S. No.	Course Code	Name of Value Added Course Offered
1	CE17VA01	Safety In Construction
2	CE17VA02	Training on STAAD Pro (Analysis and Designing)
3	CE17VA04	Clean and Green Campus Training
4	EE17VA01	Introduction to PSPICE
5	EE17VA02	Introduction to MATLAB
6	ME17VA01	ANSYS
7	ME17VA02	CREO
8	EC17VA01	Introduction to Mentor graphics Design Tools
9	EC17VA02	Introduction to LAB View
10	CS17VA01	Oracle Java Programming
11	CS17VA02	Oracle Database Design
12	CS17VA03	Salesforce Training Program
13	MB17VA01	Soft Skills
14	HS17VAQR	CRT-M

  
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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### SAFETY IN CONSTRUCTION

Students are given training in regarding safety in construction local and international standards and laws associated with health and safety in construction activities, identification and controlling of workplace dangers and hazards and practical application of knowledge in real time situations. To join and study this course, no previous health and safety knowledge is required although it is advisable that candidates should have basic understanding of construction activities.

#### Purpose the Course

To improve professional skills.

#### Eligibility

III B TECH (CE) Students.

#### Prerequisites

Knowledge on Building materials and construction.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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### Course Objectives

Students will be able to understand safety rules, regulations and norms which will help them in construction site. How to handle and maintains of tools and machinery. Also by learning about SHE, carefully planned, organised, monitored and controlled.

### Course Outline

Module 1	Machine Operation And Guarding	5 Hours
Module 2	Materials Handling And Storage Of Materials	5 Hours
Module 3	Construction Industry	5 Hours
Module 4	Working At Heights	7 Hours
Module 5	Noise And Vibration	8 Hours

### COURSE OUTCOMES:

After successfully completion of this course, the student will be able to

- CO 1: Know how to Handle and Protect the Machine and himself while working on it
- CO 2: Know the Knowledge of lifting and carrying the materials and also maintenance of mechanical material handling equipment
- CO 3: Understand the knowledge of safety rules and regulations of working at construction industry
- CO 4: Understand the knowledge about working at all stages in different heights at construction site
- CO 5: Know the knowledge of different types of noise and vibration, its causes, effects and controlling measures.

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## DEPARTMENT OF CIVIL ENGINEERING

### Reference Books

1. Industrial safety management By: L.M. Deshmukh Publishers: Tata Megraw Hill ,New Delhi  
Year: 2006 Edition: First
2. Industrial safety health and environment Management system By: R.K. Jain & Sunil S. Rao  
Publishers: Khanna Publishers Year: 2008 Edition: Second

Course Coordinator	Resource Person
P. Sruthi Asst. professor CE Department Anurag Engineering College	G. Narendar Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 12-6-2017

### CIRCULAR

All the III B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: Safety in Construction  
Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 15-6-2017

Course coordinator:  
P. Sruthi.  
Asst. Professor,  
CE Dept.,  
Mobile No: 8985113144

*C. M. Reddy*  
12/6/17  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 19-06-2017

### CIRCULAR

All the III B: Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Safety In Construction" is scheduled from 20 June 2017 to 16 Oct 2017.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

**Resource Person Details:**

G. Narendar

Asst. Professor,

CE Department,

Mobile: 9492909685

*C. M. Reddy*  
HOD

**Encl:** List of students

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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### **TRAINING ON STAAD PRO**

STAAD Pro is a Structural Analysis and Design Computer Program. It supports several steel, concrete and timber design codes. It can also make use of various forms of dynamic analysis from modal extraction to time history and response spectrum analysis.

#### **Purpose the Course**

To improve professional skills.

#### **Eligibility**

Anyone interested Pursuing graduate.

#### **Prerequisites**

Knowledge on DRCS, DSS and Structural Analysis.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	29	4.00-5.00PM	-

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### Course Objectives

Students will be able to understand the importance of STAAD in designing the structures and basic commands, model generation, application of loads, material properties. Students are analyse simple structures without errors and to analyse concrete and steel structural designing.

### Course Outline

Course Outline		
Module 1	Introduction Of STAAD PRO Layout & Tool Bars, STAAD Entities.	4 Hours
Module 2.	Solving And Understanding 2D & 3D Structures.	4 Hours
Module 3	STAAD Coordinate System, Viewing Results and Debugging.	4 Hours
Module 4	Assigning Properties, Material, and Supports & Loads to a beam.	4 Hours
Module 5	Moving, Wind and Seismic Loads.	4 Hours
Module 6	Design Of Steel Structures and Classification Of Structures.	4 Hours
Module 7	Animations, Concrete Design and Specifications.	3 Hours
Module 8	Concrete Parameters and Creating Geometry Of Plane Frame Using Structure Wizard.	3 Hours

### COURSE OUTCOMES:

On successful completion of this course, it is expected that

**CO 1:** Basics and Introduction about STAAD PRO analysis.

**CO 2:** Students will be able to generate a mode & can apply the loads & properties of the material for beams.

**CO 3:** Students will be able to analyse & design a steel structure.

**CO 4:** Students will understand performance of steel Structures.

**CO 5:** Students will be able to analyse & design a plane frame.

  
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## DEPARTMENT OF CIVIL ENGINEERING

### Reference Books

1. Design of R C C Buildings using Staad Pro V8i with Indian Examples English by Author T S Sarma.
2. Illustrated Design Of Reinforced Concrete Buildings by Dr. S.R.Karve & Dr. V.L.Shah Paperback
3. Exploring Bentley STAAD.Pro V8i (SELECTseries 6) by Sham Tickoo/TIET

Course Coordinator	Resource Person
D.VNV Laxmi Alekhya Asst. professor CE Department Anurag Engineering College	C.Manikanta Reddy Asst. professor CE Department Anurag Engineering College

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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 12-6-2017

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: Training on STAAD Pro (Analysis and Designing)

Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 15-6-2017

Course coordinator:

D.VNV LAXMI ALEKHYA

Asst. Professor,

CE Dept.,

Mobile-No: 8463944571

*C. Madhavi*  
HOD 15/6/17

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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 20-6-2016

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Training on STAAD Pro (Analysis and Designing)" is scheduled from 21 June 2016 to 03 November 2016.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

#### Resource Person Details:

C. Manikanta Reddy

Asst. Professor,

CE Department,

Mobile: 9703111700

HOD

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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### CLEAN AND GREEN CAMPUS TRAINING

Students are introduced to all major aspects of Providing optimal clean, green and smart learning space continues to be the aspiration of almost each and every educational institute. However, there are several barriers such as lack of availability of knowledge. How about the technologies, limited expertise of the people in the institutes, time & budgetary constraints and more. On the other hand, there are several tangible and intangible benefits of infusing and expanding clean, green and smart learning spaces in colleges, institutes and universities.

### Purpose the Course

To improve social developmental skills and responsible for social activities.

### Eligibility

III B TECH (CE) Students.

### Prerequisites

Knowledge on Environmental Studies.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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## DEPARTMENT OF CIVIL ENGINEERING

Course Coordinator	Resource Person
G. Narendar Asst. professor CE Department Anurag Engineering College	D. Sravanthi Asst. professor CE Department Anurag Engineering College

  
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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 20-11-2017

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: Clean and Green Campus Training  
Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 23-11-2017.

Course coordinator:  
G.NARENDAR  
Asst. Professor,  
CE Dept.,  
Mobile No: 9492909685

*amreddy*  
HOD 20/11/17

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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 26-11-2017

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Clean and Green Campus Training" is scheduled from 27 Nov 2017 to 17 March 2018.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

D. Sravanthi

Asst. Professor,

CE Department,

Mobile: 8985113144

  
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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to PSPICE (Personal Computer Simulation Program with Integrated Circuit Emphasis)

### About The Course

This course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters.

### Purpose of the Course

This course is focused on engineers, professors and researchers in power systems and power electronics, interested in exploring the modern tools available for modeling, design and analysis.

### Eligibility

II B.Tech I Semester (Electrical and Electronics Engineering)

### Prerequisites

Basic schematic entry with ORCAD Capture

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To use techniques of creating PSPICE Designs.
- To Run DC bias, DC sweep, AC sweep, mesh and nodal analysis. Design of circuits for power electronics converters.
- To learn how to edit a stimulus and run a parametric analysis
- To create, edit and implement models, create sub-circuits and create parts for simulation.
- To analyze the single phase converters with RLE loads.

### Course Outlines

MODULE	Topic	Hours
MODULE 1	Introduction to PSPICE	6 Hours
MODULE 2	Preparing a design for simulation	6 Hours
MODULE 3	Creating and Editing Models	6 Hours
MODULE 4	Creating Parts for Models	6 Hours
MODULE 5	Simulation Examples	6 Hours

### Course Outcomes

After completing this course, student will be able to:

- Use techniques of creating PSPICE Designs.
- Run DC bias, DC sweep, AC sweep, mesh and nodal analysis and able to design power electronics converter circuits for simulation.
- Edit a stimulus and run a parametric analysis
- Create, edit and implement models, create sub-circuits and create parts for simulation.
- Analyze the single phase converters with RLE loads.

### Reference Books

1. Muhammad H. Rashid, "Spice for Circuits and Electronics Using Pspice", 2<sup>nd</sup> Edition, Prentice Hall India, 1990.
2. Muhammad H. Rashid, "SPICE for Power Electronics and Electric Power" CRC Press, 3<sup>rd</sup> Edition, 2012.

<b>Course Coordinator</b>	<b>Resource Person</b>
Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College	T.Raghu Asst. professor EEE Department Anurag Engineering College

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 01.07.2017

### CIRCULAR

All the II B.Tech I Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2017-18.

**Name of the Value added course:** Introduction to PSPICE

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 03.07.2017

**Course coordinator:**

J.Srinivas Rao

Asst.Professor,

EEE Dept.,

Mobile No: 9848044209




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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 04.07.2017

### CIRCULAR

All the II.B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to PSPICE" is scheduled from 05.07.2017 to 07.11.2017.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

T.Raghu,

Asst. Professor,

EEE Department,

Mobile:9959692070



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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to MATLAB

### About The Course

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include:

- Math and computation
- Algorithm development
- Modeling, simulation, and prototyping
- Data analysis, exploration, and visualization
- Scientific and engineering graphics
- Application development, including Graphical User Interface building

### Purpose of the Course

A proprietary programming language developed by MathWorks, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, Java, Fortran and Python.

### Eligibility

III B.Tech II Semester (Electrical and Electronics Engineering)

### Prerequisites

Once you get to know the representation of data and other things used while programming you can easily pull it off. The Prerequisites are

- Basic knowledge of Programming.
- Know the need of MAT LAB and its implementation.
- The most important aspect is the syntax.
- Get to know the need of different tools used in it.
- Know the applications of it in different fields

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To familiarize the student in introducing and exploring MATLAB software.
- To enable the student on how to approach for solving Engineering problems using simulation tools.
- To prepare the students to use MATLAB in their project works.
- To provide a foundation in use of this softwares for real time applications.
- To familiarize the student in modeling the networks by using MATLAB software.

### Course Outlines

MODULE 1	MATLAB Window Environment and the Base Program: Introduction to MATLAB family of programs Brief introduction to MATLAB base program in an interactive "hands on" tutorial style Brief introduction to various tool boxes related to electrical and electronics engineering design and applications.	6 Hours
MODULE 2	Simulink: Building simple Simulink simulations. Running Simulink simulation to predict a system's behaviour. Feedback System Simulation: Presenting MATLAB functions to carryout block diagram manipulations, followed by time-response analysis. Using Simulink simulation for time-response analysis.	6 Hours
MODULE 3	Time Response Characteristics and LTI Viewer: Analysis of the system behavior to changes in input reference signals or disturbances using steady-state and transient performance indices. Brief introduction to LTI Viewer, and time response analysis using this GUI tool. Stability Analysis on Root Locus Plots: Stability analysis in state space. Stability analysis using root locus plots.	6 Hours
MODULE 4	Root Locus Design and SISO Design Tools: Design using root locus plots. Brief introduction to SISO Design Tool, and root locus design using this GUI tool. Stability Analysis on Bode / Nyquist Plots: Introducing MATLAB commands for Bode and Nyquist plots. Stability analysis using these plots.	6 Hours
MODULE 5	Simulink models: Simulink models for single phase and three phase full converters. Simulink models for single phase and three phase half controlled converters.	6 Hours



	Simulink models for single area load frequency control problem.	
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<b>Course Outcomes</b>	
•	Student gets familiarize in using the MATLAB software.
•	Student gain the knowledge for solving Engineering problems using simulation tools.
•	Student will be able to do their project works by using MATLAB software.
•	Student develops awareness in use of this software for real time applications.
•	Student will be able to analyze and modeling the networks by using MATLAB software.

<b>Reference Books</b>	
1.	Modeling and Simulation using MATLAB - Simulink, 2ed
2.	Getting Started with MATLAB: A Quick Introduction for Scientists & Engineers
3.	MATLAB: An Introduction with Applications, 4ed
4.	MATLAB Programming for Engineers by Stephen J. Chapman
5.	Introduction to MATLAB for Engineers by William J. Palm III
6.	An Introduction to Scientific Computing: Twelve Computational Projects Solved with MATLAB by Ionut Danaila

<b>Course Coordinator</b>	<b>Resource Person</b>
T.Raghu Asst. professor EEE Department Anurag Engineering College	Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College

  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 20.11.2017

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2017-18.

**Name of the Value added course:** Introduction to MATLAB

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 22.11.2017


**Course coordinator:**

T.Raghu  
Asst.Professor,  
EEE Dept.,  
Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 23.11.2017

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to MATLAB" is scheduled from 24.11.2017 to 17.03.2018.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

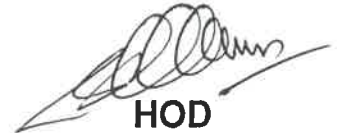
**Resource Person Details:**

J.Srinivas Rao

Asst. Professor,

EEE Department,

Mobile:9848044209



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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ANSYS

To learn the basis of AUTOCAD commands, 3D Modelling and Analysis of various problems

#### Purpose the Course

To generate the engineering drawings, creates the 3D modelling and analyses the various engineering component problems.


#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

3D Modelling.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

- To be able to understand and handle design problems in a systematic manner
- To be able to apply CAD in real life Applications.
- To be able to understand the 3D Design Components.
- To be able to apply Stress Analysis on Various Beams.
- To be able to apply Stress Analysis on Various Parts.

### Course Outcomes

- CO 1: Ability to understand the basics of AUTOCAD.
- CO 2: To get Practice on 2D and Isometric Diagrams.
- CO 3: To get Practice on 3D Components.
- CO 4: To get Practice on stress analysis of Various Beams.
- CO 5: To get practice on Truss Analysis.

### Course Outline

Module	Topic	Hours
Module 1	Basics of AUTOCAD	6hours
Module 2	Practice on 2D and isometric Diagrams	6hours
Module 3	Introduction to CREO Practice on 3D Modelling	6hours
Module 4	Introduction to ANSYS	6hours
Module 5	Practice on various engineering components Analysis.	6hours

### Reference Books

1. AutoCAD reference guide by Chetan Prakasan Published by CCAD Solution training and sense
2. PTC Creo Parametric by Prof. Shan Tickoo.
3. Engineering analysis with ansys software by Tadeusz stolarski s. yoshimoto, yasuniro narkasone.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 20-11-2017

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: ANSYS  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 23-11-2017

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 26-11-2017

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "ANSYS" is scheduled from 27-11-2017 to 17-03-2018.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM ((Thursday, Friday & Saturday)

#### Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### About the Course

CREO

To Learn the Sketching, 3D Modelling and assembling of Mechanical Components

### Purpose the Course

To Generate the Engineering drawings and creates the 3D Modelling.


### Eligibility

Pursuing Graduates of Mechanical Engineering.

### Prerequisites

AutoCAD Commands.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to apply CAD in real life applications.  
To be able to apply Sketching of 2D Commands.  
To be able to apply Conversion of Diagrams.  
To be able to apply Modelling Techniques.  
To be able to apply Assembling Components.

### Course Outcomes

CO 1: Ability to understand the basics of CREO  
CO 2: Ability to understand the sketching of 2D.  
CO 3: To get Practice converting from 2D to 3D.  
CO 4: To get Practice on 3D modelling  
CO 5: To get practice on assembling of 3D components.


### Course Outline

Course Outline		
Module 1	Introduction to CREO	6hours
Module 2	Practice on Sketching	6hours
Module 3	Conversion of 2D to 3D	6hours
Module 4	Practice on 3D Modelling	6hours
Module 5	Practice on assembly	6hours

### Reference Books

1.PTC Creo Parametric by Prof.Shan Tickoo.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 12-06-2017

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: CREO  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 15-06-2017

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 19-06-2017

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PTC CREO" is scheduled from 20-06-2017 to 16-10-2017

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM (Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

  
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

Introduction to Mentor graphics Design Tools

## About The Course

Fundamental of Designers can develop an executable functional specification that documents the exact behavior of all the components and their interfaces Designers can make decisions about cost, performance, power, and area earlier in the design process Designers can create tools which automatically manipulate the design for verification, synthesis, optimization, etc. Hardware description languages are an essential part of modern digital design.

## Purpose The Course

This course is focused to engineers, to Digital Design concepts & implementation.

## Eligibility

IV B.Tech (Electronics and Communication Engineering)


## Prerequisites

Knowledge on schematic, simulation-calbre and analog and digital electronics, VLSI.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Learn the mentor graphic tool or equivalent tool for design the schematic and simulation of all gates.
- To understand the concept of digital system design method through practical domain.
- Design combinational and sequential circuits
- HDLs help manage complexity inherent in modern designs.
- Analyze and layout design of CMOS circuits in micron and submicron level using any platform.


  
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Course Outlines		
<b>MODULE 1</b>	Introduction to mentor graphics <ul style="list-style-type: none"> <li>• Mentor graphics resources</li> <li>• Different Modeling styles used in Verilog.</li> <li>• Mentor graphics Application builder</li> <li>• DUT verification by simulation using Directed Test bench</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual instruments <ul style="list-style-type: none"> <li>• Introduction to back end</li> <li>• FPGA Architecture basics</li> <li>• ASIC &amp; FPGA Design flow</li> <li>• Synthesis and FPGA Implementation</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	Mentor graphics Environment <ul style="list-style-type: none"> <li>• Verifying the HDL codes by writing Test benches using ISim .</li> <li>• Synthesis of HDL codes of Xilinx FPGA Kits</li> <li>• ICoregen flow</li> <li>• mentor graphics tool functions description</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Build the schematic and simulation <ul style="list-style-type: none"> <li>• Full Adder,</li> <li>• Ripple-carry Adder.</li> <li>• DFF. Counters. Shifters.</li> <li>• ALU, RAM/ROM etc..</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Layout design <ul style="list-style-type: none"> <li>• DRC, PEX</li> <li>• FPGA development kits</li> <li>• ASIC v/s FPGA design flows</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1: Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary For engineering practice.
CO2: Use computer-aided design tools to synthesize, map, place, routing, and download the digital Designs on the FPGA board.
CO3: Model digital systems in VHDL and Partition a digital system into different subsystems..
CO4: Transfer a design from a version possible to simulate to a version possible to synthesize..
CO5: Learn the basics of Fabrication and Layout of CMOS Integrated Circuits..

References
1. <a href="https://www.mentor.com/products/ic-manufacturing/">https://www.mentor.com/products/ic-manufacturing/</a>
2. <a href="https://www.mentor.com/training">https://www.mentor.com/training</a>

COURSE COORDINATOR:	RESOURCE PERSON:
Mr. L.Hariprasad, Assistant Professor, ECE Dept. Anurag Engineering college	Mr. S.Rambabu Assistant Professor, ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 12-06-2017

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2017-2018.

Name of the Value-added course: Introduction to Mentor graphics Design Tools.  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 14-06-2017

Course coordinator:  
Mr.L.Hari Prasad,  
Assistant Professor,  
ECE Dept.,  
Mobile No: 97050 29143

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE:15-06-2017

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to Mentor graphics Design Tools" is scheduled from 16<sup>th</sup> June 2017 to 16<sup>th</sup> Oct 2017.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr.S.Rambabu,  
Assistant Professor,  
ECE Dept.  
Mobile: 84148 15720

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DEPARTMENT OF ELECTRONICS and COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY

## About The Course

This course describes the LABVIEW graphical programming environment and techniques for building applications in LABVIEW, such as test and measurement, data acquisition, instrument control, data logging, measurement analysis, and report generation applications.

## Purpose The Course

This course is to learn about LABVIEW programming features, including the LABVIEW user interface and programming workspaces, and the LABVIEW palettes and tools. This course does not include specific information about each palette, tool, menu, dialog box, control, or built-in VI or function.

## Eligibility

III B.Tech (Electronics And Communication Engineering)

## Prerequisites

Knowledge on logical designing, signals and electronic analysis.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Introduction to LABVIEW
- Introduction to Virtual Instruments
- LABVIEW Environment
- Building the Front Panel and Building the Block Diagram
- Running and Debugging VIs and Creating VIs and Sub VIs

## Course Outlines

Course Outlines		
MODULE 1	Introduction To Labview <ul style="list-style-type: none"><li>• LABVIEW Resources</li><li>• LABVIEW Measurements</li><li>• LABVIEW Application Builder and</li><li>• LABVIEW Template VIs</li></ul>	6 Hours

  
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<b>MODULE 2</b>	Introduction to Virtual Instruments <ul style="list-style-type: none"> <li>● Introduction to Front Panel</li> <li>● Introduction to Nodes, Wires and Structures</li> <li>● Introduction to Icon and Connector Pane</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	LABVIEW Environment <ul style="list-style-type: none"> <li>● Controls and Functions palettes</li> <li>● Navigating the Controls and Functions Palettes</li> <li>● Tools Palette</li> <li>● Menus and the Toolbar</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Building the Front Panel and Building the Block Diagrams <ul style="list-style-type: none"> <li>● Configuring Front Panel Objects and Changing Controls to Indicators and Indicators to Controls</li> <li>● Replacing Front Panel Objects and Configuring the Front Panel</li> <li>● Relationship between Front Panel Objects and Block Diagram Terminals</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Running and Debugging VIs and Creating VIs and Sub VIs <ul style="list-style-type: none"> <li>● Running Vis and Configuring How a VI Runs</li> <li>● Correcting Broken VIs</li> <li>● Finding Causes for Broken Vis and Common Causes of Broken VIs</li> <li>● Debugging Techniques and Execution Highlighting</li> <li>● Disabling Debugging Tools</li> </ul>	<b>6 Hours</b>

<b>Course Outcomes</b>
CO1:Understand the basics of virtual instrumentation concept and dataflow programming
CO2:Understand various functions available in Lab VIEW for engineering applications
CO3:Design projects using the functions available in Lab VIEW
CO4:Understand the interfacing of DAQ devices and customized user designed hardware with Lab VIEW
CO5:Write the Certified LabVIEW Associate Developer (CLAD) exam, administered by National Instruments, for the certification and leading to placements in core companies

<b>References</b>
1. <a href="http://www.ni.com/en-in/innovations">www.ni.com/en-in/innovations</a> .

<b>COURSE COORDINATOR:</b>	<b>RESOURCE PERSON:</b>
Mr.L.Hari Prasad, Assistant Professor ECE Dept. Anurag Engineering college	Mr.K.Srinadh Assistant Professor, ECE Dept. Anurag Engineering college



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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 20-11-2017

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2017-18.


Name of the Value-added course: INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 04-11-2017

Course coordinator:  
Mr.L.Hari Prasad,  
Assistant Professor,  
ECE Dept.,  
Mobile No: 97050 29143

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 23-11-2017

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to LABVIEW Laboratory Feasibility" is scheduled from 24<sup>th</sup> Nov 2017 to 17<sup>th</sup> Mar 2018.

Venue: Block-D: 408-SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr.K.Srinadh,  
Assistant Professor  
ECE Dept,  
Mobile: 7306106104

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### Oracle Java Programming

This course helps students to gain advance Java programming skills. Students will be trained to design object-oriented applications for real time problems with hands-on, engaging activities.

#### Purpose the Course

Students who wish to extend their programming experience in Java and develop more complex Java applications .This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.


#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Fundamental knowledge of object-oriented concepts, terminology, and syntax, and the steps required to create basic Java programs.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	40	90	9.30AM - 4:00PM	-

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives

This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.

Students will design object-oriented applications with Java and will create Java programs using hands-on, engaging activities.


### Course Outline

Module 1	<b>Class Design and Exceptions:</b> Java Class Design – Interfaces, Abstract Classes, Exceptions and Assertions.	5 Hours
Module 2	<b>Data Structures:</b> Generics , Collections , Sorting and Searching	8 Hours
Module 3	<b>Strings, Regular Expressions, and Recursion:</b> String Processing, Regular Expressions, Recursion. <b>Input and Output:</b> Basics of Input and Output , Input and Output Fundamentals , Deploying an Application	5 Hours
Module 4	<b>JDBC:</b> Introduction , JDBC Basics	7 Hours
Module 5	<b>Java Memory and the JVM:</b> Introduction to JVM Architecture, Java Memory Structure. <b>class File and the JDK :</b> JDK Tools, Class File <b>Bytecode and Class Loader :</b> Java Bytecode , ClassLoader	5 Hours

### Course Outcomes

After completion of this course the students will be able to

1. Students can gain advance Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
2. George Reese, Database Programming with JDBC & JAVA, Second Edition, O'Reilly Media
3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education

Course Coordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	K.Ramesh Technical Trainer TASK

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 06-07-2017

### CIRCULAR


All the IV B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with TASK is going to offer the following value added courses for the academic year 2017-18.

**Name of the Value Added Course:** Oracle Java Programming  
**Eligible Branches:** CSE

**Course Coordinator:**  
Ch.Vidya Sagar,  
Training and Placement Officer,  
Mobile No: 9553122279

  
HOD

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 10-07-2017

### CIRCULAR

All the IV B.Tech I Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Oracle Java Programming**" is scheduled from 11 July 2017 to 15 July 2017.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 4:00 PM

**Resource Person Details:**

K.Ramesh,  
TASK.

  
HOD


**Encl:** List of students

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### Oracle Database Design

This course engages students to analyze complex business scenarios and create a data model—a conceptual representation of an organization's information. Participants implement their database design by creating a physical database using SQL. Basic SQL syntax and the rules for constructing valid SQL statements are reviewed. This course culminates with a project that challenges students to design, implement, and demonstrate a database solution for a business or organization.

#### Purpose the Course

Students who wish to learn the techniques and tools to design, build and extract information from a database. Students who possess basic mathematical, logical, and analytical problem-solving skills. Novice programmers, as well as those at advanced levels, to learning the SQL Programming language to an advanced level

#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

General knowledge of databases and query activity.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	60	130	9.30AM - 4:00PM	-

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives

This course of study teaches students to analyze complex business scenarios, design and create data models, and create databases using SQL. Oracle SQL Developer Data Modeler and Application Express (APEX) are utilized to provide practical, hands-on, engaging activities. Leveraging project-based learning techniques, students will create and work with projects which challenge them to design, implement, and demonstrate a database solution for a business or organization.

### Course Outline

Module 1	<b>Introduction:</b> Data vs. Information ,History of the Database, Major Transformations in Computing Entities and Attributes ,Conceptual and Physical Models ,Entities, Instances, Attributes, and Identifiers ,Entity Relationship Modelling and ERDs.	5 Hours
Module 2	<b>Relationship Basics:</b> Identifying Relationships, ER Diagramming Conventions, Speaking ERDish & Drawing Relationships, Matrix Diagrams Super/Sub Types and Business Rules, Super types and Sub types, Documenting Business Rules. <b>Relationship Fundamentals:</b> Relationship Transferability, Relationship Types, Resolving Many-to-Many Relationships, Understanding CRUD Requirements	5 Hours
Module 3	<b>UIDs and Normalization:</b> Artificial, Composite, and Secondary UIDs ,Normalization and First Normal Form ,Second Normal Form ; Third Normal Form Arcs, Hierarchies, and Recursive Modeling ,Arcs ,Hierarchies and Recursive Relationships Changes and Historical Modeling, Modeling Historical Data ,Modeling Change: Time ,Modeling Change: Price , Drawing Conventions for Readability	5 Hours
Module 4	<b>Mapping:</b> Introduction to Relational Database Concepts, Basic Mapping: The Transformation Process, Relationship Mapping, Subtype Mapping. <b>Creating Database Projects :</b> System Development Life Cycle, Project Overview and Getting Started ,Presentation Project Management , Final Presentation Components	10 Hours

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Module 5	Programming with SQL: SELECT, WHERE, ORDER BY, Functions, JOINS, Subqueries, DML, DDL, Views.	5 Hours
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### Course Outcomes

After completion of this course the students will be able to

1. Students can analyze complex business scenarios, design and create data models, and create databases using SQL.
2. Oracle SQL Developer Data Modeler and Application Express (APEX) are utilized to provide practical, hands-on, engaging activities. Students can create Java programs using hands-on, engaging activities.
3. Students can create and work with projects which challenge them to design, implement, and demonstrate a database solution for a business or organization.

### Reference Books

1. Abraham Silberschatz, Henry F. Korth, S. Sudarshan, Database System Concepts, Sixth Edition, Tata Mc Graw- Hill 2006.
2. Raghu Rama Kirshna, Johannes Gchrke, Database Management System, Third Edition, TATA MC Graw Hill, 2003.
3. Peter Rob & Carlos Coronel, Data base Systems design, Implementation and Management, 7th Edition, 2007.
4. Ramez Elmasri, Shamkanth B. Navrate, Fundamentals of Database Systems, Pearson Education, 2008.
5. C.J. Date ,Introduction to Database Systems, Pearson Education

Course Coordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	P.Vamshidhar Reddy Technical Trainer TASK

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 10-07-2017

### CIRCULAR


All the III B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with TASK is going to offer the following value added courses for the academic year 2017-18.

**Name of the Value Added Course:** Oracle database Design  
**Eligible Branches:** CSE

**Course Coordinator:**  
Ch.Vidya Sagar,  
Training and Placement Officer,  
Mobile No: 9553122279

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 17-07-2017

### CIRCULAR

All the III B.Tech I Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Oracle Database Design**" is scheduled from 18 July 2017 to 22 July 2017.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 4:00 PM

**Resource Person Details:**

P.Vamshidhar Reddy,

Technical Trainer ,

TASK,

Mobile:8886139700

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

#### SALESFORCE

This Course helps to understand the basic concepts of Salesforce. Now Salesforce.com is a number one on-demand CRM in the market. It runs on the force.com platform, it reduces development cost, and we can deliver the application in a short time. It is cloud computing technology. It is available on the cloud, no need to install any software and no hardware required. We can customize/develop applications, or you can buy applications on demand from app-exchange. App Exchange is a marketplace to sell our custom applications and to buy applications from app exchange.

#### Purpose the Course


Salesforce is a cloud computing service as a software (SaaS) company that specializes in customer relationship management (CRM). Salesforce's services allow businesses to use cloud technology to better connect with customers, partners and potential customers. Students will have hands on exposure to develop applications using App Exchange.

#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Knowledge of cloud computing is essential to understand the environment and its architecture. Knowledge of databases is essential to understand how the data is stored, retrieved and manipulated.

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
34	40	60	9.30AM - 5:00PM	-

### Course Objectives

Salesforce started as a cloud based solution for CRM. CRM stands for Customer Relationship Management. It involves managing all aspects of relationship between an organization and its customers. For example, the contact details of the customer, the deals that are in progress or already completed, the support requests from a customer or a new lead from a new customer. Beyond the customer related information, it also involves storing and managing the details of the people and the concerned department from the seller organization that is managing the customer's account and needs. This makes it easy to manage and enhance the relationship with the customer and hence better growth for the organization. Students will learn how to customize software for specific business needs.

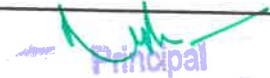
### Course Outline

Module	Content	Hours
Module 1	Salesforce-Architecture, Environment, Sales cloud, Service cloud, Navigating setup.	8 Hours
Module 2	Salesforce objects	8 Hours
Module 3	Data security	5 Hours
Module 4	Process Builder	8 Hours
Module 5	Visualforce	5 Hours

### Course Outcomes

After completion of this course the students will be able to

1. Students can able to learn how to customize software for specific business needs.
2. Students can able to manage the organization's data focused on customer and sales details.

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Reference Books

1. Salesforce CRM-The Definitive Admin Book, Fourth Edition by Paul Goodey
2. Practical Salesforce.com Development Without Code: Customizing Salesforce on the Force.com Platform 1st Edition by Philip Weinmeister.
3. Salesforce Lightning Reporting and Dashboards: Create, customize, and manage your Salesforce reports and dashboards in depth with Lightning Experience by Johan Yu

Course Coordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	Rohith Kapoor Salesforce Certified Instructor TASK

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 01-12-2017

### CIRCULAR


All the III B.Tech II Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with TASK is going to offer the following value added courses for the academic year 2017-18.

**Name of the Value Added Course:** Salesforce Essentials for Business Specialists  
**Eligible Branches:** CSE

**Course Coordinator:**  
Ch.Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 09-12-2017

### CIRCULAR

All the III B.Tech II Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Salesforce Essentials for Business Specialists**" is scheduled from 11 December 2017 to 15 Decemeber 2017.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 5:00 PM

**Resource Person Details:**

Rohit Kapoor,

Salesforce Certified Instructor,

Mobile:8860986348.

  
HOD


**Encl:** List of students

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### VALUE ADDED/SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### SOFT SKILLS

Soft skills are a cluster of productive personality traits that characterize one's relationships in a milieu.

#### Purpose the Course

Soft skills training gives benefits for Empowerment, Mastery & Purpose ability to communicate effectively with co-workers, employers, clients and customers, friends and family members etc.


#### Eligibility

MBA

#### Prerequisites

- Leadership Skills,
- Work Ethics.
- Teamwork.
- Time Management.
- Critical Thinking.
- Adaptability.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timmings	Fees
30	20	60	4:00 pm to 5:00 pm	NILL

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### Course Objectives

Each student has a realistic perspective of work and work expectations, to help formulate problem solving skills, to guide students in making appropriate and responsible decisions, to create a desire to fulfill individual goals.

### Course Outline

Course Outline		
Module 1	<ol style="list-style-type: none"><li>1. Introduction to Soft Skills &amp; Hard Skills</li><li>2. Break the ice berg – FEAR</li><li>3. Self Development – Etiquette &amp; Manners</li></ol>	6 Hours
Module 2	<ol style="list-style-type: none"><li>1. Communication Skills</li><li>2. Body Language</li><li>3. Priority Management &amp; Time Management</li></ol>	6 Hours
Module 3	<ol style="list-style-type: none"><li>1. Positive Attitude &amp; Self Confidence</li><li>2. Motivation Skills &amp; Personality</li><li>3. Development</li></ol>	6 Hours
Module 4	<ol style="list-style-type: none"><li>1. Career Planning</li><li>2. Resume Building</li><li>3. Goal Setting</li></ol>	6 Hours

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

Module 5	<ol style="list-style-type: none"><li>1. Psychometric Test</li><li>2. Interviewing Skills</li><li>3. Group Discussion (GD)</li></ol>	6 Hours
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### Reference Books

1. "Leading with GRIT: Inspiring Action and Accountability with Generosity, Respect, Integrity, and Truth" By **Laurie Sudbrink**
2. "Stronger: Develop the Resilience You Need to Succeed" By **George S. Everly Jr, and Douglas A. Strouse**
3. Extreme Ownership: How U.S. Navy SEALs Lead and Win" By **Jocko Willink and Leif Babin**

Course Coordinator	Resource Person
G.Varma Asst. professor MBA Department Anurag Engineering College	CH.Ramesh Asst. professor MBA Department Anurag Engineering College

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 09-01-2018

### CIRCULAR

All the II MBA II Semester students are hereby informed that the department of Master of Business Administration is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: SOFT SKILLS

Eligible branches: MBA

Interested students can enrol their names with the course coordinator on or before 27-01-2018

Course coordinator:

G.Varma

Asst. Professor,

MBA Dept.,

Mobile No: 9989382096


  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

DATE: 10-02-2018

### CIRCULAR

All the II MBA II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "SOFT SKILLS" is scheduled from 12-02-2018 to 03-07-2018.

Venue: A-308, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

CH.Ramesh,

Asst. Professor,

MBA Department.

Mobile: 9553122280

  
HOB


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## DEPARTMENT OF H&S

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES, A.Y.: 2017-18

#### About the Course

##### CAMPUS RECRUITMENT TRAINING-MATHEMATICS

The course is designed for the students who are appearing in campus placements to improve their logical thinking and aptitude levels which are key points in campus placements.

#### Purpose of the Course

The purpose of the course is to improve logical thinking and aptitude levels of the students and make them industry ready.

#### Eligibility

All the B.Tech pursuing students are eligible for this course.

#### Prerequisites

Students should have basic arithmetic skills.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
35	20	-		-

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## DEPARTMENT OF H&S

A.Y.: 2017-18

DATE: 12.06.2017

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the department of H&S is going to offer the following value added courses for the academic year 2017-18.

Name of the Value added course: CAMPUS RECRUITMENT TRAINING-MATHEMATICS  
Eligible branches: All Branches

Interested students can enroll their names with the course coordinator on or before 19.06.2017

Course coordinator:

T. Naga Raju

Asst. Professor,

H&S Dept.,

Mobile No: 9491406080

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## DEPARTMENT OF H&S

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A.Y.: 2017-18

DATE: 20.06.2017

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "CAMPUS RECRUITMENT TRAINING-MATHEMATICS" is scheduled from **22 June 2017 to 16 Oct 2017**.

#### Resource Person Details:

Ch Baskar Reddy  
Asst. Professor,  
H&S Department,  
Mobile: 9985072716

N Koti Reddy  
Asst. Professor,  
H&S Department,  
Mobile: 8106520434

  
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## LIST OF VALUE ADDED COURSES OFFERED IN THE A.Y. 2018 – 19

S. No.	Course Code	Name of Value Added Course Offered
1	CE18VA01	Safety In Construction
2	CE18VA04	Clean and Green Campus Training
3	EE18VA01	Introduction to PSPICE
4	EE18VA02	Introduction to MATLAB
5	ME18VA01	ANSYS
6	ME18VA02	CREO
7	EC18VA01	Introduction to Mentor graphics Design Tools
8	EC18VA02	Introduction to LAB View
9	CS18VA02	Oracle Java Fundamentals
10	CS18VA03	Infosys Campus Connect Foundation Program(Python)
11	CS18VA01	Programming for problem solving
12	CS18VA02	Oracle Java fundamentals
13	MB18VA02	Personality Development
14	HS17VASS	CRT-E
15	HS17VAQR	CRT-M

  
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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### SAFETY IN CONSTRUCTION

Students are given training in regarding safety in construction local and international standards and laws associated with health and safety in construction activities, identification and controlling of workplace dangers and hazards and practical application of knowledge in real time situations. To join and study this course, no previous health and safety knowledge is required although it is advisable that candidates should have basic understanding of construction activities.

#### Purpose the Course

To improve professional skills.

#### Eligibility

III B TECH (CE) Students.

#### Prerequisites

Knowledge on Building materials and construction.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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Ananthagiri (V&M), Suryapet (Dt). Pin: 508 206.

## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand safety rules, regulations and norms which will help them in construction site. How to handle and maintains of tools and machinery. Also by learning about SHE, carefully planned, organised, monitored and controlled.

### Course Outline

Course Outline		
Module 1	Machine Operation And Guarding	5 Hours
Module 2	Materials Handling And Storage Of Materials	5 Hours
Module 3	Construction Industry	5 Hours
Module 4	Working At Heights	7 Hours
Module 5	Noise And Vibration	8 Hours

### COURSE OUTCOMES:

After successfully completion of this course, the student will be able to


CO 1: Know how to Handle and Protect the Machine and himself while working on it

CO 2: Know the Knowledge of lifting and carrying the materials and also maintenance of mechanical material handling equipment

CO 3: Understand the knowledge of safety rules and regulations of working at construction industry

CO 4: Understand the knowledge about working at all stages in different heights at construction site

CO 5: Know the knowledge of different types of noise and vibration, its causes, effects and controlling measures.

  
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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 05-11-2018

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: Safety in Construction  
Eligible branches: CE


Interested students can enrol their names with the course coordinator on or before 08-11-2018

Course coordinator:  
P. Sruthi.  
Asst. Professor,  
CE Dept.,  
Mobile No: 9110714939

*C. M. Reddy*  
5/11/18  
HOD

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---

## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 19-11-2018

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Safety In Construction" is scheduled from 20 Nov 2018 to 08 March 2019.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

**Resource Person Details:**

G. Narendar

Asst. Professor,

CE Department,

Mobile: 9492909685

HOD

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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### CLEAN AND GREEN CAMPUS TRAINING

Students are introduced to all major aspects of Providing optimal clean, green and smart learning space continues to be the aspiration of almost each and every educational institute. However, there are several barriers such as lack of availability of knowledge. How about the technologies, limited expertise of the people in the institutes, time & budgetary constraints and more. On the other hand, there are several tangible and intangible benefits of infusing and expanding clean, green and smart learning spaces in colleges, institutes and universities.

### Purpose the Course

To improve social developmental skills and responsible for social activities.

### Eligibility

III B TECH (CE) Students.

### Prerequisites

Knowledge on Environmental Studies.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand Rediscovered key attributes of Clean & Green Campus. Conceptualised Innovative ways to internalise and realise Clean and green campus. Incorporated holistic approach to green campus. Strengthened collaborations with clean and green technology industries, institutions and professional bodies Learned to leverage on national critical campaigns & issues, technology development and Personal Experience.

### Course Outline

Course Outline		
Module 1 -	Concept, Benefits & Challenges in adopting Clean, Green Campus.	5 Hours
Module 2	Advanced Technology in Renewable Energy Resources	5 Hours
Module 3	Water Treatment System	5 Hours
Module 4	Hazardous Management and Recycling System	7 Hours
Module 5	Group Discussion	8 Hours

### COURSE OUTCOMES:

At the end of course student will learn about to

CO 1: Basics in Clean, Green Campus.

CO 2: Advanced Technology in Renewable Energy Resources.

CO 3: Understand How to use the Water Treatment System.

CO 4: Various Hazardous Management and Recycling Systems.

CO 5: Improve Communication and Interaction skills.

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## DEPARTMENT OF CIVIL ENGINEERING

Course Coordinator	Resource Person
G. Narendar Asst. professor CE Department Anurag Engineering College	D. Sravanthi Asst. professor CE Department Anurag Engineering College

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## DEPARTMENT OF CIVIL ENGINEERING

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DATE: 11-6-2018

### CIRCULAR

All the III B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: Clean and Green Campus Training  
Eligible branches: CE

Interested students can enroll their names with the course coordinator on or before 14-6-2018.

Course coordinator:  
G.NARENDAR  
Asst. Professor,  
CE Dept.,  
Mobile No: 9492909685

*C. M. Reddy*  
HOD 11/6/2018

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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 19-06-2018

### CIRCULAR

All the III B. Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Clean and Green Campus Training" is scheduled from 20 June 2018 to 04 Oct 2018.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

#### Resource Person Details:

D. Sravanthi

Asst. Professor,

CE Department,

Mobile: 8985113144

*C. M. Reddy*  
HOD

Encl: List of students

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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to PSPICE (Personal Computer Simulation Program with Integrated Circuit Emphasis)

### About The Course

This course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters.

### Purpose of the Course

This course is focused on engineers, professors and researchers in power systems and power electronics, interested in exploring the modern tools available for modeling, design and analysis.

### Eligibility

II B.Tech I Semester (Electrical and Electronics Engineering)

### Prerequisites

Basic schematic entry with ORCAD Capture

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To use techniques of creating PSPICE Designs.
- To Run DC bias, DC sweep, AC sweep, mesh and nodal analysis. Design of circuits for power electronics converters.
- To learn how to edit a stimulus and run a parametric analysis
- To create, edit and implement models, create sub-circuits and create parts for simulation.
- To analyze the single phase converters with RLE loads.

### Course Outlines

MODULE	Topic	Hours
MODULE 1	Introduction to PSPICE	6 Hours
MODULE 2	Preparing a design for simulation	6 Hours
MODULE 3	Creating and Editing Models	6 Hours
MODULE 4	Creating Parts for Models	6 Hours
MODULE 5	Simulation Examples	6 Hours

### Course Outcomes

After completing this course, student will be able to:

- Use techniques of creating PSPICE Designs.
- Run DC bias, DC sweep, AC sweep, mesh and nodal analysis and able to design power electronics converter circuits for simulation.
- Edit a stimulus and run a parametric analysis
- Create, edit and implement models, create sub-circuits and create parts for simulation.
- Analyze the single phase converters with RLE loads.

### Reference Books

1. Muhammad H. Rashid, "Spice for Circuits and Electronics Using Pspice", 2<sup>nd</sup> Edition, Prentice Hall India, 1990.
2. Muhammad H. Rashid, "SPICE for Power Electronics and Electric Power" CRC Press, 3<sup>rd</sup> Edition, 2012.

<b>Course Coordinator</b>	<b>Resource Person</b>
Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College	T.Raghu Asst. professor EEE Department Anurag Engineering College

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 28.06.2018

### CIRCULAR


All the II B.Tech I.Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2018-19.

**Name of the Value added-course:** Introduction to PSPICE  
**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 30.06.2018

**Course coordinator:**

J.Srinivas Rao  
Asst.Professor,  
EEE Dept.,  
Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 02.07.2018

### CIRCULAR

All the II B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to PSPICE" is scheduled from 03.07.2018 to 27.10.2018.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

T.Raghu,

Asst. Professor,

EEE Department,

Mobile:9959692070

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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to MATLAB

### About The Course

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include:

- Math and computation
- Algorithm development
- Modeling, simulation, and prototyping
- Data analysis, exploration, and visualization
- Scientific and engineering graphics
- Application development, including Graphical User Interface building

### Purpose of the Course

A proprietary programming language developed by MathWorks, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, Java, Fortran and Python.

### Eligibility

III B.Tech II Semester (Electrical and Electronics Engineering)

### Prerequisites

Once you get to know the representation of data and other things used while programming you can easily pull it off. The Prerequisites are

- Basic knowledge of Programming.
- Know the need of MAT LAB and its implementation.
- The most important aspect is the syntax.
- Get to know the need of different tools used in it.
- Know the applications of it in different fields

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To familiarize the student in introducing and exploring MATLAB software.
- To enable the student on how to approach for solving Engineering problems using simulation tools.
- To prepare the students to use MATLAB in their project works.
- To provide a foundation in use of this softwares for real time applications.
- To familiarize the student in modeling the networks by using MATLAB software.

### Course Outlines

MODULE 1	MATLAB Window Environment and the Base Program: Introduction to MATLAB family of programs Brief introduction to MATLAB base program in an interactive "hands on" tutorial style Brief introduction to various tool boxes related to electrical and electronics engineering design and applications.	6 Hours
MODULE 2	Simulink: Building simple Simulink simulations. Running Simulink simulation to predict a system's behaviour. Feedback System Simulation: Presenting MATLAB functions to carryout block diagram manipulations, followed by time-response analysis. Using Simulink simulation for time-response analysis.	6 Hours
MODULE 3	Time Response Characteristics and LTI Viewer: Analysis of the system behavior to changes in input reference signals or disturbances using steady-state and transient performance indices. Brief introduction to LTI Viewer, and time response analysis using this GUI tool. Stability Analysis on Root Locus Plots: Stability analysis in state space. Stability analysis using root locus plots.	6 Hours
MODULE 4	Root Locus Design and SISO Design Tools: Design using root locus plots. Brief introduction to SISO Design Tool, and root locus design using this GUI tool. Stability Analysis on Bode / Nyquist Plots: Introducing MATLAB commands for Bode and Nyquist plots. Stability analysis using these plots.	6 Hours
MODULE 5	Simulink models: Simulink models for single phase and three phase full converters. Simulink models for single phase and three phase half controlled converters.	6 Hours

	Simulink models for single area load frequency control problem.	
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<b>Course Outcomes</b>	
●	Student gets familiarize in using the MATLAB software.
●	Student gain the knowledge for solving Engineering problems using simulation tools.
●	Student will be able to do their project works by using MATLAB software.
●	Student develops awareness in use of this software for real time applications.
●	Student will be able to analyze and modeling the networks by using MATLAB software.

<b>Reference Books</b>	
1.	Modeling and Simulation using MATLAB - Simulink, 2ed
2.	Getting Started with MATLAB: A Quick Introduction for Scientists & Engineers
3.	MATLAB: An Introduction with Applications, 4ed
4.	MATLAB Programming for Engineers by Stephen J. Chapman
5.	Introduction to MATLAB for Engineers by William J. Palm III
6.	An Introduction to Scientific Computing: Twelve Computational Projects Solved with MATLAB by Ionut Danaila

<b>Course Coordinator</b>	<b>Resource Person</b>
T.Raghu Asst. professor EEE Department Anurag Engineering College	Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College

  
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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 05.11.2018

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2018-19.

**Name of the Value added course:** Introduction to MATLAB  
**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 07.11.2018

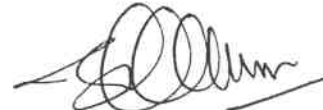
**Course coordinator:**

T.Raghu

Asst. Professor,

EEE Dept.,

Mobile No: 9848044209



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

---

DATE: 08.11.2018

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to MATLAB" is scheduled from 09.11.2018 to 08.03.2019.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

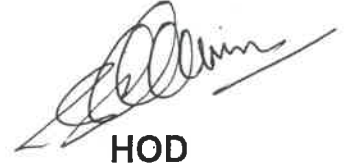
**Resource Person Details:**

J.Srinivas Rao

Asst. Professor,

EEE Department,

Mobile:9848044209



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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ANSYS

To learn the basis of AUTOCAD commands, 3D Modelling and Analysis of various problems

#### Purpose the Course

To generate the engineering drawings, creates the 3D modelling and analyses the various engineering component problems.

#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

3D Modelling.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to understand and handle design problems in a systematic manner  
To be able to apply CAD in real life Applications.  
To be able to understand the 3D Design Components.  
To be able to apply Stress Analysis on Various Beams.  
To be able to apply Stress Analysis on Various Parts.

### Course Outcomes

CO 1: Ability to understand the basics of AUTOCAD.  
CO 2: To get Practice on 2D and Isometric Diagrams.  
CO 3: To get Practice on 3D Components.  
CO 4: To get Practice on stress analysis of Various Beams.  
CO 5: To get practice on Truss Analysis.

### Course Outline

Module	Topic	Hours
Module 1	Basics of AUTOCAD	6hours
Module 2	Practice on 2D and isometric Diagrams	6hours
Module 3	Introduction to CREO Practice on 3D Modelling	6hours
Module 4	Introduction to ANSYS	6hours
Module 5	Practice on various engineering components Analysis.	6hours

### Reference Books

1. AutoCAD reference guide by Chetan Prakasan Published by CCAD Solution training and sense
2. PTC Creo Parametric by Prof. Shan Tickoo.
3. Engineering analysis with ansys software by Tadeusz stolarski s. yoshimoto, yasuniro narkasone.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 05-11-2018

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: ANSYS  
Eligible Branches: Mechanical Engineering


Interested students can enrol their names with the course coordinator on or before 08-11-2018

Course coordinator:  
E Sadanandam  
Assistant Professor  
Department of Mechanical Engineering  
Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 12-11-2018

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "ANSYS" is scheduled from 13-11-2018 to 08-03-2019.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM ((Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

CREO

To Learn the Sketching, 3D Modelling and assembling of Mechanical Components

#### Purpose the Course

To Generate the Engineering drawings and creates the 3D Modelling.

#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

AutoCAD Commands.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to apply CAD in real life applications.  
To be able to apply Sketching of 2D Commands.  
To be able to apply Conversion of Diagrams.  
To be able to apply Modelling Techniques.  
To be able to apply Assembling Components.

### Course Outcomes

CO 1: Ability to understand the basics of CREO  
CO 2: Ability to understand the sketching of 2D.  
CO 3: To get Practice converting from 2D to 3D.  
CO 4: To get Practice on 3D modelling  
CO 5: To get practice on assembling of 3D components.

### Course Outline

Module	Topic	Hours
Module 1	Introduction to CREO	6hours
Module 2	Practice on Sketching	6hours
Module 3	Conversion of 2D to 3D	6hours
Module 4	Practice on 3D Modelling	6hours
Module 5	Practice on assembly	6hours

### Reference Books

1.PTC Creo Parametric by Prof.Shan Tickoo.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

---

DATE: 11-06-2018

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: PTC CREO

Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 14-06-2018

Course coordinator:

E Sadanandam

Assistant Professor.

Department of Mechanical Engineering

Mobile No: 9985498008

  
HOD

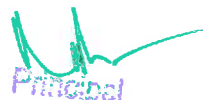
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Pin: 508.206.

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 19-06-2018

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PTC CREO" is scheduled from 20-06-2018 to 04-10-2018.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM ((Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

HOD

Encl: List of students

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

#### Introduction to Mentor graphics Design Tools

#### About The Course

Fundamental of Designers can develop an executable functional specification that documents the exact behavior of all the components and their interfaces Designers can make decisions about cost, performance, power, and area earlier in the design process Designers can create tools which automatically manipulate the design for verification, synthesis, optimization, etc. Hardware description languages are an essential part of modern digital design.

#### Purpose The Course

This course is focused to engineers, to Digital Design concepts & implementation.

#### Eligibility

IV B.Tech (Electronics and Communication Engineering)


#### Prerequisites

Knowledge on schematic, simulation-calbre and analog and digital electronics, VLSI.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

#### Course Objectives

- Learn the mentor graphic tool or equivalent tool for design the schematic and simulation of all gates.
- To understand the concept of digital system design method through practical domain.
- Design combinational and sequential circuits
- HDLs help manage complexity inherent in modern designs.
- Analyze and layout design of CMOS circuits in micron and submicron level using any platform.

  
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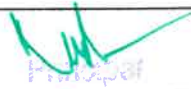


Course Outlines		
<b>MODULE 1</b>	Introduction to mentor graphics <ul style="list-style-type: none"> <li>• Mentor graphics resources</li> <li>• Different Modeling styles used in Verilog.</li> <li>• Mentor graphics Application builder</li> <li>• DUT verification by simulation using Directed Test bench</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual instruments <ul style="list-style-type: none"> <li>• Introduction to back end</li> <li>• FPGA Architecture basics</li> <li>• ASIC &amp; FPGA Design flow</li> <li>• Synthesis and FPGA Implementation</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	Mentor graphics Environment <ul style="list-style-type: none"> <li>•Verifying the HDL codes by writing Test benches using ISim .</li> <li>•Synthesis of HDL codes of Xilinx FPGA Kits</li> <li>•IPCoregen flow</li> <li>•mentor graphics tool functions description</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Build the schematic and simulation <ul style="list-style-type: none"> <li>• Full Adder,</li> <li>• Ripple-carry Adder.</li> <li>•DFF. Counters. Shifters.</li> <li>•ALU, RAM/ROM etc..</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Layout design <ul style="list-style-type: none"> <li>• DRC,PEX</li> <li>• FPGA development kits</li> <li>• ASIC v/s FPGA design flows</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1:Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary For engineering practice.
CO2: Use computer-aided design tools to synthesize, map, place, routing, and download the digital Designs on the FPGA board.
CO3: Model digital systems in VHDL and Partition a digital system into different subsystems..
CO4: Transfer a design from a version possible to simulate to a version possible to synthesize..
CO5: Learn the basics of Fabrication and Layout of CMOS Integrated Circuits..

References
1. <a href="https://www.mentor.com/products/ic-manufacturing/">https://www.mentor.com/products/ic-manufacturing/</a>
2. <a href="https://www.mentor.com/training">https://www.mentor.com/training</a>

COURSE COORDINATOR:	RESOURCE PERSON:
Dr.B.Rajan Professor, ECE Dept. Anurag Engineering college	,Mrs.V.Kalyani Assistant Professor, ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DATE: 11-06-2018

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2018-2019.

Name of the Value-added course: Introduction to Mentor graphics Design Tools.  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 13-06-2018

Course coordinator:  
Dr. B. Rajan,  
Professor,  
ECE Dept.  
Mobile No: 79971 30343

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE:14-06-2018

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to Mentor graphics Design Tools" is scheduled from 15<sup>th</sup> Jun 2018 to 04<sup>th</sup> Oct 2019.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mrs.V.Kalyani  
Assistant Professor,  
ECE Dept,  
Mobile: 9603107049

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Encl: List of students

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

### INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY

#### About The Course

This course describes the LABVIEW graphical programming environment and techniques for building applications in LABVIEW, such as test and measurement, data acquisition, instrument control, data logging, measurement analysis, and report generation applications.

#### Purpose The Course

This course is to learn about LABVIEW programming features, including the LABVIEW user interface and programming workspaces, and the LABVIEW palettes and tools. This course does not include specific information about each palette, tool, menu, dialog box, control, or built-in VI or function.

#### Eligibility

III B.Tech (Electronics And Communication Engineering)

#### Prerequisites

Knowledge on logical designing, signals and electronic analysis.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

#### Course Objectives

- Introduction to LABVIEW
- Introduction to Virtual Instruments
- LABVIEW Environment
- Building the Front Panel and Building the Block Diagram
- Running and Debugging VIs and Creating VIs and Sub VIs

  
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Course Outlines		
<b>MODULE 1</b>	Introduction To Labview <ul style="list-style-type: none"> <li>● LABVIEW Resources</li> <li>● LABVIEW Measurements</li> <li>● LABVIEW Application Builder and</li> <li>● LABVIEW Template VIs</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual Instruments <ul style="list-style-type: none"> <li>● Introduction to Front Panel</li> <li>● Introduction to Nodes, Wires and Structures</li> <li>● Introduction to Icon and Connector Pane</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	LABVIEW Environment <ul style="list-style-type: none"> <li>● Controls and Functions palettes</li> <li>● Navigating the Controls and Functions Palettes</li> <li>● Tools Palette</li> <li>● Menus and the Toolbar</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Building the Front Panel and Building the Block Diagrams <ul style="list-style-type: none"> <li>● Configuring Front Panel Objects and Changing Controls to Indicators and Indicators to Controls</li> <li>● Replacing Front Panel Objects and Configuring the Front Panel</li> <li>● Relationship between Front Panel Objects and Block Diagram Terminals</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Running and Debugging VIs and Creating VIs and Sub Vis <ul style="list-style-type: none"> <li>● Running Vis and Configuring How a VI Runs</li> <li>● Correcting Broken VIs</li> <li>● Finding Causes for Broken Vis and Common Causes of Broken VIs</li> <li>● Debugging Techniques and Execution Highlighting</li> <li>● Disabling Debugging Tools</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1: Understand the basics of virtual instrumentation concept and dataflow programming
CO2: Understand various functions available in Lab VIEW for engineering applications
CO3: Design projects using the functions available in Lab VIEW
CO4: Understand the interfacing of DAQ devices and customized user designed hardware with Lab VIEW
CO5: Write the Certified LabVIEW Associate Developer (CLAD) exam, administered by National Instruments, for the certification and leading to placements in core companies

References
1. <a href="http://www.ni.com/en-in/innovations">www.ni.com/en-in/innovations</a> .

COURSE COORDINATOR:	RESOURCE PERSON:
Dr.B.Rajan, Professor ECE Dept. Anurag Engineering college	Mr.K.Srinadh Assistant Professor, ECE Dept. Anurag Engineering college



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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 05-11-2018

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2018-19.

Name of the Value-added course: INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 08-11-2018

Course coordinator:  
Dr. B. Rajan,  
Professor,  
ECE Dept.  
Mobile No: 79971 30343

A handwritten signature in black ink, appearing to be 'V. S. R.', is written above the title 'HOD'.

**HOD**

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A handwritten signature in green ink is written above the text 'Principal'.  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 08-11-2018

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to LABVIEW Laboratory Feasibility" is scheduled from 09<sup>th</sup> Nov 2018 to 08<sup>th</sup> Mar 2019.

Venue: Block-D: 408-SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:


Mr.K.Srinadh,  
Assistant Professor  
ECE Dept,  
Mobile: 7306106104.

  
HOD

Encl: List of students

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### About the Course

#### ORACLE JAVA FUNDAMENTALS

This course engages students with little or no programming experience to create Java programs. Participants are introduced to object-oriented programming concepts, terminology, and syntax, and the steps required to create basic Java programs using the Alice, Greenfoot, and Eclipse interactive development environments. Hand-on practices figure prominently throughout this course so students can experience firsthand the power of computer programming

### Purpose the Course

Students who wish to extend their programming experience in Java and develop more complex Java applications. This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.

### Eligibility

Any pursuing graduate (ECE & CSE)

### Prerequisites

- Basic understanding of at least one programming language
- † The ability to follow software installation instructions and install Alice, Greenfoot, and Eclipse on a computer

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	100	9.30AM - 4:00PM	-

### Course Objectives

- This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.
- Students will design animations, games and applications using fun and engaging tools.

### Course Outline

Module 1	<b>Introduction:</b> Introduction, <b>Alice3:</b> Getting Started with Alice 3 , Add and Position Objects ,Procedures and Arguments ,Rotation and Randomization, Declare Procedures , Control Statements, Functions , IF and WHILE Control Structures , Expressions , Variables ,Keyboard Controls ,Develop a Complete Animation ,Java Variables and Data Types ,Java Methods and Classes	8 Hours
Module 2	<b>Greenfoot:</b> Getting Started With Greenfoot, Methods, Variables and Parameters , Source Code and Documentation, Developing and Testing an Application, Randomization and Constructors , Defined Methods ,Sound and Keyboard Control ,World Animating and Game End ,Abstraction, Loops, Variables, and Arrays	8 Hours
Module 3	<b>Java Basics:</b> Getting Started with Eclipse, Object and Driver Classes, Data Types and Operators, Strings.	4 Hours
Module 4	<b>Program Structure</b> :Using Scanner and Conditional Statements ,Using Program Control Statements Arrays and Exceptions , Arrays , Handling Errors	5 Hours
Module 5	<b>Java Classes</b> :Classes, Objects, and Methods , Parameters and Overloading Methods ,The Static Modifier and Nested Classes ,Inheritance ,Polymorphism	5 Hours

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Students can gain Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
2. George Reese, Database Programming with JDBC & JAVA, Second Edition, O'Reilly Media
3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education.

Course Co-ordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.K.Ramesh Technical Trainer TASK 2.P.Arun Reddy Technical Trainer TASK

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE:02-07-2018

### CIRCULAR

All the III & IV B. Tech students are hereby informed that the Department of Electronics and Communication Engineering in collaboration with TASK is going to offer the following value-added courses for the academic year 2018-2019.

Name of the Value-added course: Oracle Java Fundamentals.  
Eligible branches: ECE &CSE.

Course coordinator:  
Ch. Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 05-07-2018

### CIRCULAR


All the III & IV B. Tech students who have registered with **TASK** are hereby informed that the value-added course entitled "Oracle Java Fundamentals" is scheduled from 9<sup>th</sup> July 2018 to 13<sup>th</sup> Nov 2018.

Venue: Block-A: CSE SEMINAR HALL

Timings: 9:30 AM to 4:00 PM

**Resource Person Details:**

K.Ramesh,  
Technical Trainer,  
TASK.

  
HOD


**Encl:** List of students

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ◀INFOSYS CAMPUS CONNECT FOUNDATION PROGRAM 5.0

- Course is about to give sound knowledge in the introduction to both fundamental programming concepts and the Python Database Integration.
- Course provides students to prepare well in hands-on learning experience and now, students will also be assessed (on Programming & Database)
- Course allows students to focus on core functionality of the application by taking care of common programming tasks.
- Course is about to develop desktop GUI applications, websites and web applications.

#### Purpose of the Course

- Based upon this course the institution will Roll Out batches for Infosys Campus Connect


#### Eligibility

Any pursuing graduate.

#### Prerequisites

Basic C Programming knowledge

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
65	60	80	4:00PM- 5:00PM	-

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives

- To understand why Python is a useful scripting language for developers.
- To learn how to design and program Python applications.
- To learn how to use lists, tuples, and dictionaries in Python programs.
- To learn how to identify Python object types.
- To learn how to use indexing and slicing to access data in Python programs.
- To define the structure and components of a Python program.
- To learn how to design object oriented programs using python.
- To get exposure in python Database Integration and Advanced Database Concepts

### Course Outline

Course Outline		
Module 1	Programming Fundamentals	17 Hours
Module 2	Database Fundamentals	12 Hours
Module 3	Python Database Integration	6 Hours
Module 4	Object Oriented Programming using python	15 Hours
Module 5	Advanced database concepts	15 Hours

### Course Outcomes

1. To label the structure and components of a Python program.
2. Will make use of Database fundamentals.
3. Able to show how Python is integrated with Database.
4. Able to design object oriented programs with Python classes.
5. How to use Advanced Database Concepts

### Reference Books

1. Python Tutorial Release 3.7.0 Guido van Rossum

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Course Coordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.P.Sandeep Reddy Assoc. Prof. Department Of C.S.E Anurag Engineering College  2.P.Niranjana Kumar Assoc. Prof. Department Of C.S.E Anurag Engineering College  3.A.Naresh Kumar Asst. Prof. Department Of C.S.E Anurag Engineering College

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 29-11-2018

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with **INFOSYS** is going to offer the following value added courses for the academic year 2018-19.

**Name of the Value Added Course:** Infosys Campus Connect Foundation Programming 5.0

**Eligible Branches:** All Branches

**Course Coordinator:**

Ch.Vidya Sagar

Training and Placement Officer,

Mobile No: 9553122279


  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 04-12-2018

### CIRCULAR

All the III B.Tech II Semester students who are registered with **INFOSYS** are hereby informed that the value added course entitled "**Infosys Campus Connect Foundation Programming 5.0**" is scheduled from 06 December 2018 to 08 March 2019.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 4:00 PM to 5:00 PM

**Resource Person Details:**

- 1.P.Sandeep Reddy,  
Assoc.Prof,  
Dept. Of CSE,
- 2.P.Niranjana Kumar,  
Assoc.Prof,  
Dept. Of CSE,
- 3.A.Naresh Kumar,  
Asst.Prof,  
Dept. Of CSE.

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PROGRAMMING FOR PROBLEM SOLVING

Students are introduced to all major language elements including fundamental data types, flow control, and standard function libraries. The course explains the use of aggregate Arrays, structures, unions, and pointers early on so the students can practice extensively in the hands on labs. The course also includes coverage of portability tips drawn from experienced programmers working in production environments. Comprehensive hands on exercises are integrated throughout to reinforce learning and develop real competency.

#### Purpose the Course

To improve proficiency in programming

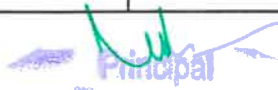
#### Eligibility

Any pursuing graduate(CSE).

#### Prerequisites

Knowledge on Mathematical and fundamental programming concepts .

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
31	40	180	4.00PM - 5:00PM	-

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Objectives


Students will be able to develop logics which will help them to create programs, applications in C. Also by learning the basic programming constructs they can easily switch over to any other language in future.

### Course Outline

Module 1	Introduction to Problem Solving ,Variables and Data Types, Operators and expressions	5 Hours
Module 2	Control Structures	5 Hours
Module 3	Arrays , 2-D arrays, Character Arrays and Strings	5 Hours
Module 4	Functions, Storage classes, Pointers	8 Hours
Module 5	Structures ,Unions ,files	8 Hours

### Reference Books

1. Byron Gottfried, Schaum's Outline of Programming with C, McGraw-Hill
2. E. Balaguruswamy, Programming in ANSI C, Tata McGraw-Hill
3. Brian W. Kernighan and Dennis M. Ritchie, The C Programming Language, Prentice Hall of India

  
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Ananthagiri (V&M), Suryapet (Dt). Pin: 508 206.

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course outcomes

1. Able to understand the fundamental syntax & operators of C
2. Able to design programs utilizing decision making and repetition
3. Ability to work with arrays
4. Able to design modular programs using functions , Demonstrate the concept of pointer
5. Develop programs using user defined data types and perform I/O operations in files

Course Coordinator	Resource Person
CH.Krishna Prasad Assoc. Prof. Department Of C.S.E Anurag Engineering College	Y.V.R.Naga Pawan Assoc. Prof. Department Of C.S.E Anurag Engineering College

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE:11-06-2018

### CIRCULAR

All the III B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering is going to offer the following value added courses for the academic year 2018-19.

**Name of the Value Added Course:** Programming for Problem Solving  
**Eligible Branches:** CSE

Interested students can enroll their names with the course coordinator on or before 14-06-2018


**Course Coordinator:**

Ch.Krishna Prasad,  
Assoc.Professor;  
CSE Dept.,  
Mobile No: 9848684256

  
HOB

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 18-06-2018

### CIRCULAR

All the III B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Programming for Problem Solving" is scheduled from 19 June 2018 to 04 October 2018.

**Venue:** A-209, Block-A

**Timings:** 4:00 PM to 5:00 PM (Thursday - Saturday)

**Resource Person Details:**


Y.V.R Naga Pawan,  
Assoc. Professor,  
CSE Department,  
Mobile:9553122276

  
HOD

**Encl:** List of students

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ORACLE JAVA FUNDAMENTALS

This course engages students with little or no programming experience to create Java programs. Participants are introduced to object-oriented programming concepts, terminology, and syntax, and the steps required to create basic Java programs using the Alice, Greenfoot, and Eclipse interactive development environments. Hand-on practices figure prominently throughout this course so students can experience firsthand the power of computer programming

#### Purpose the Course

Students who wish to extend their programming experience in Java and develop more complex Java applications. This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.

#### Eligibility

Any pursuing graduate(CSE & ECE).

#### Prerequisites

- Basic understanding of at least one programming language
- The ability to follow software installation instructions and install Alice, Greenfoot, and Eclipse on a computer

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	100	9.30AM - 4:00PM	-

### Course Objectives

- This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.
- Students will design animations, games and applications using fun and engaging tools.

### Course Outline

Module 1	<b>Introduction:</b> Introduction, <b>Alice3:</b> Getting Started with Alice 3 , Add and Position Objects ,Procedures and Arguments ,Rotation and Randomization, Declare Procedures , Control Statements, Functions , IF and WHILE Control Structures , Expressions , Variables ,Keyboard Controls ,Develop a Complete Animation ,Java Variables and Data Types ,Java Methods and Classes	8 Hours
Module 2	<b>Greenfoot:</b> Getting Started With Greenfoot, Methods, Variables and Parameters , Source Code and Documentation, Developing and Testing an Application, Randomization and Constructors , Defined Methods ,Sound and Keyboard Control ,World Animating and Game End ,Abstraction, Loops, Variables, and Arrays	8 Hours
Module 3	<b>Java Basics:</b> Getting Started with Eclipse, Object and Driver Classes, Data Types and Operators, Strings.	4 Hours
Module 4	<b>Program Structure :</b> Using Scanner and Conditional Statements ,Using Program Control Statements Arrays and Exceptions , Arrays , Handling Errors	5 Hours
Module 5	<b>Java Classes :</b> Classes, Objects, and Methods , Parameters and Overloading Methods ,The Static Modifier and Nested Classes ,Inheritance ,Polymorphism	5 Hours

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Students can gain Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
2. George Reese, Database Programming with JDBC & JAVA, Second Edition, O'Reilly Media
3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education.

Course Co-ordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.K.Ramesh Technical Trainer TASK 2.P.Arun Reddy Technical Trainer TASK

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DATE: 04-07-2018

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with **TASK** is going to offer the following value added courses for the academic year 2018-19.

**Name of the Value Added Course:** Oracle Java Fundamentals.  
**Eligible Branches:** CSE and ECE

**Course Coordinator:**  
Ch.Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
HOD

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 06-07-2018

### CIRCULAR

All the IV B.Tech I Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Oracle Java Fundamentals**" is scheduled from 09 July 2018 to 13 July 2018.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 4:00 PM

**Resource Person Details:**


P. Arun reddy,  
Technical Trainer,  
TASK.

  
HOD

**Encl:** List of students

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### VALUE ADDED/SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PERSONALITY DEVELOPMENT

Personality development is a cluster of productive personality traits that characterize one's relationships in a Milieu.

#### Purpose the Course

Personality development helps you develop an impressive personality and makes you stand apart from the rest. Personality development also plays an essential role in improving one's communication skills. Individuals ought to master the art of expressing their thoughts and feelings in the most desired way.


#### Eligibility

MBA

#### Prerequisites

- Keep a smart head and keep cool
- Don't copy anyone
- Create your own personal style
- Be an active Listener
- Learn to have patience and focus on your communication skills

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timmings	Fees
30	20	60	4:00 pm to 5:00 pm	NIL

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### Course Objectives

The objective of the programme is to build self-confidence, enhance self-esteem and improve over all personality of the participants.

### Course Outline

Module 1	<ol style="list-style-type: none"><li>1. Introduction to Soft Skills</li><li>2. Break the ice berg – FEAR</li><li>3. Self Development – Etiquette &amp; Manners</li></ol>	6 Hours
Module 2	<ol style="list-style-type: none"><li>1. Communication Skills</li><li>2. Body Language</li><li>3. Time Management</li></ol>	6 Hours
Module 3	<ol style="list-style-type: none"><li>1. Positive Attitude &amp; Self Confidence</li><li>2. Personality Development</li></ol>	6 Hours
Module 4	<ol style="list-style-type: none"><li>1. Listening Skills</li><li>2. Written Communication</li><li>3. Human Values</li></ol>	6 Hours
Module 5	<ol style="list-style-type: none"><li>1. Speaking Skills</li></ol>	6 Hours

  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

2. Personal life and Professional life Balance

### Reference Books


1. Soft skills,2015,career development centre, Green peari publications
2. Covey Sean, Seven Habits of highly effective teens, New York, fireside publishers,1998.
3. Carnegie dale, how to win friends and influence people, New York: Simon & Schuster,1998
4. Thomas A Harris, I am ok, you are ok, New York-Harper and row,1972
5. Daniel Coleman, emotional intelligence, Bantam Book,2006.

#### Course Coordinator

G.Varma  
Asst. professor  
MBA Department  
Anurag Engineering College

#### Resource Person

CH.Ramesh  
Asst. professor  
MBA Department  
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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 01-12-2018

### CIRCULAR

All the II MBA II Semester students are hereby informed that the department of Master of Business Administration is going to offer the following value added courses for the academic year 2018-19.


Name of the Value added course: PERSONALITY DEVELOPEMENT  
Eligible branches: MBA

Interested students can enrol their names with the course coordinator on or before 13-12-2018

Course coordinator:  
G.Varma  
Asst.Professor,  
MBA Dept.,  
Mobile No: 9989382096

  
HOD

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 27-12-2018

### CIRCULAR

All the II MBA II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PERSONALITY DEVELOPEMENT" is scheduled from 31-12-2018 to 05-03-2019.

Venue: A-308, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

CH.Ramesh,

Asst. Professor,

MBA Department,

Mobile: 9553122280

  
HOD


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## DEPARTMENT OF H&S

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES, A.Y.: 2018-19

#### About the Course

##### CAMPUS RECRUITMENT TRAINING-ENGLISH

The course, with its interactive and need based modules, will address various challenges of communication as well as behavioural skills faced by individual at workplace and organizations in bridging the gaps through effective skills of interviews, group discussion, presentations, time management, goal setting and nuances of drafting resume, cover letter for sustainability in today's world.

#### Purpose of the Course

The purpose of the course is to develop insights into language and enrich the professional competence of the students and make the students industry ready.

#### Eligibility

All the B.Tech III year students are eligible for this course.

#### Prerequisites

Students should have basic Communication Skills.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	40	80	-	-

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## DEPARTMENT OF H&S

### Course Objectives

1. To improve the fluency of students in English
2. To facilitate learning through interaction
3. To train students in group dynamics, body language and various other activities which boost their confidence levels
4. To encourage students develop behavioral skills and personal management skills
5. To meet challenges job and career advancement

### Course Outline

Module 1	SWOT Analysis, Time Management and Leadership Skills	4 hours
Module 2	Error Spotting, Subject Verb Agreement, Preposition, Articles and Voice	6 hours
Module 3	Goal Setting, Motivation Problem Solving and Decision Making	4 hours
Module 4	Cover Letter and Resume Writing Paragraph Writing	8 hours
Module 5	Group Discussion, Presentation Skills and Interview Skills	8 hours

  
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## DEPARTMENT OF H&S


### Course Outcomes

1. Effectively communicate through verbal/oral communication
2. Developing task based learning skills
3. Participate effectively in heterogeneous teams through the knowledge of team work
4. Become more effective individual through goal setting and self motivation
5. Actively participate in group discussion, presentations and interviews

### Reference Books

1. Raman, Meenakshi & Sangeeta Sharma.2009.Technical Communication.Oxford University Press.
2. Alex,K. Soft Skills.2010.S Chand Publication.

Course Coordinator	Course resource person
D.Pandu Ranga Rao, Assistant Professor Dept of H&S	Dr. K. Ramesh, Assistant Professor J. Poorna Kumar, Assistant Professor

  
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## DEPARTMENT OF H&S

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A.Y.: 2018-19

DATE: 05.11.2018

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of H&S is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: CAMPUS RECRUITMENT TRAINING-ENGLISH  
Eligible branches: All Branches

Interested students can enroll their names with the course coordinator on or before 12.11.2018

Course coordinator:

D. Pandu Ranga Rao  
Asst. Professor,  
H&S Dept.,  
Mobile No: 9948123648

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## DEPARTMENT OF H&S

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A.Y.: 2018-19

DATE: 13.11.2018

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "CAMPUS RECRUITMENT TRAINING-ENGLISH" is scheduled from **14 Nov 2018 to 8 Mar 2019** as per the time table.

#### Resource Person Details:

1. Dr K Ramesh  
Asst. Professor,  
H&S Department,  
Mobile: 9912650052
2. J Poorna Kumar  
Asst. Professor,  
H&S Department,  
Mobile: 9553139384

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## DEPARTMENT OF H&S

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES, A.Y.: 2018-19

#### About the Course

##### CAMPUS RECRUITMENT TRAINING-MATHEMATICS

The course is designed for the students who are appearing in campus placements to improve their logical thinking and aptitude levels which are key points in campus placements.

#### Purpose of the Course

The purpose of the course is to improve logical thinking and aptitude levels of the students and make them industry ready.


#### Eligibility

All the B.Tech pursuing students are eligible for this course.

#### Prerequisites

Students should have basic arithmetic skills.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
35	20	-		-

  
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## DEPARTMENT OF H&S

DATE: 05.11.2018

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of H&S is going to offer the following value added courses for the academic year 2018-19.

Name of the Value added course: CAMPUS RECRUITMENT TRAINING-MATHEMATICS  
Eligible branches: All Branches

Interested students can enroll their names with the course coordinator on or before 12.11.2018

Course coordinator:  
T. Naga Raju  
Asst. Professor,  
H&S Dept.,  
Mobile No: 9491406080

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## DEPARTMENT OF H&S

DATE: 13.11.2018

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "CAMPUS RECRUITMENT TRAINING-MATHEMATICS" is scheduled from **14 Nov 2018 to 8 Mar 2019** as per the time table.

#### **Resource Person Details:**

Dr P Jyothi  
Asst. Professor,  
H&S Department,  
Mobile: 9490486268

NCH Sridhar  
Asst. Professor,  
H&S Department,  
Mobile: 9441566662

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## LIST OF VALUE ADDED COURSES OFFERED IN THE A.Y. 2019 – 20

S. No.	Course Code	Name of Value Added Course Offered
1	CE19VA01	Safety In Construction
2	CE19VA04	Clean and Green Campus Training
3	EE19VA01	Introduction to PSPICE
4	EE19VA02	Introduction to MATLAB
5	ME19VA01	ANSYS
6	ME19VA02	CREO
7	EC19VA01	Introduction to Mentor graphics Design Tools
8	EC19VA02	Introduction to LAB View
9	CS19VA01	Oracle Java Fundamentals
10	CS19VA01	Oracle Java Fundamentals
11	MB19VA02	Personality Development
12	HS17VASS	CRT-E
13	HS17VAQR	CRT-M

  
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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### SAFETY IN CONSTRUCTION

Students are given training in regarding safety in construction local and international standards and laws associated with health and safety in construction activities, identification and controlling of workplace dangers and hazards and practical application of knowledge in real time situations. To join and study this course, no previous health and safety knowledge is required although it is advisable that candidates should have basic understanding of construction activities.

#### Purpose the Course

To improve professional skills.

#### Eligibility

III B TECH (CE) Students.

#### Prerequisites

Knowledge on Building materials and construction.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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## DEPARTMENT OF CIVIL ENGINEERING

### Reference Books

1. Industrial safety management By: L.M. Deshmukh Publishers: Tata Megraw Hill ,New Delhi  
Year: 2006 Edition: First
2. Industrial safety health and environment Management system By: R.K. Jain & Sunil S. Rao  
Publishers: Khanna Publishers Year: 2008 Edition: Second

Course Coordinator	Resource Person
P. Sruthi Asst. professor CE Department Anurag Engineering College	G. Narendar Asst. professor CE Department Anurag Engineering College

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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand safety rules, regulations and norms which will help them in construction site. How to handle and maintains of tools and machinery. Also by learning about SHE, carefully planned, organised, monitored and controlled.

### Course Outline

Course Outline		
Module 1	Machine Operation And Guarding	5 Hours
Module 2	Materials Handling And Storage Of Materials	5 Hours
Module 3	Construction Industry	5 Hours
Module 4	Working At Heights	7 Hours
Module 5	Noise And Vibration	8 Hours

### COURSE OUTCOMES:

After successfully completion of this course, the student will be able to

**CO 1:** Know how to Handle and Protect the Machine and himself while working on it

**CO 2:** Know the Knowledge of lifting and carrying the materials and also maintenance of mechanical material handling equipment

**CO 3:** Understand the knowledge of safety rules and regulations of working at construction industry

**CO 4:** Understand the knowledge about working at all stages in different heights at construction site

**CO 5:** Know the knowledge of different types of noise and vibration, its causes, effects and controlling measures.

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# ANURAG Engineering College

(An Autonomous Institution)

Ananthagiri (V&M), Kodad, Suryapet (Dt). Telangana

Pin: 508 206.

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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 12-6-2019

### CIRCULAR

All the III B. Tech I Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: Safety in Construction

Eligible branches: CE

Interested students can enrol their names with the course coordinator on or before 15-6-2019.

Course coordinator:

P. Sruthi.

Asst. Professor,

CE Dept.,

Mobile No: 9110714939

*C. Madhavi*  
HOD 12/6/19

To

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Pin: 508 206.

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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 17-6-2019

### CIRCULAR

All the III B. Tech I Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Safety In Construction" is scheduled from 18 June 2019 to 16 Oct 2019.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

G. Narendar

Asst. Professor,

CE Department,

Mobile: 9492909685

*C. M. Reddy*

HOD

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## DEPARTMENT OF CIVIL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### ABOUT THE COURSE

#### CLEAN AND GREEN CAMPUS TRAINING

Students are introduced to all major aspects of Providing optimal clean, green and smart learning space continues to be the aspiration of almost each and every educational institute. However, there are several barriers such as lack of availability of knowledge. How about the technologies, limited expertise of the people in the institutes, time & budgetary constraints and more. On the other hand, there are several tangible and intangible benefits of infusing and expanding clean, green and smart learning spaces in colleges, institutes and universities.

#### Purpose the Course

To improve social developmental skills and responsible for social activities.

#### Eligibility

III B TECH (CE) Students.

#### Prerequisites

Knowledge on Environmental Studies.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	10	25	4.00-5.00PM	-

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## DEPARTMENT OF CIVIL ENGINEERING

### Course Objectives

Students will be able to understand Rediscovered key attributes of Clean & Green Campus. Conceptualised Innovative ways to internalise and realise Clean and green campus. Incorporated holistic approach to green campus. Strengthened collaborations with clean and green technology industries, institutions and professional bodies Learned to leverage on national critical campaigns & issues, technology development and Personal Experience.

### Course Outline

Module 1	Concept, Benefits & Challenges in adopting Clean, Green Campus.	5 Hours
Module 2	Advanced Technology in Renewable Energy Resources	5 Hours
Module 3	Water Treatment System	5 Hours
Module 4	Hazardous Management and Recycling System	7 Hours
Module 5	Group Discussion	8 Hours

### COURSE OUTCOMES:

At the end of course student will learn about to

CO 1: Basics in Clean, Green Campus.

CO 2: Advanced Technology in Renewable Energy Resources.

CO 3: Understand How to use the Water Treatment System.

CO 4: Various Hazardous Management and Recycling Systems.

CO 5: Improve Communication and Interaction skills.

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## DEPARTMENT OF CIVIL ENGINEERING

Course Coordinator	Resource Person
G. Narendar Asst. professor CE Department Anurag Engineering College	D. Sravanthi Asst. professor CE Department Anurag Engineering College

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## DEPARTMENT OF CIVIL ENGINEERING

DATE: 15-11-2019

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the department of Civil Engineering is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: Clean and Green Campus Training  
Eligible branches: CE

Interested students can enroll their names with the course coordinator on or before 19-11-2019.

Course coordinator:  
G.NARENDAR  
Asst. Professor,  
CE Dept.,  
Mobile No: 9492909685

*C.M. Reddy*  
15/11/19  
HOD

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## DEPARTMENT OF CIVIL ENGINEERING

---

DATE: 21-11-2019

### CIRCULAR

All the III B. Tech II Semester students who have enrolled their names with the course coordinator are hereby informed that the value added course entitled "Clean and Green Campus Training" is scheduled from 22 Nov 2019 to 16 March 2020.

Venue: A-309, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

#### Resource Person Details:


D. Sravanthi  
Asst. Professor,  
CE Department,  
Mobile: 8985113144

*C. Meddally*  
HOD

Encl: List of students

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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to PSPICE (Personal Computer Simulation Program with Integrated Circuit Emphasis)

### About The Course

This course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters.

### Purpose of the Course

This course is focused on engineers, professors and researchers in power systems and power electronics, interested in exploring the modern tools available for modeling, design and analysis.

### Eligibility

II B.Tech I Semester (Electrical and Electronics Engineering)

### Prerequisites

Basic schematic entry with ORCAD Capture

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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### Course Objectives

- To use techniques of creating PSPICE Designs.
- To Run DC bias, DC sweep, AC sweep, mesh and nodal analysis. Design of circuits for power electronics converters.
- To learn how to edit a stimulus and run a parametric analysis
- To create, edit and implement models, create sub-circuits and create parts for simulation.
- To analyze the single phase converters with RLE loads.

### Course Outlines

MODULE	Topic	Hours
MODULE 1	Introduction to PSPICE	6 Hours
MODULE 2	Preparing a design for simulation	6 Hours
MODULE 3	Creating and Editing Models	6 Hours
MODULE 4	Creating Parts for Models	6 Hours
MODULE 5	Simulation Examples	6 Hours

### Course Outcomes

After completing this course, student will be able to:

- Use techniques of creating PSPICE Designs.
- Run DC bias, DC sweep, AC sweep, mesh and nodal analysis and able to design power electronics converter circuits for simulation.
- Edit a stimulus and run a parametric analysis
- Create, edit and implement models, create sub-circuits and create parts for simulation.
- Analyze the single phase converters with RLE loads.

### Reference Books

1. Muhammad H. Rashid, "Spice for Circuits and Electronics Using Pspice", 2<sup>nd</sup> Edition, Prentice Hall India, 1990.
2. Muhammad H. Rashid, "SPICE for Power Electronics and Electric Power" CRC Press, 3<sup>rd</sup> Edition, 2012.

<b>Course Coordinator</b>	<b>Resource Person</b>
Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College	T.Raghu Asst. professor EEE Department Anurag Engineering College



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

DATE: 12.06.2019

### CIRCULAR

All the II B.Tech I.Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2019-20.

**Name of the Value added course:** Introduction to PSPICE

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 15.06.2019

**Course coordinator:**

J.Srinivas Rao

Asst.Professor,

EEE Dept.,

Mobile No: 9848044209




HOD

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 17.06.2019

### CIRCULAR

All the II B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to PSPICE" is scheduled from 18.06.2019 to 16.10.2019.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

T.Raghu,  
Asst. Professor,  
EEE Department,  
Mobile:9959692070

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## DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

### VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

## Introduction to MATLAB

### About The Course

MATLAB is a high-performance language for technical computing. It integrates computation, visualization, and programming in an easy-to-use environment where problems and solutions are expressed in familiar mathematical notation. Typical uses include:

- Math and computation
- Algorithm development
- Modeling, simulation, and prototyping
- Data analysis, exploration, and visualization
- Scientific and engineering graphics
- Application development, including Graphical User Interface building

### Purpose of the Course

A proprietary programming language developed by MathWorks, MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, Java, Fortran and Python.

### Eligibility

III B.Tech II Semester (Electrical and Electronics Engineering)

### Prerequisites

Once you get to know the representation of data and other things used while programming you can easily pull it off. The Prerequisites are

- Basic knowledge of Programming.
- Know the need of MATLAB and its implementation.
- The most important aspect is the syntax.
- Get to know the need of different tools used in it.
- Know the applications of it in different fields

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

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	Simulink models for single area load frequency control problem.	
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<b>Course Outcomes</b>	
●	Student gets familiarize in using the MATLAB software.
●	Student gain the knowledge for solving Engineering problems using simulation tools.
●	Student will be able to do their project works by using MATLAB software.
●	Student develops awareness in use of this software for real time applications.
●	Student will be able to analyze and modeling the networks by using MATLAB software.

<b>Reference Books</b>	
1.	Modeling and Simulation using MATLAB - Simulink, 2ed
2.	Getting Started with MATLAB: A Quick Introduction for Scientists & Engineers
3.	MATLAB: An Introduction with Applications, 4ed
4.	MATLAB Programming for Engineers by Stephen J. Chapman
5.	Introduction to MATLAB for Engineers by William J. Palm III
6.	An Introduction to Scientific Computing: Twelve Computational Projects Solved with MATLAB by Ionut Danaila

<b>Course Coordinator</b>	<b>Resource Person</b>
T.Raghu Asst. professor EEE Department Anurag Engineering College	Mr.J.Srinivas Rao Asst. professor EEE Department Anurag Engineering College

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 15.11.2019

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of electrical and electronics engineering is going to offer the following value added courses for the academic year 2019-20.

**Name of the Value added course:** Introduction to MATLAB

**Eligible branches:** EEE

Interested students can enrol their names with the course coordinator on or before 18.11.2019.

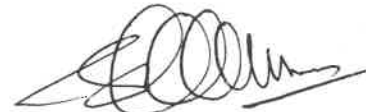
**Course coordinator:**

T.Raghu

Asst.Professor,

EEE Dept.,

Mobile No: 9848044209




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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DATE: 19.11.2019

### CIRCULAR


All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Introduction to MATLAB" is scheduled from 20.11.2019 to 16.03.2020.

**Venue:** B-304, Block-B

**Timings:** 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

**Resource Person Details:**

J.Srinivas Rao  
Asst. Professor,  
EEE Department,  
Mobile:9848044209



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## DEPARTMENT OF MECHANICAL ENGINEERING

## VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

### About the Course

#### ANSYS

To learn the basis of AUTOCAD commands,3D Modelling and Analysis of various problems

### Purpose the Course

To generate the engineering drawings, creates the 3D modelling and analyses the various engineering component problems.

### Eligibility

Pursuing Graduates of Mechanical Engineering.

### Prerequisites

3D Modelling.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

- To be able to understand and handle design problems in a systematic manner
- To be able to apply CAD in real life Applications.
- To be able to understand the 3D Design Components.
- To be able to apply Stress Analysis on Various Beams.
- To be able to apply Stress Analysis on Various Parts.

### Course Outcomes

- CO 1: Ability to understand the basics of AUTOCAD.
- CO 2: To get Practice on 2D and Isometric Diagrams.
- CO 3: To get Practice on 3D Components.
- CO 4: To get Practice on stress analysis of Various Beams.
- CO 5: To get practice on Truss Analysis.

### Course Outline

Course Outline		
Module 1	Basics of AUTOCAD	6hours
Module 2	Practice on 2D and isometric Diagrams	6hours
Module 3	Introduction to CREO Practice on 3D Modelling	6hours
Module 4	Introduction to ANSYS	6hours
Module 5	Practice on various engineering components Analysis.	6hours

### Reference Books

1. AutoCAD reference guide by Chetan Prakasan Published by CCAD Solution training and sense
2. PTC Creo Parametric by Prof.Shan Tickoo.
3. Engineering analysis with ansys software by Tadeusz stolarski s. yoshimoto, yasuniro narkasone.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

DATE:15-11-2019

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: ANSYS  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 19-11-2019

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

---

DATE: 21-11-2019

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "ANSYS" is scheduled from 22-11-2019 to 16-03-2020.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM ((Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.



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## DEPARTMENT OF MECHANICAL ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

CREO

To Learn the Sketching, 3D Modelling and assembling of Mechanical Components

#### Purpose the Course

To Generate the Engineering drawings and creates the 3D Modelling.

#### Eligibility

Pursuing Graduates of Mechanical Engineering.

#### Prerequisites

AutoCAD Commands.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	180	4:00pm-5:00pm	NIL

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## DEPARTMENT OF MECHANICAL ENGINEERING

### Course Objectives

To be able to apply CAD in real life applications.  
To be able to apply Sketching of 2D Commands.  
To be able to apply Conversion of Diagrams.  
To be able to apply Modelling Techniques.  
To be able to apply Assembling Components.

### Course Outcomes

CO 1: Ability to understand the basics of CREO  
CO 2: Ability to understand the sketching of 2D.  
CO 3: To get Practice converting from 2D to 3D.  
CO 4: To get Practice on 3D modelling  
CO 5: To get practice on assembling of 3D components.

### Course Outline

Course Outline		
Module 1	Introduction to CREO	6hours
Module 2	Practice on Sketching	6hours
Module 3	Conversion of 2D to 3D	6hours
Module 4	Practice on 3D Modelling	6hours
Module 5	Practice on assembly	6hours

### Reference Books

1.PTC Creo Parametric by Prof.Shan Tickoo.

Course Coordinator	Resource Person
Mr E Sadanandam Assistant Professor ME Department ANURAG Engineering College	Mr J Ashok Assistant Professor ME Department ANURAG Engineering College

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## DEPARTMENT OF MECHANICAL ENGINEERING

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DATE: 12-06-2019

### CIRCULAR

All the IV B.Tech I Semester students are hereby informed that the Department of Mechanical Engineering is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: PTC CREO  
Eligible Branches: Mechanical Engineering

Interested students can enrol their names with the course coordinator on or before 15-06-2019

Course coordinator:  
E Sadanandam  
Assistant Professor.  
Department of Mechanical Engineering  
Mobile No: 9985498008

  
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## DEPARTMENT OF MECHANICAL ENGINEERING

---

DATE: 17-06-2019

### CIRCULAR

All the IV B.Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "PTC CREO" is scheduled from 18-06-2019 to 16-10-2019.

Venue: B-109, Block-B

Timings: 4:00 PM to 5:00 PM ((Thursday, Friday&Saturday)

Resource Person Details:

Mr J Ashok,  
Assistant Professor,  
Department of Mechanical Engineering,  
Mobile: 9704566243.

  
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

Introduction to Mentor graphics Design Tools

## About The Course

Fundamental of Designers can develop an executable functional specification that documents the exact behavior of all the components and their interfaces Designers can make decisions about cost, performance, power, and area earlier in the design process Designers can create tools which automatically manipulate the design for verification, synthesis, optimization, etc. Hardware description languages are an essential part of modern digital design.

## Purpose The Course

This course is focused to engineers, to Digital Design concepts & implementation.

## Eligibility

IV B.Tech (Electronics and Communication Engineering)

## Prerequisites

Knowledge on schematic, simulation-calbre and analog and digital electronics, VLSI.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Learn the mentor graphic tool or equivalent tool for design the schematic and simulation of all gates.
- To understand the concept of digital system design method through practical domain.
- Design combinational and sequential circuits
- HDLs help manage complexity inherent in modern designs.
- Analyze and layout design of CMOS circuits in micron and submicron level using any platform.



PRINCIPAL

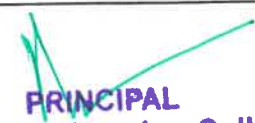
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Course Outlines		
<b>MODULE 1</b>	Introduction to mentor graphics <ul style="list-style-type: none"> <li>• Mentor graphics resources</li> <li>• Different Modeling styles used in Verilog.</li> <li>• Mentor graphics Application builder</li> <li>• DUT verification by simulation using Directed Test bench</li> </ul>	<b>6 Hours</b>
<b>MODULE 2</b>	Introduction to Virtual instruments <ul style="list-style-type: none"> <li>• Introduction to back end</li> <li>• FPGA Architecture basics</li> <li>• ASIC &amp; FPGA Design flow</li> <li>• Synthesis and FPGA Implementation</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	Mentor graphics Environment <ul style="list-style-type: none"> <li>• Verifying the HDL codes by writing Test benches using ISim .</li> <li>• Synthesis of HDL codes of Xilinx FPGA Kits</li> <li>• ICoregen flow</li> <li>• mentor graphics tool functions description</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Build the schematic and simulation <ul style="list-style-type: none"> <li>• Full Adder, .</li> <li>• Ripple-carry Adder.</li> <li>• DFF. Counters. Shifters.</li> <li>• ALU. RAM/ROM etc..</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Layout design <ul style="list-style-type: none"> <li>• DRC, PEX</li> <li>• FPGA development kits</li> <li>• ASIC v/s FPGA design flows</li> </ul>	<b>6 Hours</b>

Course Outcomes
CO1: Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary For engineering practice.
CO2: Use computer-aided design tools to synthesize, map, place, routing, and download the digital Designs on the FPGA board.
CO3: Model digital systems in VHDL and Partition a digital system into different subsystems..
CO4: Transfer a design from a version possible to simulate to a version possible to synthesize..
CO5: Learn the basics of Fabrication and Layout of CMOS Integrated Circuits..

References
1. <a href="https://www.mentor.com/products/ic-manufacturing/">https://www.mentor.com/products/ic-manufacturing/</a>
2. <a href="https://www.mentor.com/training">https://www.mentor.com/training</a>

COURSE COORDINATOR:	RESOURCE PERSON:
Dr.B.Rajan, Professor, ECE Dept. Anurag Engineering college	Mr.Md.Fareed Ahamad Assistant Professor, ECE Dept. Anurag Engineering college

  
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# ANURAG Engineering College

(An Autonomous Institution)

Ananthagiri (V&M), Kodad, Suryapet (Dt). Telangana

Pin: 508 206.

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

---

DATE: 12-06-2019

### CIRCULAR

All the IV B. Tech I Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2019-2020.

Name of the Value-added course: Introduction to Mentor graphics Design Tools.

Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 15-06-2019

Course coordinator:

Dr. B. Rajan,

Professor,

ECE Dept.

Mobile No: 79971 30343




HOD

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Pin: 508 206.

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

---

DATE:20-06-2019

### CIRCULAR

All the IV B. Tech I Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to Mentor graphics Design Tools" is scheduled from 21<sup>st</sup>Jun 2019 to 24<sup>th</sup>Oct 2020.

Venue: Block-D: 408 SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

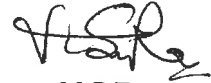
Resource Person Details:

Mr.Md.Fareed Ahmed

Assistant Professor,

ECE Dept,

Mobile: 94949 25012.

  
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Encl: List of students

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# ANURAG ENGINEERING COLLEGE

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DEPARTMENT OF ELECTRONICS and COMMUNICATION ENGINEERING

VALUE ADDED /SKILL AND COMPETENCY DEVELOPMENT COURSES

INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY

## About The Course

This course describes the LABVIEW graphical programming environment and techniques for building applications in LABVIEW, such as test and measurement, data acquisition, instrument control, data logging, measurement analysis, and report generation applications.

## Purpose The Course

This course is to learn about LABVIEW programming features, including the LABVIEW user interface and programming workspaces, and the LABVIEW palettes and tools. This course does not include specific information about each palette, tool, menu, dialog box, control, or built-in VI or function.

## Eligibility

III B.Tech (Electronics And Communication Engineering)

## Prerequisites

Knowledge on logical designing, signals and electronic analysis.

No Of Contact Hours	Min Number Of Participants	Max Number Of Participants	Scheduled Course Timings	Fees
30	30	180	4.00 pm – 5.00 pm	Nil

## Course Objectives

- Introduction to LABVIEW
- Introduction to Virtual Instruments
- LABVIEW Environment
- Building the Front Panel and Building the Block Diagram
- Running and Debugging VIs and Creating VIs and Sub VIs

## Course Outlines

Course Outlines		
MODULE 1	Introduction To Labview <ul style="list-style-type: none"><li>● LABVIEW Resources</li><li>● LABVIEW Measurements</li><li>● LABVIEW Application Builder and</li><li>● LABVIEW Template VIs</li></ul>	6 Hours

<b>MODULE 2</b>	Introduction to Virtual Instruments <ul style="list-style-type: none"> <li>● Introduction to Front Panel</li> <li>● Introduction to Nodes, Wires and Structures</li> <li>● Introduction to Icon and Connector Pane</li> </ul>	<b>6 Hours</b>
<b>MODULE 3</b>	LABVIEW Environment <ul style="list-style-type: none"> <li>● Controls and Functions palettes</li> <li>● Navigating the Controls and Functions Palettes</li> <li>● Tools Palette</li> <li>● Menus and the Toolbar</li> </ul>	<b>6 Hours</b>
<b>MODULE 4</b>	Building the Front Panel and Building the Block Diagrams <ul style="list-style-type: none"> <li>● Configuring Front Panel Objects and Changing Controls to Indicators and Indicators to Controls</li> <li>● Replacing Front Panel Objects and Configuring the Front Panel</li> <li>● Relationship between Front Panel Objects and Block Diagram Terminals</li> </ul>	<b>6 Hours</b>
<b>MODULE 5</b>	Running and Debugging VIs and Creating VIs and Sub Vis <ul style="list-style-type: none"> <li>● Running Vis and Configuring How a VI Runs</li> <li>● Correcting Broken VIs</li> <li>● Finding Causes for Broken VIs and Common Causes of Broken VIs</li> <li>● Debugging Techniques and Execution Highlighting</li> <li>● Disabling Debugging Tools</li> </ul>	<b>6 Hours</b>

<b>Course Outcomes</b>
CO1: Understand the basics of virtual instrumentation concept and dataflow programming
CO2: Understand various functions available in Lab VIEW for engineering applications
CO3: Design projects using the functions available in Lab VIEW
CO4: Understand the interfacing of DAQ devices and customized user designed hardware with Lab VIEW
CO5: Write the Certified LabVIEW Associate Developer (CLAD) exam, administered by National Instruments, for the certification and leading to placements in core companies

<b>References</b>
1. <a href="http://www.ni.com/en-in/innovations">www.ni.com/en-in/innovations</a> .

<b>COURSE COORDINATOR:</b>	<b>RESOURCE PERSON:</b>
Dr.B.Rajan, Professor ECE Dept. Anurag Engineering college	Mr.K.Srinadh Assistant Professor, ECE Dept. Anurag Engineering college

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

---

DATE: 25-11-2019

### CIRCULAR

All the III B. Tech II Semester students are hereby informed that the Department of Electronics and Communication Engineering is going to offer the following value-added courses for the academic year 2019-20.

Name of the Value-added course: INTRODUCTION TO LABVIEW LABORATORY FEASIBILITY  
Eligible branches: ECE.

Interested students can enrol their names with the course coordinator on or before 28-11-2019

Course coordinator:

Dr. B. Rajan,

Professor,

ECE Dept.

Mobile No: 79971 30343

HOD

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DATE: 28-11-2019

### CIRCULAR

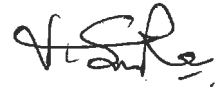
All the III B. Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value-added course entitled "Introduction to LABVIEW Laboratory Feasibility" is scheduled from 29<sup>th</sup> Nov 2019 to 21<sup>st</sup> Mar 2020.

Venue: Block-D: 408-SEMINAR HALL

Timings: 4:00 PM to 5:00 PM (Thursday, Friday & Saturday)

Resource Person Details:

Mr.K.Srinadh,  
Assistant Professor  
ECE Dept,  
Mobile: 7306106104

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ORACLE JAVA FUNDAMENTALS

This course engages students with little or no programming experience to create Java programs. Participants are introduced to object-oriented programming concepts, terminology, and syntax, and the steps required to create basic Java programs using the Alice, Greenfoot, and Eclipse interactive development environments. Hand-on practices figure prominently throughout this course so students can experience firsthand the power of computer programming

#### Purpose the Course


Students who wish to extend their programming experience in Java and develop more complex Java applications. This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.

#### Eligibility

Any pursuing graduate (ECE & CSE)

#### Prerequisites

- Basic understanding of at least one programming language
- | The ability to follow software installation instructions and install Alice, Greenfoot, and Eclipse on a computer

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	100	9.30AM - 4:00PM	-

### Course Objectives

- This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.
- Students will design animations, games and applications using fun and engaging tools.

### Course Outline

Module 1	<b>Introduction:</b> Introduction, <b>Alice3:</b> Getting Started with Alice 3 , Add and Position Objects ,Procedures and Arguments ,Rotation and Randomization, Declare Procedures , Control Statements, Functions , IF and WHILE Control Structures , Expressions , Variables ,Keyboard Controls ,Develop a Complete Animation ,Java Variables and Data Types ,Java Methods and Classes	8 Hours
Module 2	<b>Greenfoot:</b> Getting Started With Greenfoot, Methods, Variables and Parameters , Source Code and Documentation, Developing and Testing an Application, Randomization and Constructors , Defined Methods ,Sound and Keyboard Control ,World Animating and Game End ,Abstraction, Loops, Variables, and Arrays	8 Hours
Module 3	<b>Java Basics:</b> Getting Started with Eclipse, Object and Driver Classes, Data Types and Operators, Strings.	4 Hours
Module 4	<b>Program Structure</b> :Using Scanner and Conditional Statements ,Using Program Control Statements Arrays and Exceptions , Arrays , Handling Errors	5.Hours
Module 5	<b>Java Classes</b> :Classes, Objects, and Methods , Parameters and Overloading Methods ,The Static Modifier and Nested Classes ,Inheritance ,Polymorphism	5 Hours

  
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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Students can gain Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
2. George Reese, Database Programming with JDBC & JAVA, Second Edition, O'Reilly Media
3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education.

Course Co-ordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.K.Ramesh Technical Trainer TASK 2.P.Arun Reddy Technical Trainer TASK

  
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Pin: 508 206.

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

---

DATE: 08-11-2019

### CIRCULAR


All the III & IV B. Tech students are hereby informed that the Department of Electronics and Communication Engineering in collaboration with **TASK** is going to offer the following value-added courses for the academic year 2019-2020.

Name of the Value-added course: Oracle Java Fundamentals.  
Eligible branches: ECE &CSE.

Course coordinator:  
Ch. Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
HOD

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DATE: 12-11-2019

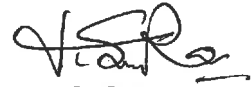
### CIRCULAR

All the III & IV B. Tech students who have registered with **TASK** are hereby informed that the value-added course entitled "Oracle Java Fundamentals" is scheduled from 25<sup>th</sup> Nov 2019 to 29<sup>th</sup> Nov 2019.

Venue: Block-A: CSE SEMINAR HALL  
Timings: 9:30 AM to 4:00 PM

#### **Resource Person Details:**


K.Ramesh,  
Technical Trainer,  
TASK.

  
HOD

**Encl:** List of students

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ORACLE JAVA FUNDAMENTALS

This course engages students with little or no programming experience to create Java programs. Participants are introduced to object-oriented programming concepts, terminology, and syntax, and the steps required to create basic Java programs using the Alice, Greenfoot, and Eclipse interactive development environments. Hand-on practices figure prominently throughout this course so students can experience firsthand the power of computer programming

#### Purpose the Course

Students who wish to extend their programming experience in Java and develop more complex Java applications. This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.

#### Eligibility

Any pursuing graduate(CSE & ECE).

#### Prerequisites

- Basic understanding of at least one programming language
- The ability to follow software installation instructions and install Alice, Greenfoot, and Eclipse on a computer

  
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Ananthagiri (V&M), Suryapet (Dt). Pin: 508 206.

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	100	9.30AM - 4:00PM	-

### Course Objectives

- This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.
- Students will design animations, games and applications using fun and engaging tools.

### Course Outline

Module 1	<b>Introduction:</b> Introduction, <b>Alice3:</b> Getting Started with Alice 3 , Add and Position Objects ,Procedures and Arguments ,Rotation and Randomization, Declare Procedures , Control Statements, Functions , IF and WHILE Control Structures , Expressions , Variables ,Keyboard Controls ,Develop a Complete Animation ,Java Variables and Data Types ,Java Methods and Classes	8 Hours
Module 2	<b>Greenfoot:</b> Getting Started With Greenfoot, Methods, Variables and Parameters , Source Code and Documentation, Developing and Testing an Application, Randomization and Constructors , Defined Methods ,Sound and Keyboard Control ,World Animating and Game End ,Abstraction, Loops, Variables, and Arrays	8 Hours
Module 3	<b>Java Basics:</b> Getting Started with Eclipse, Object and Driver Classes, Data Types and Operators, Strings.	4 Hours
Module 4	<b>Program Structure :</b> Using Scanner and Conditional Statements ,Using Program Control Statements Arrays and Exceptions , Arrays , Handling Errors	5 Hours
Module 5	<b>Java Classes :</b> Classes, Objects, and Methods , Parameters and Overloading Methods ,The Static Modifier and Nested Classes ,Inheritance ,Polymorphism	5 Hours

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Students can gain Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
2. George Reese, Database Programming with JDBC & JAVA, Second Edition, O'Reilly Media
3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education.

Course Co-ordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.K.Ramesh Technical Trainer TASK 2.P.Arun Reddy Technical Trainer TASK

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 25-06-2019

### CIRCULAR


All the IV B.Tech I Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with **TASK** is going to offer the following value added courses for the academic year 2019-20.

**Name of the Value Added Course:** Oracle Java Fundamentals.  
**Eligible Branches:** CSE and ECE

**Course Coordinator:**  
Ch.Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
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# ANURAG Engineering College

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Pin: 508 206.

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

---

DATE: 28-06-2019

### CIRCULAR

All the IV B.Tech I Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Oracle Java Fundamentals**" is scheduled from 01 July 2019 to 05 July 2019.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 4:00 PM

**Resource Person Details:**

P. Arun reddy,  
Technical Trainer,  
TASK.

  
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**Encl:** List of students

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### ORACLE JAVA FUNDAMENTALS

This course engages students with little or no programming experience to create Java programs. Participants are introduced to object-oriented programming concepts, terminology, and syntax, and the steps required to create basic Java programs using the Alice, Greenfoot, and Eclipse interactive development environments. Hand-on practices figure prominently throughout this course so students can experience firsthand the power of computer programming.

#### Purpose the Course

Students who wish to extend their programming experience in Java and develop more complex Java applications. This course is a suitable foundational class for computer science majors and non-majors alike, and when taught in sequence with Java Fundamentals or Java Foundations.

#### Eligibility

Any pursuing graduate(CSE & ECE).

#### Prerequisites

- Basic understanding of at least one programming language
- The ability to follow software installation instructions and install Alice, Greenfoot, and Eclipse on a computer

  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING


No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	30	100	9.30AM - 4:00PM	-

### Course Objectives

- This course of study builds on the skills gained by students in Java Fundamentals or Java Foundations to help advance Java programming skills.
- Students will design animations, games and applications using fun and engaging tools.

### Course Outline

Module 1	<b>Introduction:</b> Introduction, <b>Alice3:</b> Getting Started with Alice 3 , Add and Position Objects ,Procedures and Arguments ,Rotation and Randomization, Declare Procedures , Control Statements, Functions , IF and WHILE Control Structures , Expressions , Variables ,Keyboard Controls ,Develop a Complete Animation ,Java Variables and Data Types ,Java Methods and Classes	8 Hours
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### Course Outcomes


After completion of this course the students will be able to

1. Students can gain Java programming skills.
2. Students can design object-oriented applications with Java.
3. Students can create Java programs using hands-on, engaging activities.

### Reference Books

1. Herbert schildt , Java- the complete reference, Seventh edition, Tata McGraw Hill.
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3. Thinking in Java Fourth Edition, Bruce Eickel.
4. Introduction to Java programming, Y. Daniel Liang, pearson education.
5. Understanding OOP with Java, updated edition, T. Budd, pearson education.

Course Co-ordinator	Resource Person
CH.Vidya Sagar Training and Placement Officer Anurag Engineering College	1.K.Ramesh Technical Trainer TASK 2.P.Arun Reddy Technical Trainer TASK

  
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# ANURAG Engineering College

(An Autonomous Institution)

Ananthagiri (V&M), Kodad, Suryapet (Dt). Telangana  
Pin: 508 206.

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 08-11-2019

### CIRCULAR

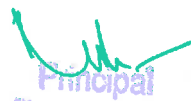
All the III B.Tech II Semester students are hereby informed that the Department of Computer Science and Engineering in collaboration with TASK is going to offer the following value added courses for the academic year 2019-20.

**Name of the Value Added Course:** Oracle Java Fundamentals.  
**Eligible Branches:** CSE and ECE

**Course Coordinator:**  
Ch.Vidya Sagar  
Training and Placement Officer,  
Mobile No: 9553122279

  
HOD

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Ananthagiri (V&M), Kodad, Suryapet (Dt). Telangana  
Pin: 508 206.

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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DATE: 12-11-2019

### CIRCULAR

All the III B.Tech II Semester students who are registered with **TASK** are hereby informed that the value added course entitled "**Oracle Java Fundamentals**" is scheduled from 25 November 2019 to 29 November 2019.

**Venue:** CSE Seminar Hall Block-A

**Timings:** 9:30 AM to 4:00 PM

**Resource Person Details:**

K.Ramesh,  
Technical Trainer,  
TASK.

  
HOD


**Encl:** List of students

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Ananthagiri (V&M), Suryapet (Dt). Pin: 508 206.

## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### VALUE ADDED/SKILL AND COMPETENCY DEVELOPMENT COURSES

#### About the Course

##### PERSONALITY DEVELOPMENT

Personality development is a cluster of productive personality traits that characterize one's relationships in a Milieu.

#### Purpose the Course

Personality development helps you develop an impressive personality and makes you stand apart from the rest. Personality development also plays an essential role in improving one's communication skills. Individuals ought to master the art of expressing their thoughts and feelings in the most desired way.

#### Eligibility

MBA

#### Prerequisites

- Keep a smart head and keep cool
- Don't copy anyone
- Create your own personal style
- Be an active Listener
- Learn to have patience and focus on your communication skills

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timmings	Fees
30	20	60	4:00 pm to 5:00 pm	NIL

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

### Course Objectives

The objective of the programme is to build self-confidence, enhance self-esteem and improve over all personality of the participants.

### Course Outline

Module 1	<ol style="list-style-type: none"><li>1. Introduction to Soft Skills</li><li>2. Break the ice berg – FEAR</li><li>3. Self Development – Etiquette &amp; Manners</li></ol>	6 Hours
Module 2	<ol style="list-style-type: none"><li>1. Communication Skills</li><li>2. Body Language</li><li>3. Time Management</li></ol>	6 Hours
Module 3	<ol style="list-style-type: none"><li>1. Positive Attitude &amp; Self Confidence</li><li>2. Personality Development</li></ol>	6 Hours
Module 4	<ol style="list-style-type: none"><li>1. Listening Skills</li><li>2. Written Communication</li><li>3. Human Values</li></ol>	6 Hours
Module 5	<ol style="list-style-type: none"><li>1. Speaking Skills</li></ol>	6 Hours

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 15-10-2019

### CIRCULAR

All the II MBA II Semester students are hereby informed that the department of Master of Business Administration is going to offer the following value added courses for the academic year 2019-20.


Name of the Value added course: Personality Development  
Eligible branches: MBA

Interested students can enrol their names with the course coordinator on or before 29-10-2019

Course coordinator:  
G.Varma  
Asst.Professor,  
MBA Dept.,  
Mobile No: 9989382096

  
HOD

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Ananthagiri (V&M), Kodad, Suryapet (Dt.) Telangana

Pin: 508 206.

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## DEPARTMENT OF MASTER OF BUSINESS ADMINISTRATION

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DATE: 11-11-2019

### CIRCULAR

All the II MBA II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "Personality Development" is scheduled from 15-11-2019 to 16-03-2020.

Venue: A-308, Block-A

Timings: 4:00 PM to 5:00 PM (Thursday & Friday)

Resource Person Details:

CH.Ramesh,

Asst. Professor,

MBA Department,

Mobile: 9553122280

  
HOD


Encl: List of students

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## DEPARTMENT OF H&S

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES, A.Y.: 2019-20

#### About the Course

##### CAMPUS RECRUITMENT TRAINING-ENGLISH

The course, with its interactive and need based modules, will address various challenges of communication as well as behavioural skills faced by individual at workplace and organizations in bridging the gaps through effective skills of interviews, group discussion, presentations, time management, goal setting and nuances of drafting resume, cover letter for sustainability in today's world.

#### Purpose of the Course

The purpose of the course is to develop insights into language and enrich the professional competence of the students and make the students industry ready.

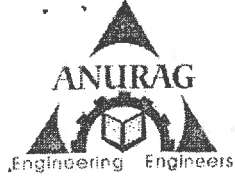
#### Eligibility

All the B.Tech III year students are eligible for this course.

#### Prerequisites

Students should have basic Communication Skills.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
30	40	-		-



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## DEPARTMENT OF H&S

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DATE: 15.11.2019

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of H&S is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: CAMPUS RECRUITMENT TRAINING-ENGLISH  
Eligible branches: All Branches


Interested students can enroll their names with the course coordinator on or before 22.11.2019

Course coordinator:  
D. Pandu Ranga Rao  
Asst.Professor,  
H&S Dept.,  
Mobile No: 9948123648

  
HOD

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Pin: 508 206.

## DEPARTMENT OF H&S

DATE: 23.11.2019

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "CAMPUS RECRUITMENT TRAINING-ENGLISH" is scheduled from **24 Nov 2019 to 16.03.2020**.

#### Resource Person Details:

Dr K Ramesh  
Asst. Professor,  
H&S Department,  
Mobile: 9912650052

J Poorna Kumar  
Asst. Professor,  
H&S Department,  
Mobile: 9553139384

  
HOD

**Encl:** List of students

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## DEPARTMENT OF H&S

### VALUE ADDED / SKILL AND COMPETENCY DEVELOPMENT COURSES, A.Y.: 2019-20

#### About the Course

##### CAMPUS RECRUITMENT TRAINING-MATHEMATICS

The course is designed for the students who are appearing in campus placements to improve their logical thinking and aptitude levels which are key points in campus placements.

#### Purpose of the Course

The purpose of the course is to improve logical thinking and aptitude levels of the students and make them industry ready.


#### Eligibility

All the B.Tech pursuing students are eligible for this course.

#### Prerequisites

Students should have basic arithmetic skills.

No. of contact Hours	Min number of Participants	Max Number of participants	Scheduled course Timings	Fees
35	20	80		-

  
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## DEPARTMENT OF H&S

### Course Objectives

1. Unique short cut techniques for mathematics to improve speed and accuracy
2. Unique short cut techniques for quantitative aptitude to improve speed and accuracy
3. To improve logical thinking to solve various questions and puzzles in reasoning.

### Course Outline

Module 1	NUMBER THEORY AND NUMBER SERIES	6hours
Module 2	ALGEBRA	4 hours
Module 3	AVERAGES, AGES, PROFIT & LOSS, PARTNERSHIP AND CLOCKS AND CALENDERS	7hours
Module 4	TIME & DISTANCE, TIME & WORK, PERCENTAGES, RATIO, PROPORTION SOLUTIONS & MIXTURES, SIMPLE INTEREST & COMPOUND INTEREST	10hours
Module 5	CODING-DECODING, SERIES COMPLETION, CUBES, BLOOD RELATIONS, DATA SUFFICIENCY	8hours

  
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## DEPARTMENT OF H&S

### Course Outcomes

1. Students are able to solve mathematical problems with speed and accuracy.
2. Students are able to solve quantitative aptitude problem with speed and accuracy.
3. Students improve logical thinking and solve various questions and puzzles in reasoning.

### Reference Books

1. A MODERN APPROACH TO VERBAL AND NON VERBAL REASONING BY R.S.AGARWAL.

Course Coordinator	Course resource person
T. Naga Raju, Assistant Professor Dept. Of H&S	Dr. P. Jyothi, Assistant Professor N.CH. Sridhar, Assistant Professor Dr. Y. Hari Krishna, Assistant Professor

  
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## DEPARTMENT OF H&S

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A.Y.: 2019-20

DATE: 15.11.2019

### CIRCULAR

All the III B.Tech II Semester students are hereby informed that the department of H&S is going to offer the following value added courses for the academic year 2019-20.

Name of the Value added course: CAMPUS RECRUITMENT TRAINING-MATHEMATICS  
Eligible branches: All Branches

Interested students can enroll their names with the course coordinator on or before 22.11.2019


Course coordinator:

T. Naga Raju  
Asst.Professor,  
H&S Dept.,  
Mobile No: 9491406080

  
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## DEPARTMENT OF H&S

A.Y.: 2019-20

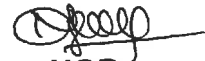
DATE: 23.11.2019

### CIRCULAR

All the III B.Tech II Semester students who have enrolled their names with course coordinator are hereby informed that the value added course entitled "CAMPUS RECRUITMENT TRAINING-MATHEMATICS" is scheduled from 24 Nov 2019 to 16 Mar 2020 as per time table.

#### Resource Person Details:


1. Dr. P. Jyothi  
Asst. Professor,  
H&S Department,  
Mobile: 9490486268
2. N.CH. Sridhar  
Asst. Professor,  
H&S Department,  
Mobile: 9441566662
3. Dr. Y. Hari Krishna  
Asst. Professor,  
H&S Department,  
Mobile: 9848271469

  
HOD

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