



ANURAG ENGINEERING COLLEGE

(Autonomous)

Ananthagiri (V&M), Suryapet (Dist) – 508206.
EXAMINATION BRANCH

TIME TABLE


Date: 08.08.2017

I.M.TECH I SEMESTER (R14) SUPPLEMENTARY EXAMINATIONS, AUG/SEP - 2017

Branch	28.08.2017 (Monday) 02:00 PM to 05:00 PM	30.08.2017 (Wednesday) 02:00 PM to 05:00 PM	01.09.2017 (Friday) 02:00 PM to 05:00 PM	04.09.2017 (Monday) 02:00 PM to 05:00 PM	06.09.2017 (Wednesday) 02:00 PM to 05:00 PM	08.09.2017 (Friday) 02:00 PM to 05:00 PM
Structural Engineering (20 – SE)	Computer Oriented Numerical Methods	Theory of Elasticity and Plasticity	Theory and Analysis of Plates	Advanced Reinforced Concrete Design	Concrete Technology	Advanced Structural Analysis
Computer Science And Engineering (58 – CSE)	Advanced Data Structures and Algorithms	Computer System Design	Advanced Operating Systems	Distributed Systems	Software Process Management and project Management	Software Architecture and Design Patterns
VLSI System Design (57 – VLSI SD)	Microcontrollers for Embedded System Design	CPLD & FPGA Architectures and Applications	VLSI Technology & Design	CMOS Analog Integrated Circuit Design	Digital System Design	CMOS Digital Integrated Circuit Design
Embedded Systems (55-ES)	Microcontrollers for Embedded System Design	Embedded Real Time Operating Systems	VLSI Technology & Design	Embedded System Design	Digital System Design	Embedded C
Wireless & Mobile Communications (65 – WMC)	Image and Video Processing	---	----	-----	Detection & Estimation Theory	-----

NOTE: The students should occupy their seats 10 min before the commencement of the Examination.

To
HODs (CE, ECE, CSE) to circulate among their concerned faculty and students.
Dept. Notice Board
Examination Branch Notice Board.


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EXAMINATION BRANCH

Date: 08.08.2017

TIME TABLE**I.M.TECH II SEMESTER (R15) REGULAR/SUPPLEMENTARY EXAMINATIONS, AUG/SEPT- 2017**

Branch	28.08.2017 (Monday) 10:00 AM to 01:00 PM	30.08.2017 (Wednesday) 10:00 AM to 01:00 PM	01.09.2017 (Friday) 10:00 AM to 01:00 PM	04.09.2017 (Monday) 10:00 AM to 01:00 PM	06.09.2017 (Wednesday) 10:00 AM to 01:00 PM	08.09.2017 (Friday) 10:00 AM to 01:00 PM
MACHINE DESIGN (15-MD)	Advanced Mechanics of Machinery	Mechanical Vibrations	Experimental Stress Analysis	Industrial Robotics (Core Elective-III)	Mechatronics (Core Elective-IV)	Advanced Optimization Techniques and Applications (Open Elective - II)
STRUCTURAL ENGINEERING (20 – SE)	Finite Element Methods	Structural Dynamics	Earth Quake Resistant Design of Buildings	Pre-Stressed Concrete (Core Elective-III)	Advanced Steel Design (Core Elective - IV)	Stability Of Structures (Open Elective - II)
POWER ELECTRONICS & ELECTRIC DRIVES (54 – PEED)	Power Electronic Converters - II	Neural Networks And Fuzzy Systems	Power Electronic Control Of AC Drives	Power Quality (Core Elective-III)	Flexible AC Transmission Systems (Core Elective-IV)	AI Techniques In Electrical Engineering (Open Elective-II)
VLSI SYSTEM DESIGN (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design For Testability	Digital Signal Processors & Architectures (Core Elective-III)	System On Chip Architecture (Core Elective-IV)	CAD For VLSI Circuits (Open Elective-II)
COMPUTER SCIENCE AND ENGINEERING (58 – CSE)	Advanced Network Programming	Advanced Databases	Web Services and Service Oriented Architecture	Advanced Data Mining (Core Elective-III)	E-Commerce (Core Elective-IV)	Semantic Web and Social Networks (Open Elective-II)

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A handwritten signature in blue ink, appearing to be 'Anurag', written over the printed name of the Principal.

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Structural Engineering (20 – SE)	Finite Element Methods	Structural Dynamics	Analysis And Design Of Shells And Folded Plates	Advanced. Steel Design	Pre-Stressed Concrete	Earth Quake Resistant Design Of Buildings
Computer Science And Engineering (58 – CSE)	Advanced Network Programming	Advanced Databases	Web Services And Service Oriented Architecture	Grid And Cloud Computing	--	Semantic Web And Social Networks
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CAD For VLSI Circuits	CMOS Mixed Signal Circuit Design	Design For Testability	Digital Signal Processors & Architectures	System On Chip Architecture
Power Electronics & Electric Drives (54 – PEED)	Power Electronic Converters-II	----	Power Electronic Control Of Ac Drives	-----	-----	-----
Electrical Power Systems (07-EPS)	Power System Dynamics	Flexible Ac Transmission Systems	Power System Operation And Deregulation	Ai Techniques In Electrical Power Engineering	Energy Auditing, Conservation And Management	Reactive Power Compensation And Management

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MACHINE DESIGN (15 – MD)	Advanced Mechanical Engineering Design	Advanced Mechanics of Solids	-----	Computational Methods in Engineering	---	---
STRUCTURAL ENGINEERING (20 – SE)	Computer Oriented Numerical Methods	Theory Of Elasticity And Plasticity	Soil Dynamics and Foundation Engineering	-----	----	Advanced Structural Analysis
VLSI SYSTEM DESIGN (57 – VLSI SD)	Microcontrollers for Embedded System Design	CPLD & FPGA Architectures and Applications	VLSI Technology & Design	CMOS Analog Integrated Circuit Design	Digital System Design	CMOS Digital Integrated Circuit Design
COMPUTER SCIENCE AND ENGINEERING (58 – CSE)	Advanced Data Structures And Algorithms	Computer System Design	Advanced Operating Systems	Distributed Systems	Software Process And Project Management	Software Architecture And Design Patterns

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