



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

I M. TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), JULY – 2018

TIME TABLE

TIME → AN: 02:00 PM TO 05:00 PM

Date : 02-07-2018

BRANCH	DATE AND DAY					
	18-07-2018 WEDNESDAY	20-07-2018 FRIDAY	23-07-2018 MONDAY	25-07-2018 WEDNESDAY	27-07-2018 FRIDAY	30-07-2018 MONDAY
Structural Engineering (20 – SE)	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	----	----	----	----
Machine Design (15 – MD)	Advanced Mechanics of Solids	Computational Methods in Engineering	----	----	----	----
Power Electronics and Electrical Drives (54 – PEED)	Machine Modeling and Analysis	Modern Control Theory	Power Electronic Control of DC Drives	Special Machines	HVDC Transmission	Power Electronic Converters-I
VLSI System Design (57 – VLSI SD)	VLSI Technology and Design	Digital System Design	Micro Controllers for Embedded System Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	CPLD and FPGA Architectures and Applications
Computer Science and Engineering (58 – CSE)	Advanced Operating Systems	Software Process and Project Management	Advanced Data Structures and Algorithms	Software Architecture and Design Patterns	----	----

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among their concerned Students and Faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

PRINCIPAL
PRINCIPAL
ANURAG ENGINEERING COLLEGE
(Autonomous)
Ananthagiri(V&M), Suryapet Dt., T.S.