



# ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

I M.Tech. I Sem. Supply. (R14)

## EXAMINATION BRANCH

### TIME TABLE

### I M.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R14), DEC-2018/JAN - 2019

TIME → AN: 10:00 AM TO 01:00 PM

Date : 10-12-2018

BRANCH	DATE AND DAY					
	21-12-2018 FRIDAY	27-12-2018 THURSDAY	29-12-2018 SATURDAY	31-12-2018 MONDAY	02-01-2019 WEDNESDAY	04-01-2019 FRIDAY
<b>Structural Engineering (20 - SE)</b>	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	Theory and Analysis of Plates	Advanced Reinforced Concrete Design	Concrete Technology	Advanced structural analysis
<b>VLSI System Design (57 - VLSI SD)</b>	VLSI Technology and Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	Digital System Design	Micro Controllers for Embedded System Design	CPLD and FPGA Architectures and Applications
<b>Embedded Systems (55 - ES)</b>	VLSI Technology and Design	Embedded C	Embedded Real Time Operating Systems	Digital System Design	Micro Controllers for Embedded System Design	Embedded System Design
<b>Wireless &amp; Mobile Communications (65 - WMC)</b>	Image And Video Processing	Advanced Data Communications	TCP/IP & ATM Networks	Advanced Digital Signal Processing	Detection & Estimation Theory	Satellite Communications
<b>Computer Science and Engineering (58 - CSE)</b>	Advanced Data Structures & Algorithms	Computer System Design	Advanced Operating Systems	Distributed Systems	Software Process and Project Management	Software Architecture and Design Patterns

**NOTE:** The students should occupy their seats 10 min before Commencement of the Examination.

To  
HODs to circulate among concerned Students and Faculty.  
Department Notice Board  
Examination Branch Notice Board  
Administrative Office

  
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