



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH II SEMESTER – SUPPLEMENTARY EXAMINATIONS (R14), DEC-2018/JAN – 2019

TIME → AN: 02:00 PM TO 05:00 PM

Date : 10-12-2018

BRANCH	DATE AND DAY					
	21-12-2018 FRIDAY	27-12-2018 THURSDAY	29-12-2018 SATURDAY	31-12-2018 MONDAY	02-01-2019 WEDNESDAY	04-01-2019 FRIDAY
Structural Engineering (20 – SE)	Finite Element Methods	Structural Dynamics	Earthquake Resistant Design of Buildings	Pre-stressed concrete	Advanced. Steel Design	Analysis and Design of Shells and Folded Plates
Electrical Power Systems (07-EPS)	Power System Dynamics	Flexible AC Transmission Systems	Power System Operation and Deregulation	AI Techniques in Electrical Power Engineering	Energy Auditing Conservation and Management	Reactive Power Compensation and Management
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design For Testability	Digital Signal Processors And Architectures	System On Chip Architecture	CAD For VLSI Circuits
Computer Science and Engineering (58 – CSE)	Advanced Network Programming	Advanced Databases	Web Services and Service Oriented Architecture	Grid and Cloud Computing	Advanced Data Mining	Semantic Web and Social Networks

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office



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