



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH II SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), DEC-2018/JAN – 2019

TIME → AN: 02:00 PM TO 05:00 PM

Date : 10-12-2018

BRANCH	DATE AND DAY					
	21-12-2018 FRIDAY	27-12-2018 THURSDAY	29-12-2018 SATURDAY	31-12-2018 MONDAY	02-01-2019 WEDNESDAY	04-01-2019 FRIDAY
Structural Engineering (20 – SE)	Finite Element Methods	Structural Dynamics	Earthquake Resistant Design of Buildings	Pre stressed concrete	Advanced. Steel Design	Stability of Structures
Machine Design (15 – MD)	Advanced Mechanics of Machinery	Mechanical Vibrations	Experimental Stress Analysis	Industrial Robotics	Mechatronics	Advanced Optimization Techniques and Applications
Power Electronics and Electrical Drives (54 – PEED)	Power Electronic Converters – II	Neural Networks and Fuzzy Systems	Power Electronic Control of AC Drives	Power Quality	Flexible AC Transmission Systems	AI Techniques in Electrical Engineering
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CAD for VLSI Circuits / Coding Theory and Techniques
Computer Science and Engineering (58 – CSE)	Advanced Network Programming	Advanced Databases	Web Services and Service Oriented Architecture	Advanced Data Mining	E-Commerce	Semantic Web and Social Networks

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

PRINCIPAL
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