



# ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

## EXAMINATION BRANCH

### TIME TABLE

### I M.TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R14), JUN – 2019

TIME→ AN: 02:00 PM TO 05:00 PM

Date : 15-04-2019

BRANCH	DATE AND DAY					
	17-06-2019 MONDAY	19-06-2019 TUESDAY	21-06-2019 FRIDAY	24-06-2019 MONDAY	26-06-2019 WEDNESDAY	28-06-2019 FRIDAY
<b>Structural Engineering (20 – SE)</b>	Computer Oriented Numerical Methods	Theory of Elasticity and Plasticity	Advanced Structural Analysis	Advanced Reinforced Concrete Design	Concrete Technology	Theory and Analysis of Plates
<b>VLSI System Design (57 – VLSI SD)</b>	VLSI Technology and Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	Digital System Design	Micro Controllers for Embedded System Design	CPLD and FPGA Architectures and Applications
<b>Embedded Systems (55 – ES)</b>	VLSI Technology and Design	Embedded C	Embedded Real Time Operating Systems	Digital System Design	Micro Controllers for Embedded System Design	Embedded System Design
<b>Wireless &amp; Mobile Communications (65 – WMC)</b>	Image And Video Processing	Advanced Data Communications	TCP/IP & ATM Networks	Advanced Digital Signal Processing	Detection & Estimation Theory	Satellite Communications
<b>Computer Science and Engineering (58 – CSE)</b>	Advanced Data Structures & Algorithms	Computer System Design	Advanced Operating Systems	Distributed Systems	Software Process and Project Management	Software Architecture and Design Patterns

**NOTE:** The students should occupy their seats 10 min before Commencement of the Examination.

To  
HODs to circulate among concerned Students and Faculty.  
Department Notice Board  
Examination Branch Notice Board  
Administrative Office

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