Date • 10-01-2020



## **ANURAG Engineering College**

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

## **EXAMINATION BRANCH**

## TIME TABLE

## I M.TECH II SEMESTER - SUPPLEMENTARY EXAMINATIONS (R14), JANUARY-2020 / FEBRUARY-2020

TIME > AN: 02:00 PM TO 05:00 PM

						Date: 10-01-2020
BRANCH	DATE AND DAY					
	28.01.2020 TUESDAY	30.01.2020 THURSDAY	01.02.2020 SATURDAY	03.02.2020 MONDAY	05.02.2020 WEDNESDAY	07.02.2020 FRIDAY
Structural Engineering (20 – SE)	Finite Element Methods	Structural Dynamics	Earthquake Resistant Design of Buildings	Pre-stressed concrete	Advanced. Steel Design	Analysis and Design of Shells and Folded Plates
Electrical Power Systems (07-EPS)	Power System Dynamics	Reactive Power Compensation and Management	Power System Operation and Deregulation	Energy Auditing Conversation and Management	Flexible AC Transmission Systems	AI Techniques in Electrical Power Engineering
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CAD for VLSI Circuits
Computer Science and Engineering (58 – CSE)	Advanced Network Programming	Advanced Databases	Web Services and Service Oriented Architecture	Grid and Cloud Computing	Semantic Web and Social Networks	

**NOTE:** The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

PRINCIPAL

PRINCIPAL Assurag Engineering College (Autonomous) Ananthogiri (V&M), Suryapet Dt., T.S.