



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.), T.S. India. PIN: 508 206.

I M.Tech. II Sem. Supply. (R15)

EXAMINATION BRANCH

TIME TABLE

I M.TECH II SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), JANUARY-2020 / FEBRUARY 2020

TIME → AN: 02:00 PM TO 05:00 PM

Date : 10-01-2020

BRANCH	DATE AND DAY					
	28.01.2020 TUESDAY	30.01.2020 THURSDAY	01.02.2020 SATURDAY	03.02.2020 MONDAY	05.02.2020 WEDNESDAY	07.02.2020 FRIDAY
Structural Engineering (20 – SE)	Finite Element Methods	Structural Dynamics	Earthquake Resistant Design of Buildings	Pre stressed concrete	Stability of Structures	----
Machine Design (15 – MD)	Mechanical Vibrations	Experimental Stress Analysis	Mechatronics	Advanced Optimization Techniques and Applications	----	----
Power Electronics and Electrical Drives (54 – PEED)	Power Electronic Converters – II	Neural Networks and Fuzzy Systems	Power Electronic Control of AC Drives	Power Quality	Flexible AC Transmission Systems	AI Techniques in Electrical Engineering
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CAD for VLSI Circuits / Coding Theory and Techniques	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CMOS Mixed Signal Circuit Design

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

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