



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), JANUARY-2020 / FEBRUARY 2020

TIME → FN: 10:00 AM TO 01:00 PM

Date : 10-01-2020

BRANCH	DATE AND DAY					
	28.01.2020 TUESDAY	30.01.2020 THURSDAY	01.02.2020 SATURDAY	03.02.2020 MONDAY	05.02.2020 WEDNESDAY	07.02.2020 FRIDAY
Structural Engineering (20 – SE)	Computer Oriented Numerical Methods	Theory of Elasticity and plasticity	----	----	----	----
Machine Design (15 – MD)	Advanced Mechanics of Solids	Computational Methods in Engineering	----	----	----	----
Power Electronics and Electrical Drives (54 – PEED)	Machine Modeling and Analysis	Modern Control Theory	Power Electronic Control of DC Drives	Special Machines	HVDC Transmission	Power Electronic Converters-I
VLSI System Design (57 – VLSI SD)	VLSI Technology and Design	Digital System Design	Micro Controllers for Embedded System Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	CPLD and FPGA Architectures and Applications
Computer Science and Engineering (58 – CSE)	Advanced Data Structures and Algorithms	Software Process and Project Management	Advanced Operating Systems	Software Architecture and Design Patterns	----	----

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among their concerned Students and Faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office

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