



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R14), OCTOBER – 2020


TIME → AN: 10:30 AM TO 12:30 PM

Date : 24-09-2020

BRANCH	DATE AND DAY					
	01.10.2020 Thursday	05.10.2020 Monday	07.10.2020 Wednesday	09.10.2020 Friday	13.10.2020 Tuesday	15.10.2020 Thursday
Structural Engineering (20 – SE)	Computer Oriented Numerical Methods	Theory of Elasticity and Plasticity	Advanced Structural Analysis	Advanced Reinforced Concrete Design	Concrete Technology	Theory and Analysis of Plates
VLSI System Design (57 – VLSI SD)	VLSI Technology and Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	Digital System Design	Micro Controllers for Embedded System Design	CPLD and FPGA Architectures and Applications
Embedded Systems (55 – ES)	VLSI Technology and Design	Embedded C	Embedded Real Time Operating Systems	Digital System Design	Micro Controllers for Embedded System Design	Embedded System Design
Wireless & Mobile Communications (65 – WMC)	Image And Video Processing	Advanced Data Communications	TCP/IP & ATM Networks	Advanced Digital Signal Processing	Detection & Estimation Theory	Satellite Communications
Computer Science and Engineering (58 – CSE)	Advanced Data Structures and Algorithms	Software Process and Project Management	Advanced Operating Systems	Software Architecture and Design Patterns	Computer System Design	Distributed Systems

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among concerned Students and Faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office


PRINCIPAL
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