



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH I SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), NOVEMBER – 2021

TIME → FN: 10:00 AM TO 01:00 PM

Date : 26.10.2021

BRANCH	DATE AND DAY					
	17.11.2021 WEDNESDAY	20.11.2021 SATURDAY	23.11.2021 TUESDAY	25.11.2021 THURSDAY	27.11.2021 SATURDAY	30.11.2021 TUESDAY
Power Electronics and Electrical Drives (54 – PEED)	Machine Modeling and Analysis	Modern Control Theory	Power Electronic Control of DC Drives	Special Machines	HVDC Transmission	Power Electronic Converters-I
VLSI System Design (57 – VLSI SD)	VLSI Technology and Design	Digital System Design	Micro Controllers for Embedded System Design	CMOS Analog Integrated Circuit Design	CMOS Digital Integrated Circuit Design	CPLD and FPGA Architectures and Applications

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among their concerned Students and Faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office


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