



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH II SEMESTER – REGULAR / SUPPLEMENTARY EXAMINATIONS (R15), NOVEMBER – 2021


TIME → FN: 10:00 AM TO 01:00 PM

Date : 26.10.2021

BRANCH	DATE AND DAY					
	16.11.2021 TUESDAY	18.11.2020 THURSDAY	22.11.2021 MONDAY	24.11.2021 WEDNESDAY	26.11.2021 FRIDAY	29.11.2021 MONDAY
Power Electronics and Electrical Drives (54 – PEED)	Power Electronic Converters – II	Neural Networks and Fuzzy Systems	Power Electronic Control of AC Drives	Power Quality	Flexible AC Transmission Systems	AI Techniques in Electrical Engineering
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CAD for VLSI Circuits / Coding Theory and Techniques Adhoc Wireless Networks /

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office


PRINCIPAL
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