



RESULTS


Branch: POWER ELECTRONICS & ELECTRICAL DRIVES Course/Sem: I M.TECH I SEMESTER Regular Examinations (R15)

Held in: July 2021

Roll No SGPA CGPA

	Roll No	SGPA	CGPA	A31023	A31024	A31025	A31026	A31031	A31033	A31205	A31206
				MACHINE MODELING AND ANALYSIS	MODERN CONTROL THEORY	POWER ELECTRONIC CONTROL OF DC	SPECIAL MACHINES	HVDC TRANSMISSION	POWER ELECTRONIC CONVERTERS-I	POWER CONVERTERS LAB	SEMINAR
				Grade	Grade	Grade	Grade	Grade	Grade	Grade	Grade
20C11D5401	9.29	9.29	A+	A	O	A+	A+	O	O	O	O
20C11D5402			B+	F	A+	A	B+	B+	O	O	O
20C11D5403	9.21	9.21	A+	A+	O	O	A+	A	O	A+	A+
20C11D5404	8.93	8.93	O	A	A+	O	A	A	O	O	A+
20C11D5405	8.71	8.71	A	A	A	O	A+	A	O	O	O
20C11D5406	9.00	9.00	A	O	A+	A	A	O	O	O	O
20C11D5407			B+	A	F	B	F	B+	O	O	O
20C11D5408			A	F(Ab)	A	A+	F(Ab)	F(Ab)	O	O	O


Controller of Examination
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Anurag Engineering College
(Autonomous)
Ananthagiri(V&M), Suryapet Dist., T.S


Principal
Anurag Engineering College
(Autonomous)
Ananthagiri(V&M), Suryapet Dist., T.S


JNTUH Nominee



RESULTS

Branch: VLSI SYSTEM DESIGN

Course/Sem: I M.TECH I SEMESTER Regular Examinations (R15)

Held in: July 2021

Roll No SGPA CGPA

			A31035	A31036	A31037	A31038	A31042	A31044	A31207
			VLSI TECHNOLOGY AND DESIGN	CMOS ANALOG INTEGRATED CIRCUIT DESIGN	CMOS DIGITAL INTEGRATED CIRCUIT DESIGN	DIGITAL SYSTEM DESIGN	MICRO CONTROLLERS FOR EMBEDDED	CPLD AND FPGA ARCHITECTURES AND	VLSI LABORATORY - I
			Grade	Grade	Grade	Grade	Grade	Grade	Grade
20C11D5701			A	F	A+	O	A+	F	O
20C11D5702			F	F	F	F	F	F	A+
20C11D5703			A	F	F	F	A	F	A+

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20/07/21