



ANURAG Engineering College

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206.

EXAMINATION BRANCH

TIME TABLE

I M.TECH II SEMESTER – SUPPLEMENTARY EXAMINATIONS (R15), APRIL – 2022

TIME → AN: 02:00 PM TO 05:00 PM

Date : 29.03.2022

BRANCH	DATE AND DAY					
	19.04.2022 TUESDAY	21.04.2022 THURSDAY	23.04.2022 SATURDAY	25.04.2022 MONDAY	27.04.2022 WEDNESDAY	29.04.2022 FRIDAY
Power Electronics and Electrical Drives (54 – PEED)	Power Electronic Converters – II	Neural Networks and Fuzzy Systems	Power Electronic Control of AC Drives	Power Quality	Flexible AC Transmission Systems	AI Techniques in Electrical Engineering
VLSI System Design (57 – VLSI SD)	Low Power VLSI Design	CMOS Mixed Signal Circuit Design	Design for Testability	Digital Signal Processors and Architectures	System On Chip Architecture	CAD for VLSI Circuits / Coding Theory and Techniques / Adhoc Wireless Networks

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

To
HODs to circulate among the concerned students and faculty.
Department Notice Board
Examination Branch Notice Board
Administrative Office


PRINCIPAL
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