

RESULTS

Branch: POWER ELECTRONICS & ELECTRICAL DRIVES Course/Sem: I M.TECH II SEMESTER Regular Examinations (R15)

Held in: November 2021

Roll No SGPA CGPA

	Roll No	SGPA	CGPA	A32023	A32024	A32025	A32026	A32029	A32033	A32205	A32206
				POWER ELECTRONIC CONVERTERS - II	NEURAL NETWORKS AND FUZZY SYSTEMS	POWER ELECTRONIC CONTROL OF AC	POWER QUALITY	FLEXIBLE AC TRANSMISSION SYSTEMS	AI TECHNIQUES IN ELECTRICAL ENGINEERING	ELECTRICAL SYSTEM SIMULATION LAB	SEMINAR
				Grade	Grade	Grade	Grade	Grade	Grade	Grade	Grade
20C11D5401	9.57	9.43	A+	A+	O	A+	O	O	O	O	O
20C11D5402	8.71	8.43	A	A+	A	A	A	O	O	O	O
20C11D5403	9.86	9.54	O	O	O	A+	O	O	O	O	O
20C11D5404	9.43	9.18	A	A+	O	A+	O	O	O	O	O
20C11D5405	9.43	9.07	A+	O	A	A+	O	O	O	O	O
20C11D5406	9.86	9.43	A+	O	O	O	O	O	O	O	O
20C11D5407	8.86	8.36	A	A+	A+	A	A+	A+	A+	O	O
20C11D5408	9.43	9.21	A+	A+	O	A+	A+	A+	O	O	O

Letter Grade Indicates Grade Points (O - 10.00, A+ - 9.00, A - 8.00, B+ - 7.00, B - 6.00, F - 0.)

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Page 2 of 2


Branch: VLSI SYSTEM DESIGN


Course/Sem: I M.TECH II SEMESTER Regular Examinations (R15)

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Roll No SGPA CGPA

	Roll No	SGPA	CGPA	A32035	A32036	A32037	A32039	A32042	A32046	A32207	A32208
				LOW POWER VLS DESIGN	CMOS MIXED SIGNAL CIRCUIT DESIGN	DESIGN FOR TESTABILITY	DIGITAL SIGNAL PROCESSORS AND	SYSTEM ON CHIP ARCHITECTURE	ADHOC WIRELESS NETWORKS	VLSI LABORATORY - II	SEMINAR
				Grade	Grade	Grade	Grade	Grade	Grade	Grade	Grade
20C11D5701		8.00		A+	A	B	B+	A	A	O	O
20C11D5702				F(Ab)	F(Ab)	F(Ab)	F(Ab)	F(Ab)	F(Ab)	F(Ab)	F
20C11D5703		8.14		A	A	A	B+	A+	A	A+	A+


Controller of Examinations
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JNTUH Nominee