





## RESULTS

Branch: POWER ELECTRONICS & ELECTRICAL DRIVES Course/Sem: I M.TECH I SEMESTER Supplementary Examinations (R15)

Held in: November 2021

Roll No	SGPA	CGPA	A31024	A31025	A31031	A31033
			MODERN CONTROL THEORY	POWER ELECTRONIC CONTROL OF DC	HVDC TRANSMISSION	POWER ELECTRONIC CONVERTERS-I
			Grade	Grade	Grade	Grade
20C11D5402	8.14	8.14	A+			
20C11D5407	7.86	7.86		A+	A	
20C11D5408	9.00	9.00	A+		O	A+

  
**Controller of Examinations**  
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Anurag Engineering College  
(Autonomous)  
Ananthagiri (V&M), Suryapet Dist., T.S

  
**Principal**  
PRINCIPAL  
Anurag Engineering College  
(Autonomous)  
Ananthagiri (V&M), Suryapet Dist., T.S

  
**JNTUH Nominee**



**RESULTS**

Branch: VLSI SYSTEM DESIGN

Course/Sem: I M.TECH I SEMESTER Supplementary Examinations (R15)

Held in: November 2021

Roll No      SGPA      CGPA

Roll No	SGPA	CGPA	A31035	A31036	A31037	A31038	A31042	A31044
			VLSI TECHNOLOGY AND DESIGN	CMOS ANALOG INTEGRATED CIRCUIT DESIGN	CMOS DIGITAL INTEGRATED CIRCUIT DESIGN	DIGITAL SYSTEM DESIGN	MICRO CONTROLLERS FOR EMBEDDED	CPLD AND FPGA ARCHITECTURES AND
			Grade	Grade	Grade	Grade	Grade	Grade
20C11D5701				F(Ab)				A
20C11D5702			F(Ab)	F(Ab)	F(Ab)	F(Ab)	F(Ab)	F(Ab)
20C11D5703				F(Ab)	A	A		B+

  
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