




RESULTS


Branch: VLSI SYSTEM DESIGN

Course/Sem: I M.TECH I SEMESTER Supplementary Examinations (R15)

Held in: April 2022

Roll No	SGPA	CGPA	
			A31036
			CMOS ANALOG INTEGRATED CIRCUIT DESIGN
			Grade
20C11D5703	7.62	7.62	B


Controller of Examinations
Controller of Examinations
Anurag Engineering College
(Autonomous)
Ananthagiri(V&M), Suryapet Dist., T.S


Principal
PRINCIPAL
Anurag Engineering College
(Autonomous)
Ananthagiri(V&M), Suryapet Dist T.S.


JNTUH Nominee