Data : 03 09 2022



## **ANURAG Engineering College**

(Autonomous)

Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206

## **EXAMINATION BRANCH**

## T I M E T A B L E II B.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R14), SEP - 2022

TIME > AN: 02:00 PM TO 05:00 PM

| TIME / AM. UZ.U                                   | 0 PM 10 05:00 PM              |                          |                                  |  |                              | Date: 03.09.2022                            |
|---|-------------------------------|--------------------------|----------------------------------|--|------------------------------|---|
| BRANCH  | DATE AND DAY                  |                          |                                  |  |                              |   |
|   | 17.09.2022<br>SATURDAY        | 19.09.2022<br>MONDAY     | 21.09.2022<br>WEDNESDAY          | 23.09.2022<br>FRIDAY                       | 26.09.2022<br>MONDAY         | 28.09.2022<br>WEDNESDAY                     |
| CIVIL<br>ENGINEERING (01)                         | Mathematics - III             | Environmental<br>Studies | Strength of<br>Materials – I     | Electrical &<br>Electronics<br>Engineering | Fluid Mechanics              | Surveying – I                               |
| ELECTRICAL AND<br>ELECTRONICS<br>ENGINEERING (02) | Mathematics - IV              | Network Theory           | Electronic Devices & Circuits    | Electro Magnetic<br>Fields                 | Electrical Machines<br>- I   | Fluid Mechanics &<br>Hydraulic<br>Machinery |
| MECHANICAL<br>ENGINEERING (03)                    | Mathematics - III             | Environmental<br>Studies | Metallurgy &<br>Material Science | Electrical &<br>Electronics<br>Engineering | Mechanics of Solids          | Thermodynamics                              |
| ELECTRONICS AND COMMUNICATION ENGINEERING (04)    | Mathematics – IV              | Environmental<br>Studies | Electronic Circuit<br>Analysis   | Probability Theory & Stochastic Process    | Computer<br>Programming - II | Switching Theory<br>and Logic Design        |
| COMPUTER<br>SCIENCE AND<br>ENGINEERING (05)       | Probability and<br>Statistics | Digital Logic Design     | Electronic Devices & Circuits    | Discrete<br>Mathematics                    | Data Structures              | Object Oriented<br>Programming              |

**NOTE:** The students should occupy their seats 10 min before Commencement of the Examination.

Controller of Examinations

Controller of Examinations ANURAG ENGINEERING COLLEGIO (Autonomous)

Ananthagiri(V&M), Suryapet Dt. T.

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То

HODs to circulate among concerned Students and Faculty.

Department Notice Board

**Examination Branch Notice Board**