

ANURAG Engineering College

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001: 2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)
Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



EXAMINATION BRANCH

TIME TABLE

II B.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R18), JUL / AUG - 2023

TIME→ FN: 10:00 AM TO 01:00 PM

| $TIME \rightarrow FN: 10:0$ | 00 AM TO 01:00 PM | | | | | Date: 26.06.2023 |
|------------------------------------------------------|---------------------------------------------------------|------------------------------------------------------|------------------------------------|----------------------------------------------------|--------------------------------|--------------------------------------|
| BRANCH | DATE AND DAY | | | | | |
| | 21.07.2023 FRIDAY | 24.07.2023 MONDAY | 26.07.2023 WEDNESDAY | 28.07.2023 FRIDAY | 01.08.2023 TUESDAY | 03.08.2023 THURSDAY |
| CIVIL ENGINEERING (01) | Probability and Statistics | Building Materials, Construction & Planning | Strength of Materials – I | Fluid Mechanics | Engineering Geology | Surveying & Geomatics |
| ELECTRICAL AND ELECTRONICS ENGINEERING (02) | Mathematics –III /Basic Electrical Engineering | Power Systems-I | Electronic Devices and Circuits | Electromagnetic Fields | Electrical Circuit Analysis | Control Systems |
| MECHANICAL ENGINEERING (03) | Numerical Methods &Partial Differential Equations | Environmental Sciences | Metallurgy and Material Science | Basic Electrical and Electronics Engineering | Mechanics of Solids | Thermodynamics |
| ELECTRONICS AND COMMUNICATION ENGINEERING (04) | Mathematics – III | Environmental Sciences | Electronic Devices and Circuits | Probability Theory & Stochastic Processes | Signals & Systems | Switching Theory and Logic Design |
| COMPUTER SCIENCE AND ENGINEERING (05) | Probability and Statistics | Digital Logic Design | Electronic Devices and Circuits | Discrete Mathematical Structures | Data Structures | Object Oriented Programming |

NOTE: The students should occupy their seats 10 minutes before Commencement of the Examination.

Controller of Examinations
Controller of Examination
ANURAG ENGINEERING COLLEGE
(Autonomous)

Ananthagirl(V&M), Survaper Dt. T.5

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HODs to circulate among their II B.Tech. I Semester and backlog Students and Faculty. Department Notice Board Examination Branch Notice Board