

ANURAG Engineering College

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001: 2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)
Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



EXAMINATION BRANCH

<u>TIME TABLE</u> II B.TECH I SEMESTER – REGULAR EXAMINATIONS (R22), JAN – 2024

TIME \rightarrow FN: 10:00 AM TO 01:00 PM Date: 06.01.2024 DATE AND DAY **BRANCH** 19.01.2024 24.01.2024 27.01.2024 30.01.2024 22.01.2024 **FRIDAY** WEDNESDAY **SATURDAY TUESDAY MONDAY** Building Materials, CE (01) Probability & Statistics Construction and **Engineering Geology** Strength of Materials - I Fluid Mechanics Planning Numerical Methods and **EEE (02)** Power System - I **Analog Electronic Circuits** Electromagnetic Fields Electrical Machines – I Complex Variables Numerical Methods and Network Analysis and **ECE (04)** Signals and Systems **Analog Circuits** Digital Logic Design Complex Variables **Synthesis** Object Oriented Computer Organization Programming through **CSE (05)** Probability & Statistics **Digital Electronics Data Structures** and Architecture **JAVA** Computer Organization IT (12) Probability & Statistics **Digital Electronics Data Structures** Introduction to IOT and Microprocessor Computer Organization CSE -Probability & Statistics Software Engineering **Data Structures Operating Systems AIML (66)** and Architecture

NOTE: The students should occupy their seats 10 minutes before Commencement of the Examination.

CONTROLLER OF EXAMINATIONS

Controller of Examinations
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HODs to circulate among their II B.Tech. I Semester and backlog Students and Faculty.

Department Notice Board

To

Examination Branch Notice Board

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