

## **ANURAG Engineering College**

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001 : 2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)
Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



## **EXAMINATION BRANCH**

## TIME TABLE

## II B.TECH II SEMESTER - SUPPLEMENTARY EXAMINATIONS (R14), JUN / JUL - 2024

TIME→ FN: 10:00 AM TO 01:00 PM Date: 11.06.2024

| I I II I I I | . 10.00 HM 10 01.0                      | V 1 1/1   |   |                                      |   |                                |
|--------------|---|---|---|--------------------------------------|---|--------------------------------|
| BRANCH       | DATE AND DAY                            |   |   |                                      |   |                                |
|              | 26.06.2024<br>WEDNESDAY                 | 28.06.2024<br>FRIDAY                              | 01.07.2024<br>MONDAY                                | 03.07.2024<br>WEDNESDAY              | 05.07.2024<br>FRIDAY                                | 08.07.2024<br>MONDAY           |
| CE (01)      | Probability and<br>Statistics           | Surveying - II                                    | Strength of Materials<br>- II                       | Hydraulics and<br>Hydraulic Machines | Building Materials,<br>Construction and<br>Planning |                                |
| EEE (02)     | Electronic Circuits                     | Managerial<br>Economics and<br>Financial Analysis | Power Systems - I                                   | Control Systems                      | Environmental<br>Studies                            | Electrical Machines -          |
| ME (03)      | Probability and<br>Statistics           | Production<br>Technology                          | Kinematics of<br>Machinery                          | Thermal Engineering - I              | Mechanics of Fluids<br>and Hydraulic<br>Machines    | Machine Drawing                |
| ECE (04)     | Principles of<br>Electrical Engineering | Structured Digital<br>System Design               | Electromagnetic Theory<br>and Transmission<br>Lines | Signals and Systems                  |   | Pulse and Digital<br>Circuits  |
| CSE (05)     | Java Programming                        | Formal Languages<br>and Automata Theory           | Computer<br>Organization                            | Software Engineering                 | Environmental<br>Studies                            | Database<br>Management Systems |

NOTE: The students should occupy their seats 10 minutes before Commencement of the Examination.

CONTROLLER OF EXAMINATIONS

Controller of Examinations
ANURAG ENGINEERING COLLEGE
(Autonomous)

Ananthagin(V&M), Survapet Dt. T\$

PRINCIPAL
ANURAG ENGINEERING COLLEGE
(Autonomous)
Ananthagiri(V&M), Suryapet Dt.,T.\$.

To HODs to inform all the Students having backlogs under R14 Regulation and Faculty. Department Notice Board Examination Branch Notice Board

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