

## **ANURAG Engineering College**

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001 : 2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)
Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



## **EXAMINATION BRANCH**

## TIME TABLE

## II B.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R18), JUL/AUG - 2024

| TIME $\rightarrow$ FN: | 10:00 AM TO 01:00 PM |               | Date: 11.06.2024 |
|------------------------|----------------------|---------------|------------------|
|                        |                      | DAME AND DATE |                  |

| BRANCH   | DATE AND DAY  |   |                                    |  |                                |                                      |  |
|----------|---|---|------------------------------------|--|--------------------------------|--------------------------------------|--|
|          | 19.07.2024<br>FRIDAY                                    | 22.07.2024<br>MONDAY                              | 24.07.2024<br>WEDNESDAY            | 26.07.2024<br>FRIDAY                               | 30.07.2024<br>TUESDAY          | 01.08.2024<br>THURSDAY               |  |
| CE (01)  | Probability and<br>Statistics                           | Building Materials,<br>Construction &<br>Planning | Strength of Materials – I          | Fluid Mechanics                                    | Engineering Geology            | Surveying &<br>Geomatics             |  |
| EEE (02) | Mathematics –III<br>/Basic Electrical<br>Engineering    | Power Systems-I                                   | Electronic Devices and Circuits    | Electromagnetic<br>Fields                          | Electrical Circuit<br>Analysis | Control Systems                      |  |
| ME (03)  | Numerical Methods<br>&Partial Differential<br>Equations | Environmental<br>Sciences                         | Metallurgy and<br>Material Science | Basic Electrical and<br>Electronics<br>Engineering | Mechanics of Solids            | Thermodynamics                       |  |
| ECE (04) | Mathematics - III                                       | Environmental<br>Sciences                         | Electronic Devices and Circuits    | Probability Theory & Stochastic Processes          | Signals & Systems              | Switching Theory and<br>Logic Design |  |
| CSE (05) | Probability and<br>Statistics                           | Digital Logic Design                              | Electronic Devices and Circuits    | Discrete<br>Mathematical<br>Structures             | Data Structures                | Object Oriented<br>Programming       |  |

NOTE: The students should occupy their seats 10 minutes before Commencement of the Examination.

CONTROLLER OF EXAMINATIONS

Controller of Exammaticae
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HODs to circulate among their II B.Tech. I Semester and backlog Students and Faculty.

Department Notice Board

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**Examination Branch Notice Board** 

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