

ANURAG Engineering College

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001:2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad) Ananthagiri (V & M), Kodad, Survapet (Dt.), T.S. India, PIN: 508 206



Date: 13.12.2024

EXAMINATION BRANCH

TIME TABLE

III B.TECH I SEMESTER - REGULAR EXAMINATIONS (R22). DEC - 2024 / JAN - 2025

TIME > FN: 09:30 AM TO 12:30 PM

DATE AND DAY 28.12.2024 31.12.2024 02.01.2025 08.01.2025 04.01.2025 06.01.2025 **BRANCH** MONDAY WEDNESDAY SATURDAY SATURDAY TUESDAY THURSDAY Structural **Business Economics Transportation** Water Resources Structural Analysis-Geotechnical Engineering -CE (01) Engineering - I Engineering &Financial Analysis Engineering II I (RCC) Microprocessors & **Business Economics** Signals and Systems **Power Electronics Linear Control Systems EEE (02)** Microcontrollers &Financial Analysis **Data Communications** and Computer Antennas and Wave IOT Architectures and Networks/ Electronic Microcontrollers Control Systems **ECE (04) Propagation Protocols** Measurements and Instrumentation Scripting Languages Distributed Databases/ Design and Analysis Information Retrieval /Principles of Programming Computer Networks **DevOps** CSE (05) of Algorithms Languages Systems Design and Analysis **Automata and Compiler Embedded Systems Data Mining** Distributed Databases IT (12) of Algorithms Design **Business Economics**

&Financial Analysis

Machine Learning

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

Computer Networks

Controller of Examinations Controller of Neething at

CSE-

Al&ML (66)

HODs to circulate among concerned Students and Faculty. Department Notice Board

Design and Analysis

of Algorithms

Examination Branch Notice Board

ANURAG ENGINEERING COLLEGE (Autonomous) Ananthagiri(V&M), Survanet Dt. T.S.

Web Programming