ANURAG Engineering College

(An Autonomous Institution)

II B.Tech I Semester Regular/Supplementary Examinations, December–2024 COMPUTER ORGANIZATION AND MICROPROCESSOR (INFORMATION TECHNOLOGY)

Time: 3	Hours	Max. Marks: 60		
	Section – A (Short Answer type questions) r All Questions	Course Outcome	(10 B.T Level	Marks) Marks
1.	Define Digital Computer.	CO1	L1	1M
2.	List out any four memory reference instructions.	CO1	L1	1M
3.	Define memory segmentation.	CO2	L1	1M
4.	Draw the read cycle time diagram for minimum mode.	CO2	L1	1M
5.	What is macros and write syntax of macros.	CO3	L1	1M
6.	What is stack pointer in 8086.	CO3	L1	1M
7.	Draw the hardware for signed-magnitude addition and subtraction.	CO4	L1	1M
8.	Define priority interrupt.	CO4	L1	1M
9.	Draw the memory hierarchy in a computer system.	CO5	L1	1M
10.	List out four major groups of the flynn's classification of computers.	CO5	L1	1M
	Section B (Essay Questions)			
Answei	all questions, each question carries equal marks.	(5	X 10M	= 50M)
11. A)		CO1	L2	10M
	OR			
B)	Explain Control unit of basic computer with neat diagram.	CO1	L2	10M
12. A)	Explain 8086 Processor Architecture.	CO2	L2	10M
	OR			
B)	Differentiate between Minimum and Maximum mode of operations in microprocessors.	CO2	L3	10M
13. A)	Write an Assembly Language Program to input characters until 'q' and display.	CO3	L3	10M
	OR			
B)	Explain stack Instructions with example.	CO3	L2	10M
14. A)	Explain the booth multiplication Algorithm.	CO4	L2	10M
5)	OR	001		4.03.6
В)	Write a short note on i) strobe control ii) handshaking	CO4	L2	10M
15. A)	Explain RAM chip design with its block diagram and functional	CO5	L2	10M
	table.			
	OR			
B)	Explain about four segment CPU pipeline with neat diagram.	CO5	L2	10 M