## **ANURAG Engineering College**

(An Autonomous Institution)

## II B.Tech II Semester Supplementary Examinations, December – 2024 ANALOG AND DIGITAL COMMUNICATIONS (ELECTRONICS & COMMUNICATION ENGINEERING)

Time: 3 Hours		Max. Marks: 60		
Section – A (Short Answer type questions) Answer All Questions		Course	B.T	Marks) Marks
1.	What are the needs of Modulation.	Outcome CO1	Level	13.4
2.		CO1	L1 L1	1M 1M
3.	Compare NBFM and WBFM.	CO2	L1	1M
4.		CO2	L1	1M
5.	11 11 11 11 11 11 11 11 11 11 11 11 11	CO3	L1	1M
6.	8 1 · 1 · · · · · · · · · · · · · · ·	CO3	L1	1M
7.	1	CO4	L1	1M
8.	1 8	· CO4	L1	1M
9.	8- 31 = 1 311.	CO5	L1	1M
10.	Define ISI.	CO5	L1	1M
	Section B (Essay Questions)			
Answer all questions, each question carries equal marks.		$(5 \times 10M = 50M)$		
	Explain the generation of AM signals using switching modulator.  OR	CO1	L2	10M
B)	Discuss how a SSB wave is generated using phase discriminator method with only USB and rejecting the LSB.	CO1	L3	10M
12. A)	signal using relevant diagrams.	CO2	L2	10M
77)	OR			
В)	Explain PLL Method of demodulating FM signals with a neat block diagram.	CO2	L2	10M
13. A)	Explain the operation of Direct FM Transmitter with block Diagram.	CO3	L2	10M
	OR			
B)	Explain about Superheterodyne receiver with the help of block Diagram.	CO3	L2	10M
14. A)	Explain the generation and Demodulation of PWM signals  OR	CO4	L2	10M
B)	Derive the expression for overall SNR in PCM system	CO4	L3	10M
15. A)	Explain the operation of the binary PSK modulator and demodulator.  OR	CO5	L2	10M
B)	Discuss in detail the operation of QPSK modulator and demodulator with its phasor diagram	CO5	L2	10M

