

ANURAG Engineering College

(An Autonomous Institution)

III B.Tech I Semester Regular Examinations, December – 2024

MICROCONTROLLERS**(ELECTRONICS AND COMMUNICATION ENGINEERING)****Time: 3 Hours****Max. Marks: 60****Section – A (Short Answer type questions)****(10 Marks)****Answer All Questions**

	Course Outcome	B.T Level	Marks
1. Define EVEN assembly directive	CO1	L1	1M
2. Define offset.	CO1	L1	1M
3. Write about PSEN pin in 8051	CO2	L1	1M
4. Define PCON register	CO2	L2	1M
5. Write four features of UART.	CO3	L1	1M
6. What is the use of serial communication over parallel communication	CO3	L2	1M
7. Write the difference between user mode and supervisory mode in ARM processor.	CO4	L1	1M
8. Write about CPSR.	CO4	L1	1M
9. What is the use of debug interface in CORTEX processor?	CO5	L1	1M
10. What is use of CAN?	CO5	L2	1M

Section B (Essay Questions)**Answer all questions, each question carries equal marks.****(5 X 10M = 50M)**

11. A) Explain architecture of 8086 with neat diagram.	CO1	L2	10M
OR			
B) Explain minimum mode of 8086 with neat diagram	CO1	L2	10M
12. A) Explain timer modes of 8051	CO2	L2	10M
OR			
B) Explain TCON and TMOD registers	CO2	L2	10M
13. A) Explain LCD interfacing with 8051.	CO3	L2	10M
OR			
B) Explain features and pin diagram of RS-232.	CO3	L2	10M
14. A) Explain branching instructions of ARM processor	CO4	L3	10M
OR			
B) Explain ARM processor thumb architecture	CO4	L3	10M
15. A) Explain CORTEX processor architecture.	CO5	L3	10M
OR			
B) Explain OMP architecture with neat diagram.	CO5	L2	10M