

ANURAG Engineering College

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001:2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad)
Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



Date: 14 05 2025

EXAMINATION BRANCH

TIME TABLE

III B.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R15), JULY - 2025

TIME > FN: 09:30 AM TO 12:30 PM

TIME > FN: 09:30	AM TO 12:30 PM					Date: 14.05.2025
BRANCH	DATE AND DAY					
	01.07.2025 TUESDAY	03.07.2025 THURSDAY	05.07.2025 SATURDAY	08.07.2025 TUESDAY	10.07.2025 THURSDAY	14.07.2025 MONDAY
ELECTRICAL AND ELECTRONICS ENGINEERING (02)	Power Systems – II	Electrical Machines – III	Renewable Energy Sources / Computer Organization / Signals and Systems	Power Electronics	Management Science	Linear IC Applications
MECHANICAL ENGINEERING (03)	Design of Machine Members – I	Thermal Engineering – II	Automobile Engineering	Managerial Economics and Financial Analysis	Dynamics of Machinery	Machine Tools and Metrology
ELECTRONICS AND COMMUNICATION ENGINEERING (04)	Computer Organization and Operating System	Analog Communications	Artificial Neural Networks / Electronic Measurements and Instrumentation	Antennas and Wave Propagation	Control Systems Engineering	Linear IC Applications
COMPUTER SCIENCE AND ENGINEERING (05)	Computer Networks	Microprocessors and Microcontrollers	Human Computer Interaction / Mobile Computing	Managerial Economics and Financial Analysis	Operating Systems	Formal Languages and Automata Theory

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

Controller of Examinations

To Controller of Exminations
HODANDRAGIEN BILLER Students having backlogs under R15 Regulation (2017 Regular Students) and Faculty.

Department Notice (Brancous)
Examination practice (Notice Board)

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