

ANURAG Engineering College

An Autonomous Institution, Accredited by NAAC with A+ Grade, ISO 9001:2015 Institution (Approved by AICTE, New Delhi, Affiliated to JNTUH, Hyderabad) Ananthagiri (V & M), Kodad, Suryapet (Dt.). T.S. India. PIN: 508 206



Date: 14 05 2025

EXAMINATION BRANCH

TABLE TIME

III B.TECH I SEMESTER - SUPPLEMENTARY EXAMINATIONS (R22), JULY - 2025

TIME -> EN. 00.20 AM TO 12.20 DM

TIME > FN:	09:30 AM TO 12:30	PIVI			Dat	e: 14.03.2023
BRANCH	DATE AND DAY					
	01.07.2025 TUESDAY	03.07.2025 THURSDAY	05.07.2025 SATURDAY	08.07.2025 TUESDAY	10.07.2025 THURSDAY	14.07.2025 MONDAY
EEE (02)	Power Electronics	Linear Control Systems	Microprocessors & Microcontrollers	Business Economics &Financial Analysis	Signals and Systems	
ECE (04)	Microcontrollers	IOT Architectures and Protocols	Control Systems	Antennas and Wave Propagation	Data Communications and Computer Networks/ Electronic Measurements and Instrumentation	
CSE (05)	Design and Analysis of Algorithms	Computer Networks	DevOps	Scripting Languages /Principles of Programming Languages	Distributed Databases/ Information Retrieval Systems	
IT (12)	Design and Analysis of Algorithms	Automata and Compiler Design	Embedded Systems	Data Mining	Distributed Databases	
CSE- AI&ML (66)	Design and Analysis of Algorithms	Computer Networks	Machine Learning	Business Economics &Financial Analysis	Web Programming	

NOTE: The students should occupy their seats 10 min before Commencement of the Examination.

Controller of Examinations

To Controller of Exminations
HODs to the Board out of Exminations
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Examination Branch Notice Board

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